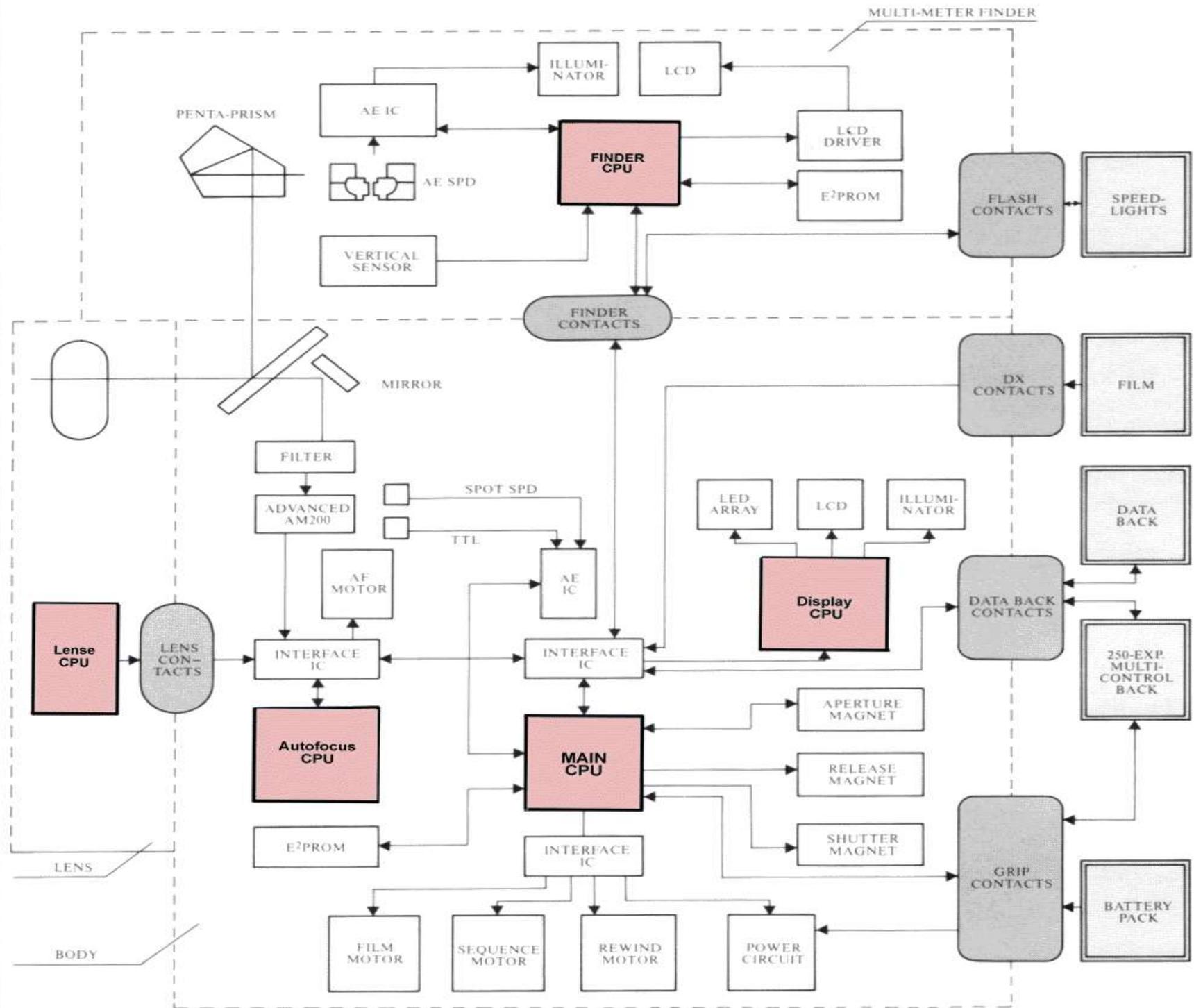


Forth - When and Where





CAMERA OPERATION BLOCK DIAGRAM



Microprocessor requirements

- For downloadable apps
 - 8K bytes ROM
 - 256 bytes RAM
 - Forth interpreter requires 4K bytes
- For development on the target processor
 - 8K bytes EROM
 - 8K bytes RAM
- 16bit or greater addresses preferable

Application space

- Robotics
- Instrumentation
- Diagnostic processor
- Keyboard processors
- Appliances
- Lighting control
- Motor control

Examples

- Finnigan gas chromatograph / mass spectrometer
- Ski lift motor controller
- Windmill energy monitor
- Appliance energy usage monitors
- Theatrical light controller
- Boot ROM

Getting Forth

- <http://www.forth.org/compilers.html>
- Android, PIC18Fxxxx, Linux/i386, Z80, 6809, 8051, 68K, PIC16F8xx, PIC16C84, etc.
- Also implemented in C, C++, Java, Javascript on Unix, Linux, Windows, and Mac
- Metacompilation
12-30 primitives - in assembler