



Speakers' Schedule

SVFIG Meeting
May 28, 2016
Fourth Saturday

Google+ Hangout

SVFIG will participate in a [Google+ Hangout](#) so that everyone can watch this month's meeting in real time as well as view it afterward. Questions and comments can either be posted on Google or emailed to the [SVFIG email list](#). (Contact [Kevin Appert](#) if you would like to be subscribed to the SVFIG email list.)

Future Meeting Dates

SVFIG will meet on the fourth Saturday of each month in 2016 with the following exceptions:
November (Forth Day) & December – Third Saturday.
Any deviations from the [posted meeting schedule](#) will be announced.

Meetup RSVP

If you are planning to attend this month's meeting, please RSVP on [Meetup.com](#). If not, there is no need to offer your regrets.

Notes for First-time Attendees

- 1) Print a Stanford map showing the meeting location, the [Peterson Building](#), so you can find it easily. If you get lost, call Kevin Appert at 650/678-0532
- 2) Browse the [additional meeting information](#).
- 3) Park in one of these suggested locations: along Santa Teresa Street, in the lot near the intersection of [Santa Teresa Street and Lomita Drive](#), or in any "A" or "C" designated areas. There is no need to feed the parking meters on the weekends. Do NOT park in places marked with 24/7 restrictions! Most other spots are okay on Saturdays. **When in doubt, read the signs!**

Links to Other SVFIG Information

- [Meeting Notes Archive](#) includes PowerPoint slides, PDFs, meeting videos, source files, and links
 - [Past Meeting Announcements Archive](#)
 - [Next Meeting Details and a link to the Speakers' Schedule](#)
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Meeting Schedule

10:00 --- DIY eP8080 FPGA Processor Workshop --- CH Ting

This all-day workshop will consist of Ting's presentation, one-on-one discussions, and solitary efforts.

Attendees who would like to participate in this workshop should purchase a [Brevia2 FPGA Development Kit](#) from Lattice Semiconductors (about \$50) and contact CH Ting by email or [Meetup.com](#) or through [Kevin Appert](#) (if you don't have Ting's email address) to obtain an eP8080 software package that synthesizes an Intel 8080 CPU. Workshop activities will include emulation experiments and discussions of old and new CPU architectures.

Alternatively you may participate by running the free Diamond IDE simulator or just watching and listening.

11:30 --- Lunch

We will enjoy lunch at the [Treehouse](#)

16:00 --- Clean Up and Adjourn

While we try to adhere to this schedule, sometimes a presentation unexpectedly runs long or the presentation start times need to be reassigned. If you're desperate to see a particular presentation at a specific time, please alert us and we'll do our best to accommodate your needs.

This schedule may be reformatted or line-justified, but please transmit it verbatim or preferably provide a link to it.

No Newsgroup posts or other media distribution please!

This schedule document is produced by [Kevin Appert](#), the SVFIG Program Chair and is distinct from the information posted on the SVFIG and FIG websites which are authored by the SVFIG Webmaster, Dave Jaffe. Your comments, corrections, and suggestions are always welcome.

