



# Evolution of Forth Chips

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Chen-Hanson Ting

SVFIG

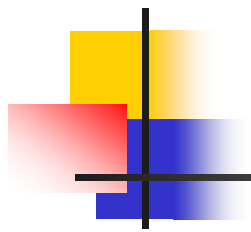
November 19, 2016



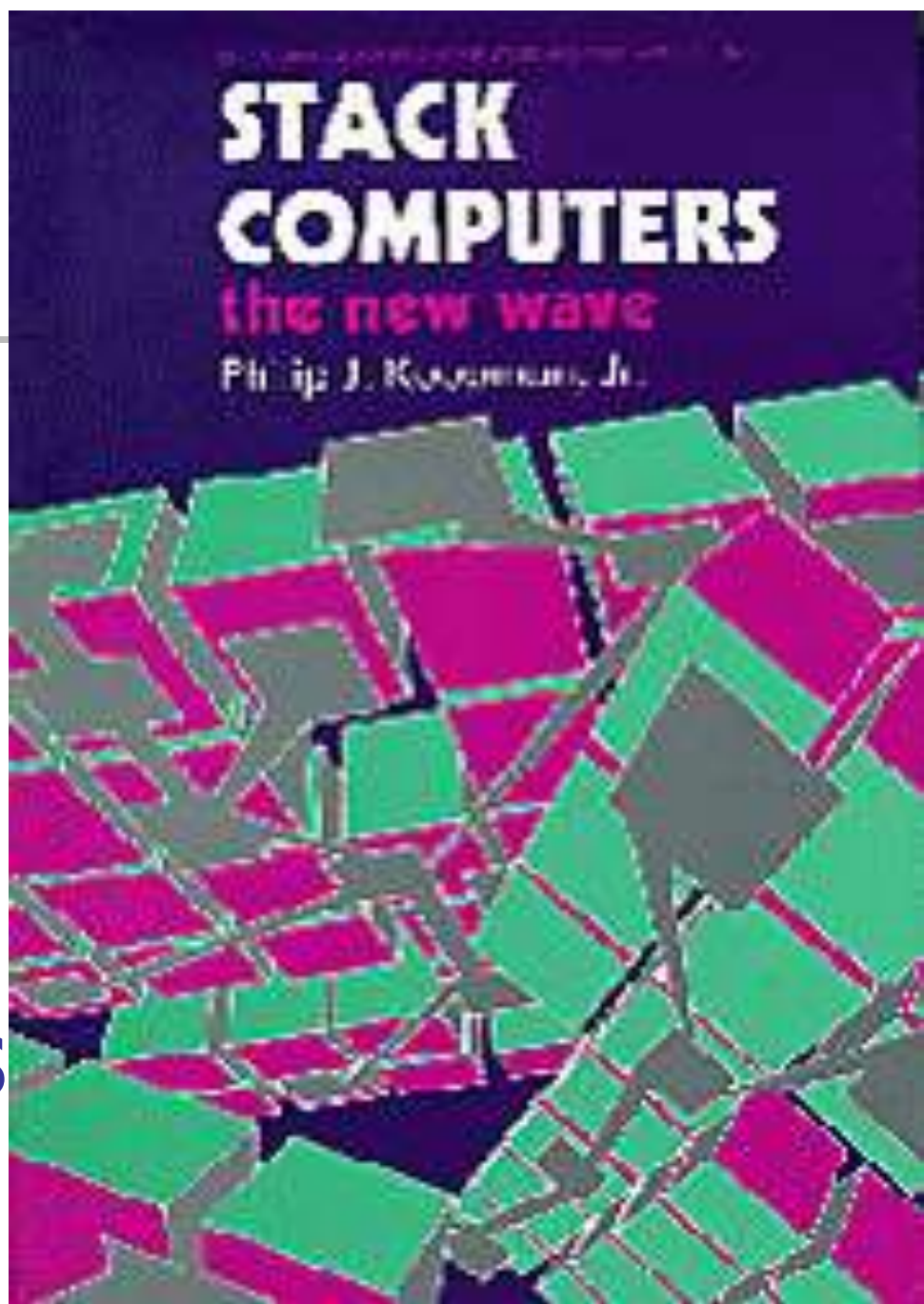
# Forth Chips

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1985	WISC CPU/16, CPU/32
1985	Novix NC4016
1986	John Hopkins FRISC-3/SC32
1988	Harris RTX 2000/2001
1988	ShBoom
1995	MuP21
2000	P8/P16/P24/eP32/eP16
2009	F18A/GA40/GA144



**Phil Koopman**  
Stack Computers  
The New Wave

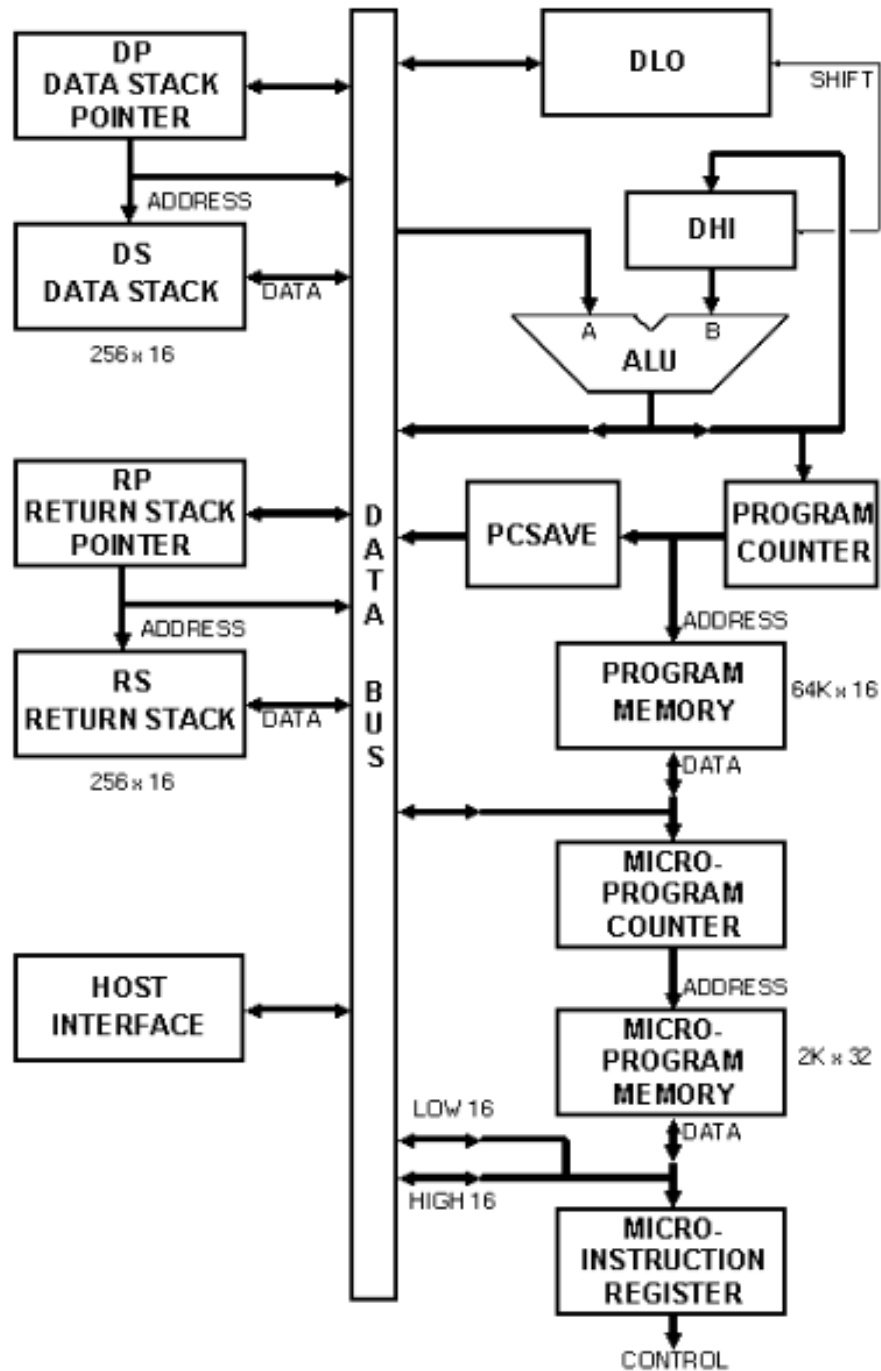




**Phil  
Koopman  
CMU**



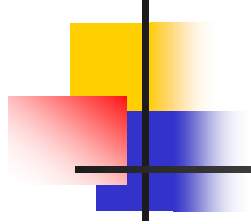
# WISC CPU/16 (1985)



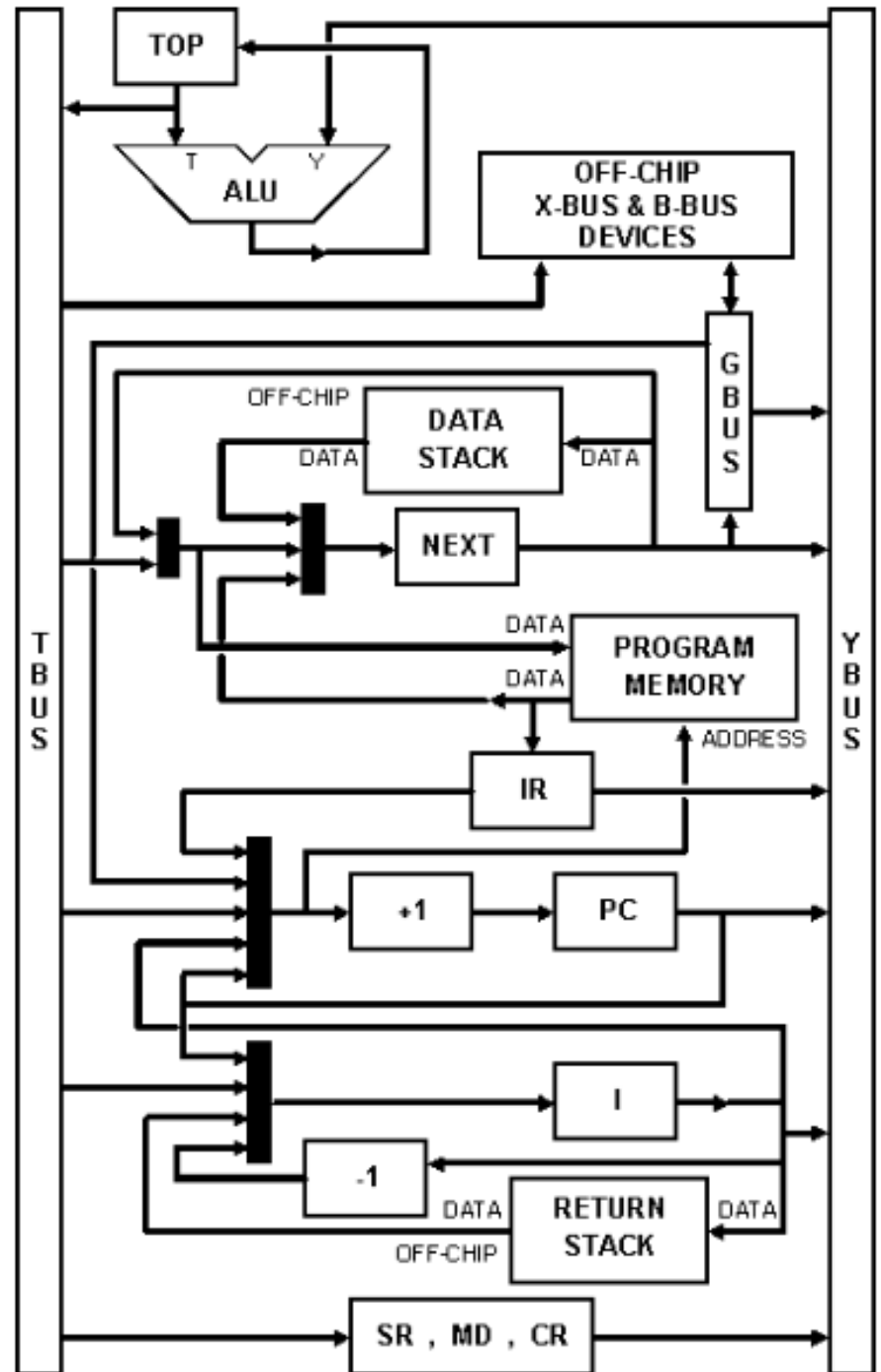


**Glen  
Hayden**





# NOVIX NC4016 (1985)



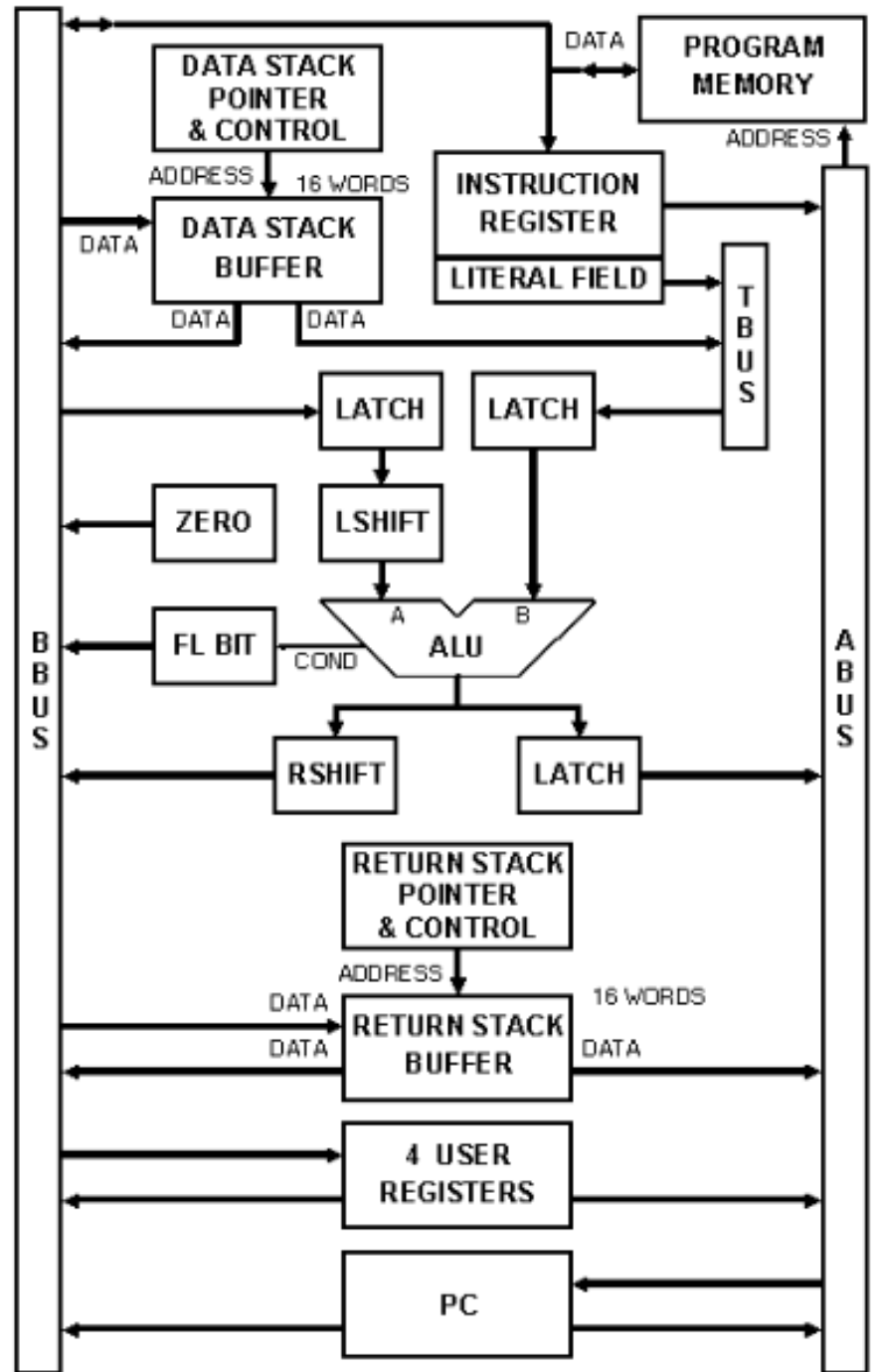
# NOVIX 4016





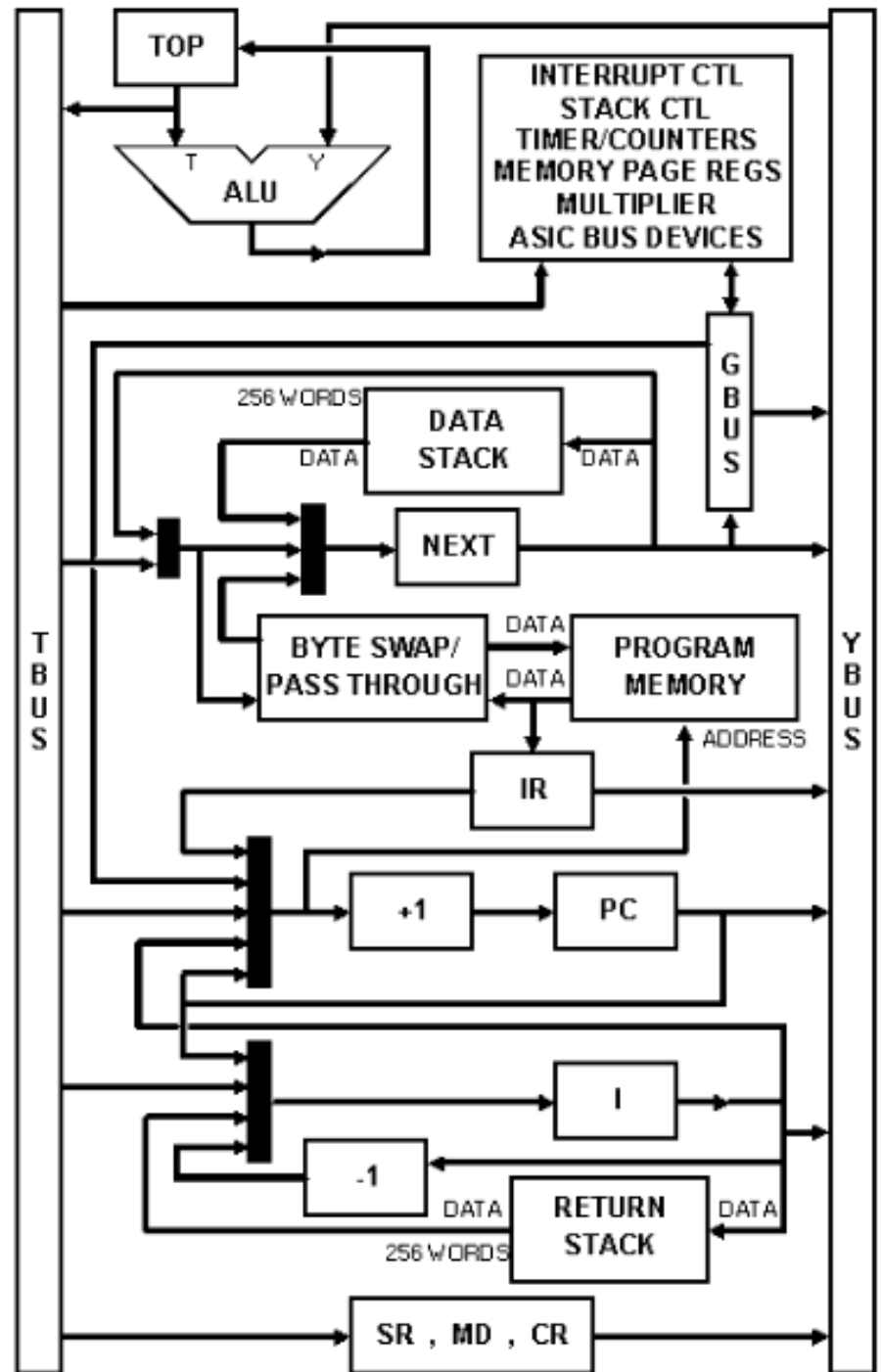


# John Hopkins FRISC3/SC32 (1986)





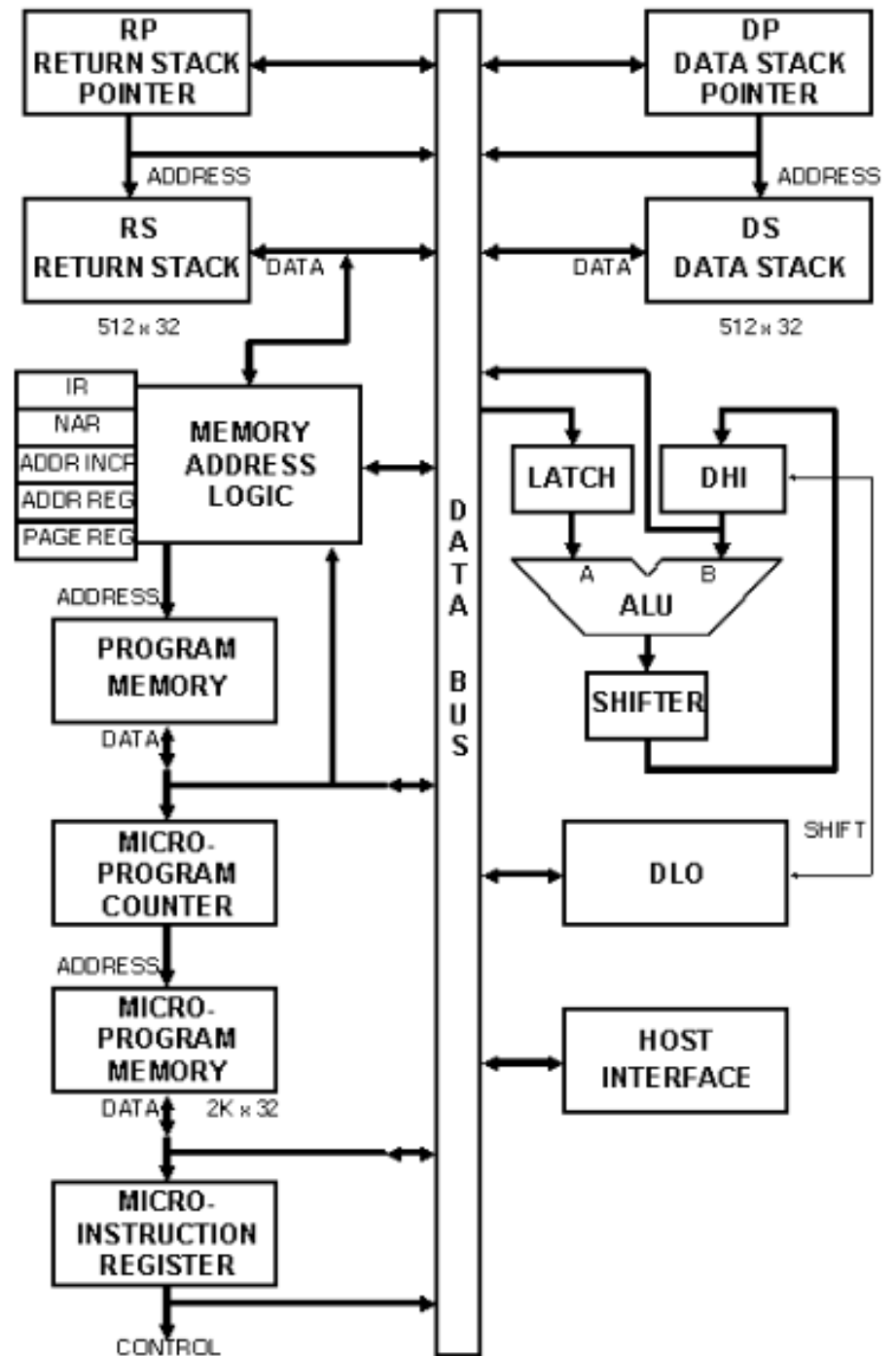
# Harris RTX 2000/1 (1988)



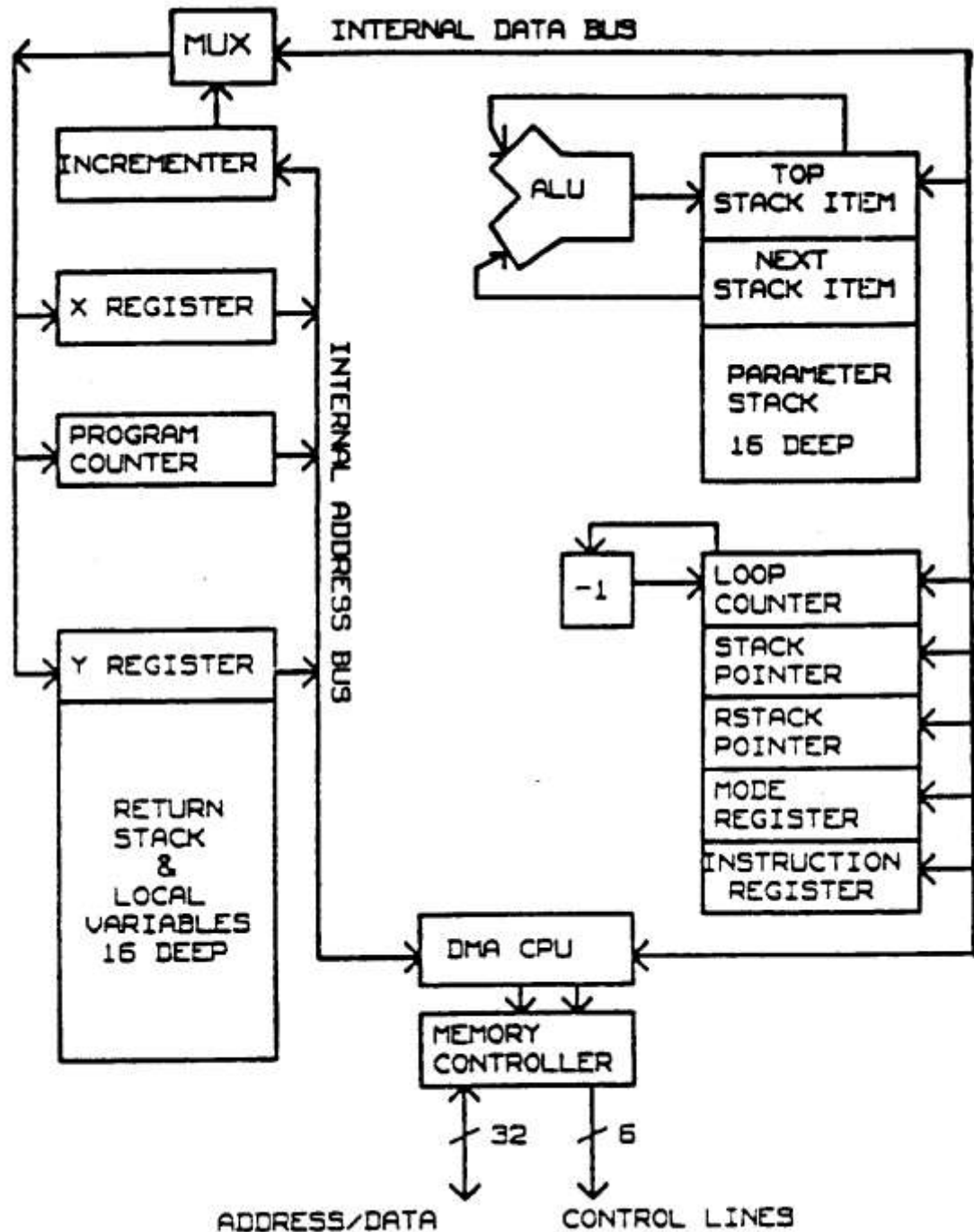
# RTX2001

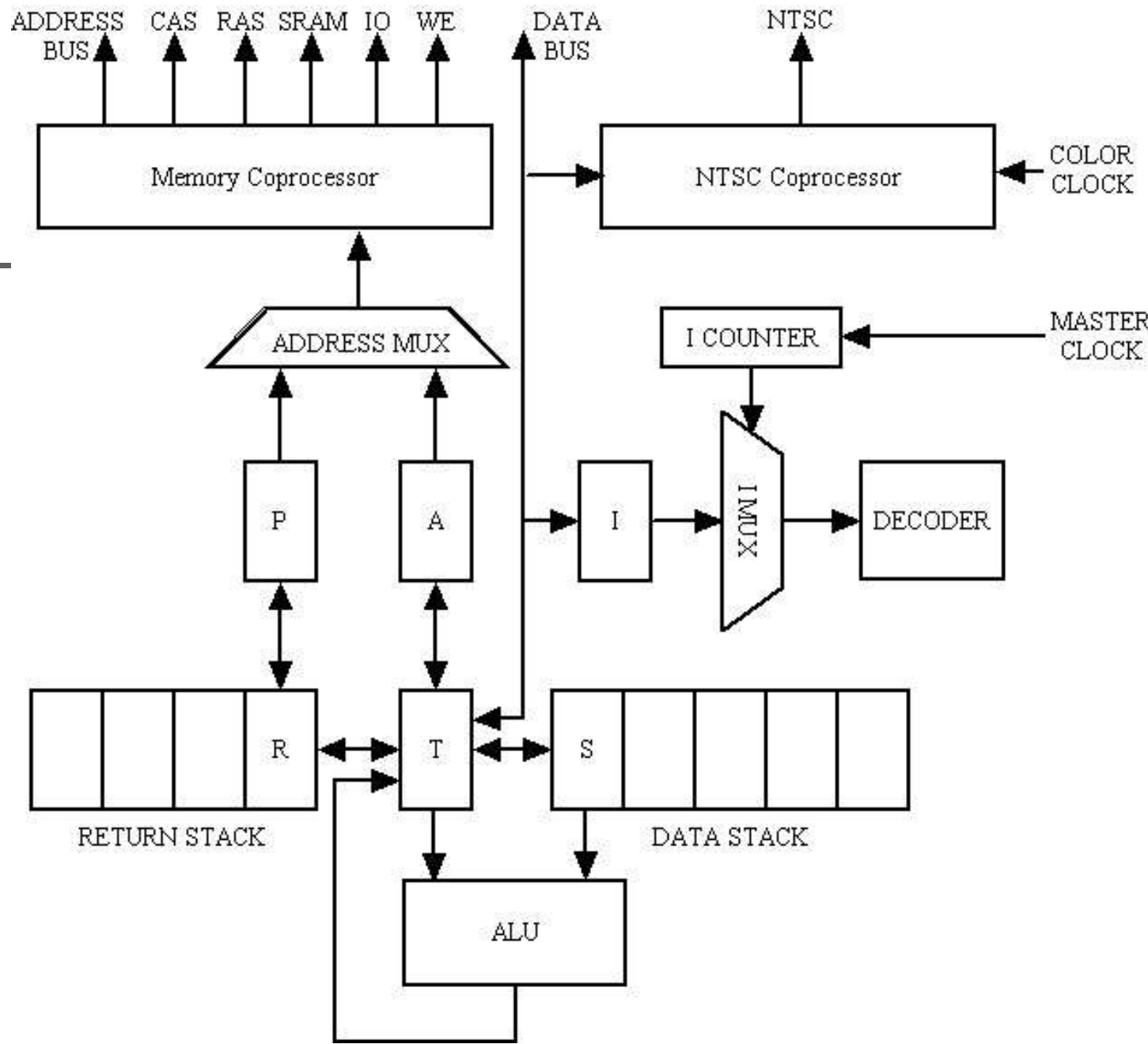
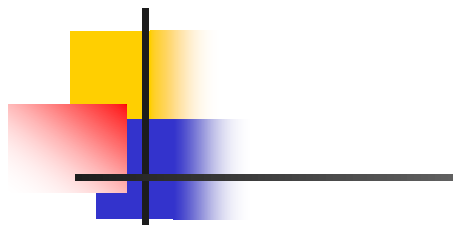


# Harris RTX 32P (1989)



# ShBoom (1988)

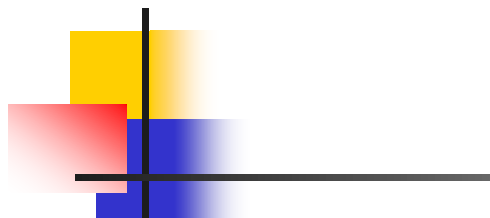




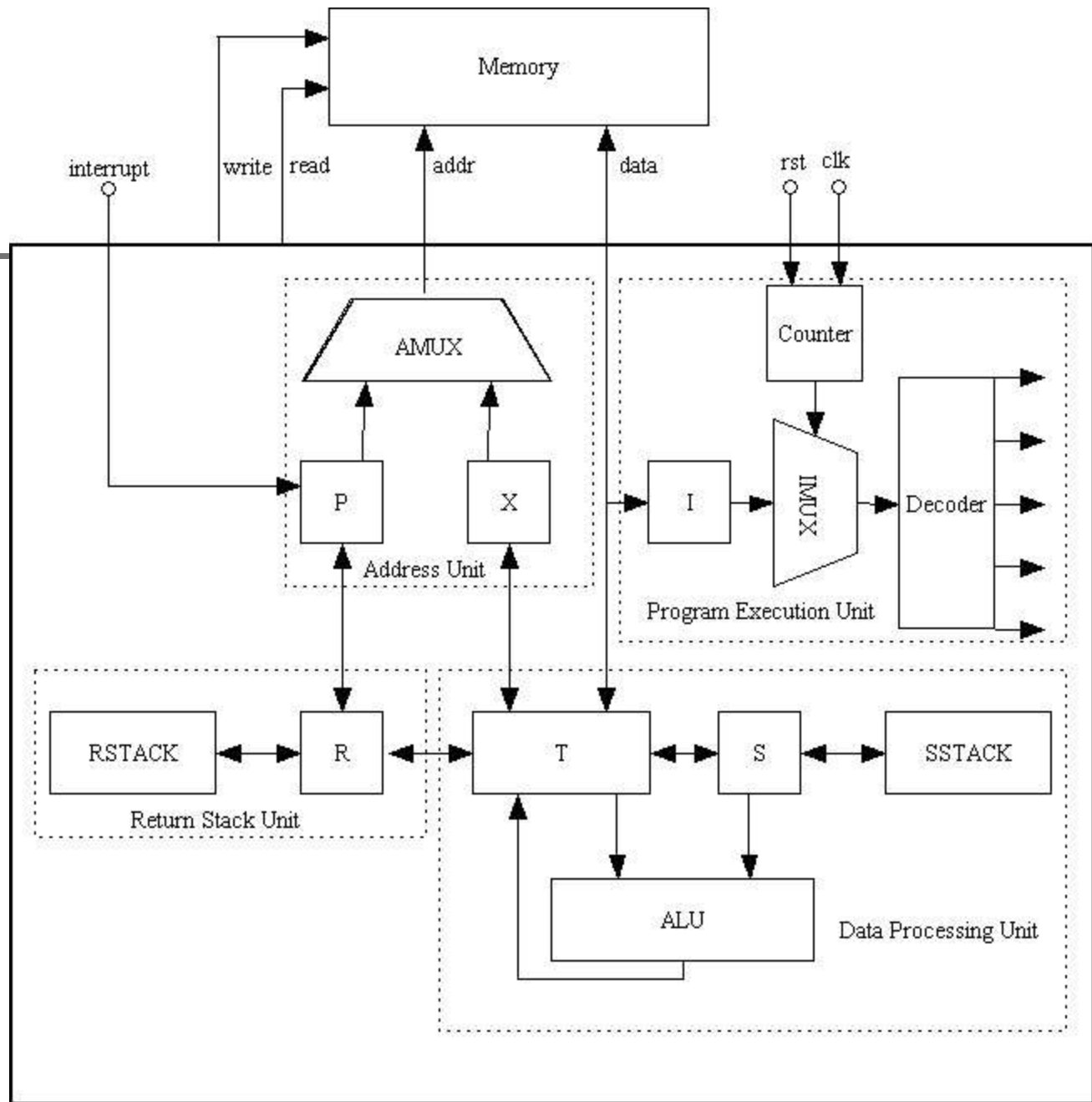
# MuP21 (1995)

# MuP21



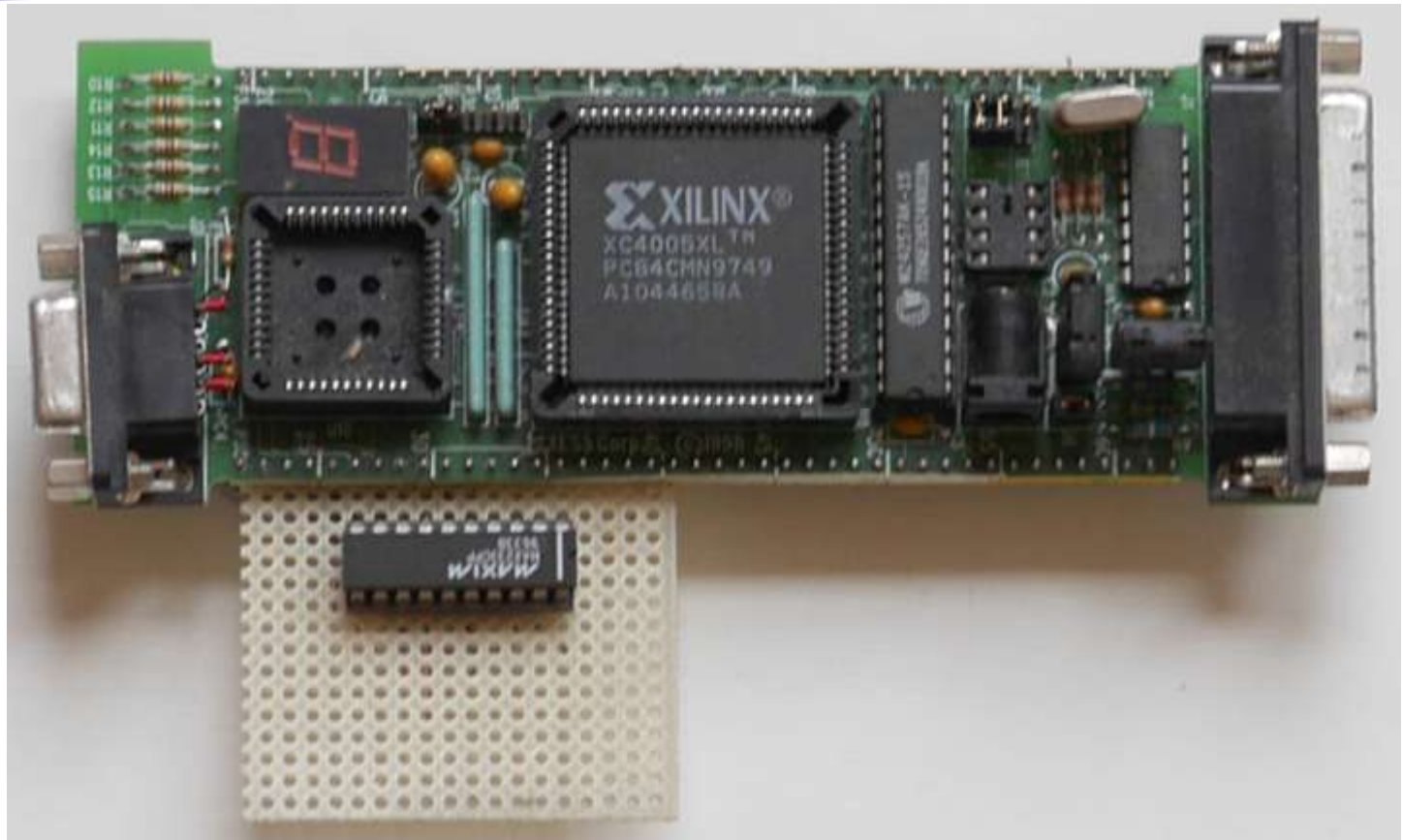


**P8**  
**P16**  
**P24**  
**eP32**  
**eP16**  
**(2000)**

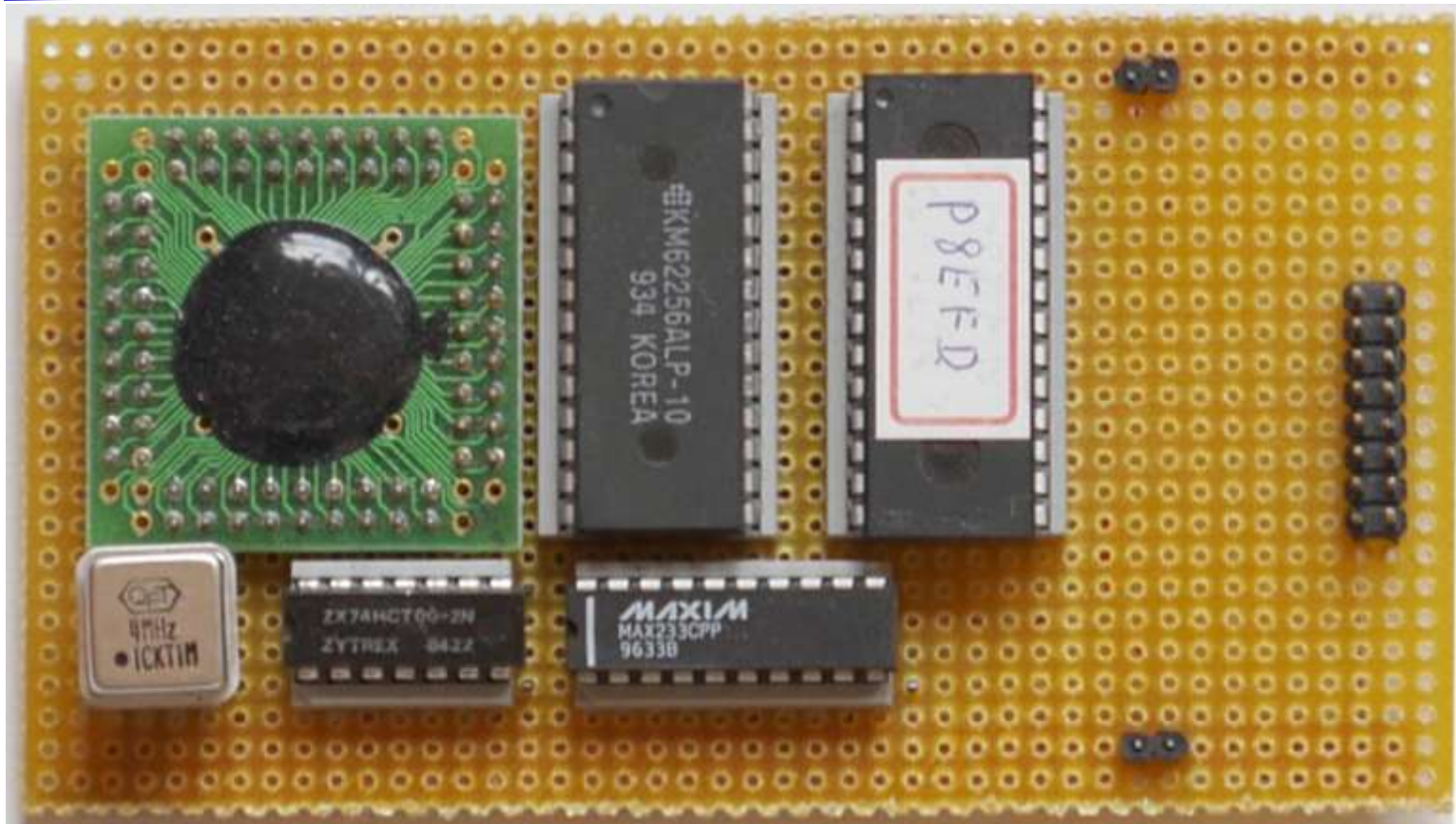




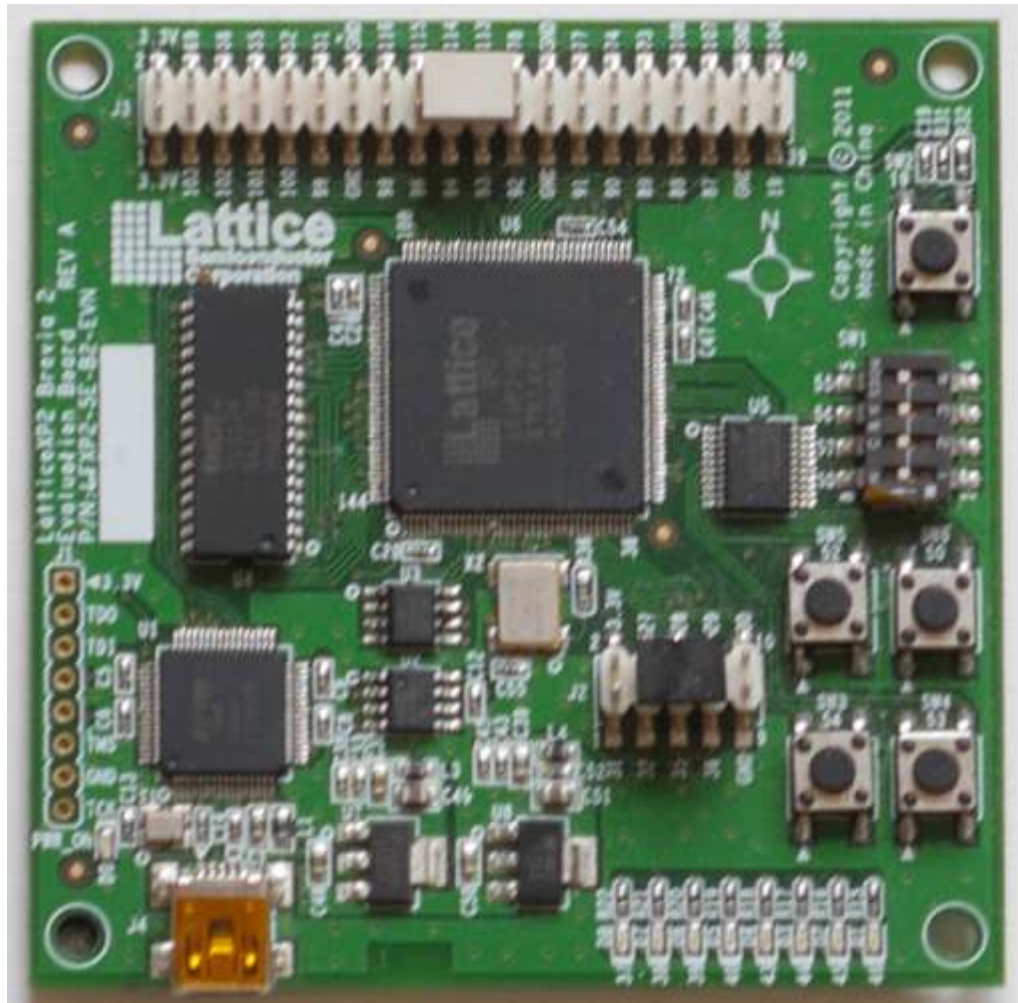
# P8



# P16



# eP32



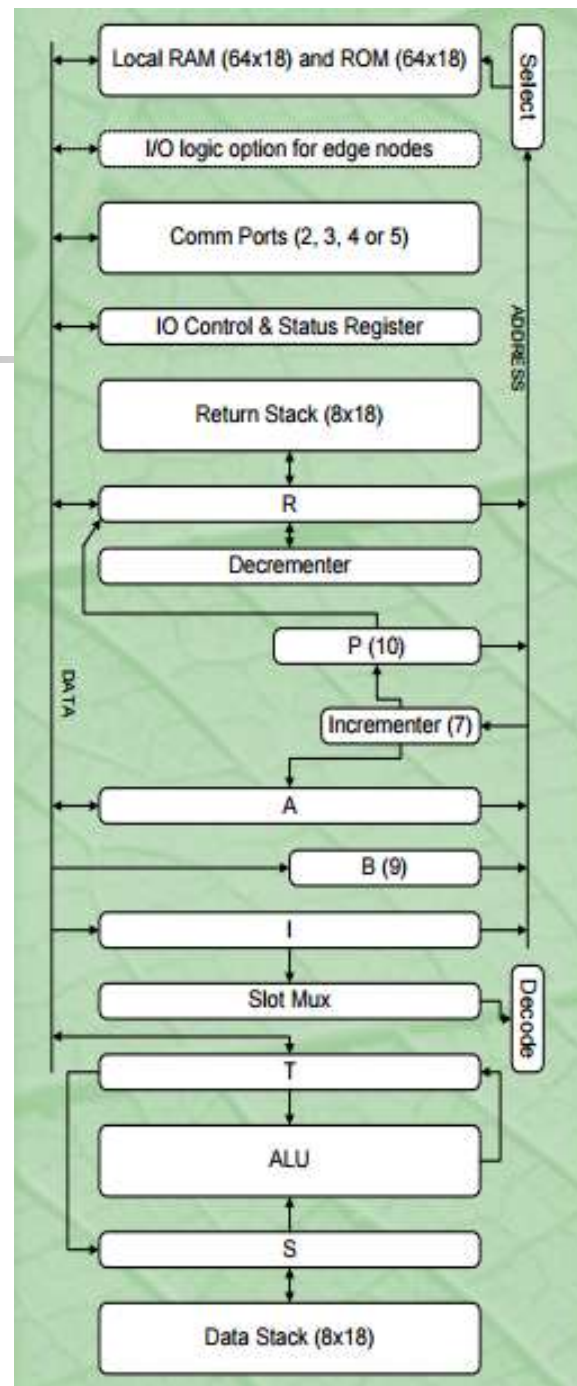


# Green Arrays

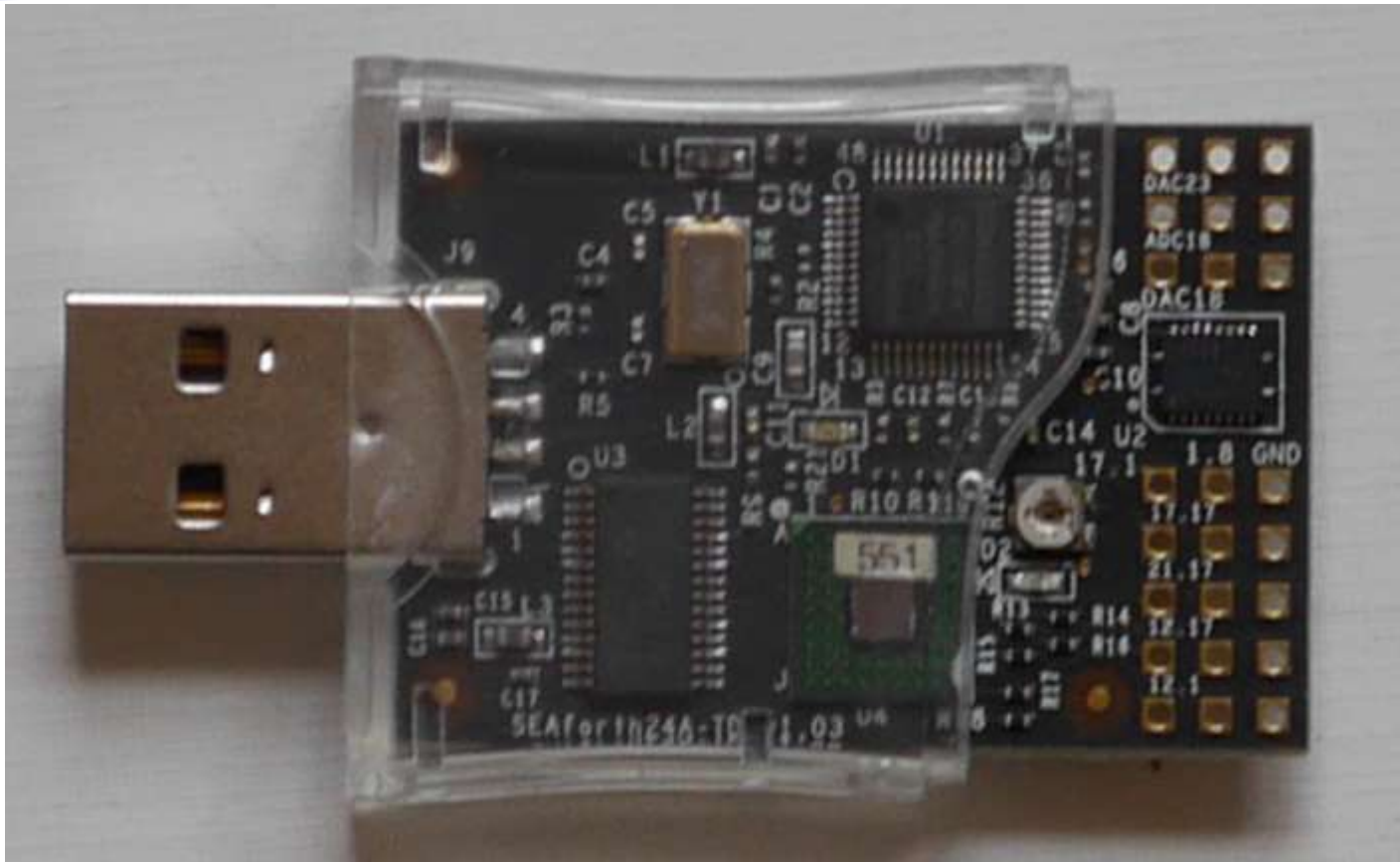
## F18A

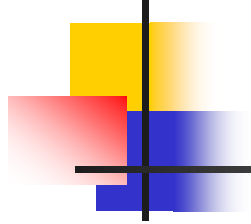
### GA40 (2009)

### GA144 (2012)



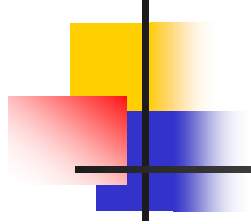
# GA40





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**Questions?**



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**Thank you.**



# ESP8266

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- It looks that ESP8266 12E will replace Arduino Uno, with its WIFI capability, 32-bit processor, and large memories.
- We need a WIFI ready eForth implementation to participate in the new revolution





# espForth

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- I ported my eForth written in C to WiFiBoy kit successfully.
- I have to communicate with it through TCP/IP. Help will be appreciated.

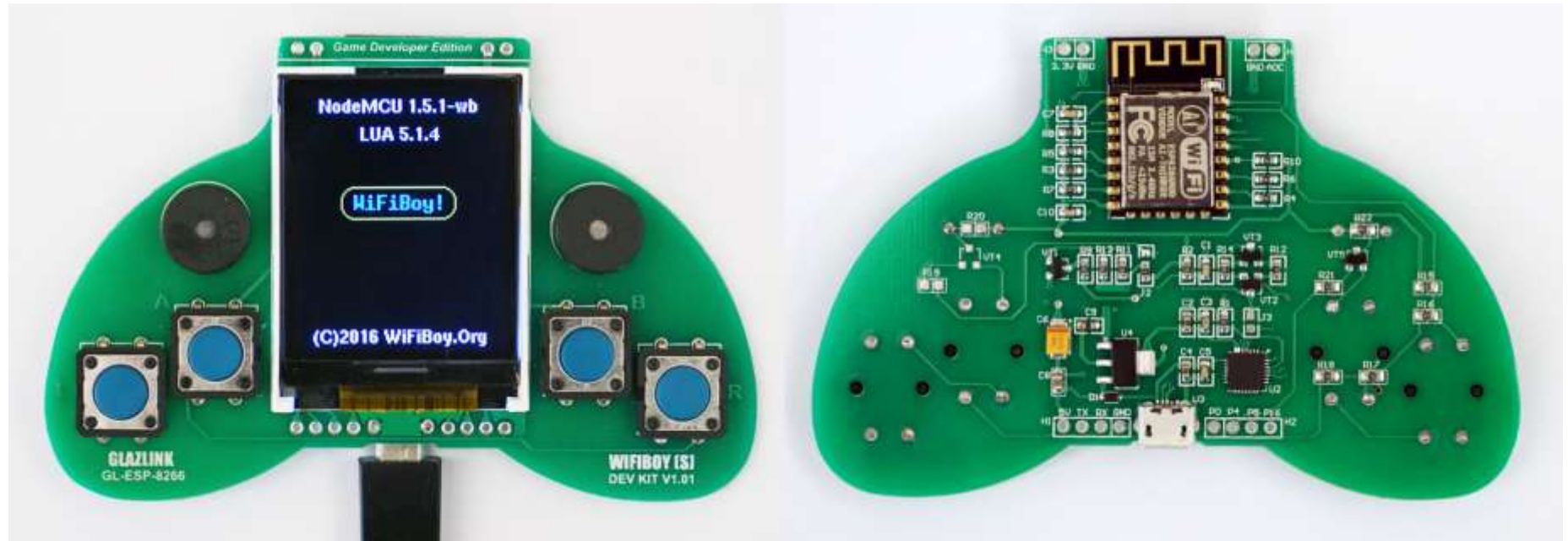


# ESP8266

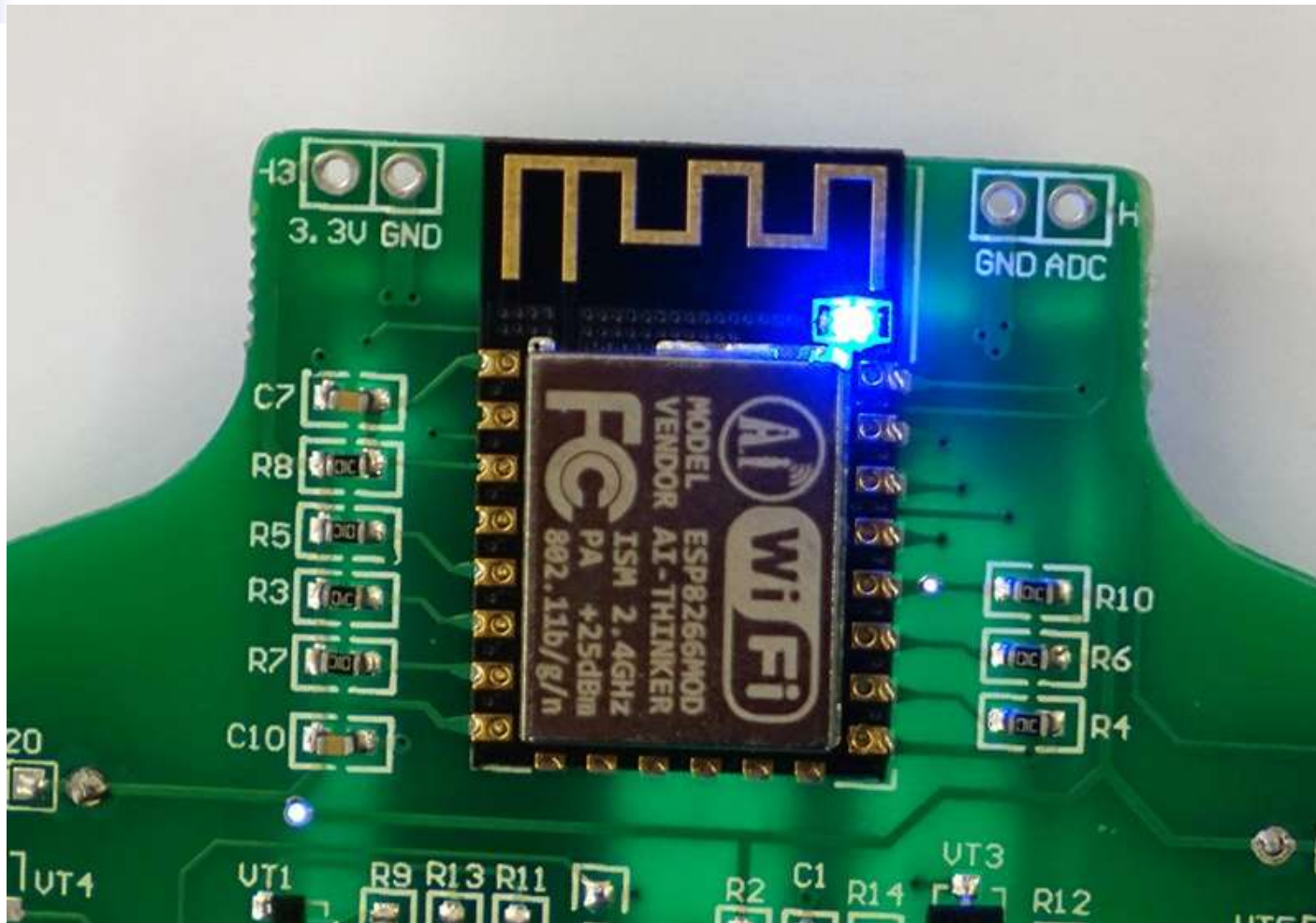
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- 32-bit Xtensa LX106 at 80 MHz
- 64 KB program RAM, 96 KB of data RAM
- IEEE 802.11 b/g/n Wi-Fi
- GPIO, SPI, I<sup>2</sup>C, UART, ADC

# WiFiBoy



# ESP8266



# ESP8266

