

Title: MyForth, a Minimalist 8-Bit Forth for 8051 Chips

Presenters: Charley Shattuck and Bob Nash

Abstract:

Written by Charley Shattuck, MyForth is an 8-bit minimalist Forth for 8051 processors. It uses many of the principles and features of ColorForth, including the use of color to highlight source code functionality, efficient tail recursion, two vocabularies and the elimination of the “else” construct in favor of code exits.

MyForth is primarily a tethered Forth-like “macro assembler” that can be used in both Linux and Windows environments.

Charley Shattuck, an accomplished practitioner of the neo-minimalist school of Forth philosophy, will describe the evolution, philosophy and implementation of MyForth.

Bob Nash, a nascent neo-minimalist and the first Windows user of MyForth, has written a manual for it. He will describe the general features of MyForth and provide an overview of the manual.

Both Charley and Bob will describe their experiences using MyForth to program a commercial application with a 100 MHz 8051 processor.

Time and interest permitting, code samples will be presented for several small applications including an LCD driver and a standalone (chip resident) interpreter.

Timing:

This presentation can be presented in 30 to 45 minutes, depending on available time and interest.

Materials and Exhibits:

Copies of the presentation will be available; it will include a URL where a copy of MyForth and the manual can be downloaded.

A printed version of the manual and selected source code will also be available for examination.

The presenters will bring a small 8051 Target system connected to a laptop for application demonstrations during breaks.