



## Speakers' Schedule

### SVFIG Meeting January Meeting

January 28, 2017  
Fourth Saturday

#### **Australian Forth Symposium - 1988**

Andreas will have copies of this proceedings document for sale at the meeting for \$10. The document's cover page, table of contents, and first page of the articles can be viewed [here](#).

#### **Meeting Video**

Links to all SVFIG videos, including this meeting's live stream can be found [here](#).

#### **Future Meeting Dates**

SVFIG will meet on the fourth Saturday of each month in 2017 with the following exceptions:

- November 18<sup>th</sup> - Third Saturday
- December 16<sup>th</sup> - Third Saturday

Any deviations from the [posted meeting schedule](#) will be announced.

## Meetup RSVP

If you are planning to attend this month's meeting, please RSVP on [Meetup.com](https://www.meetup.com). If not, there is no need to offer your regrets.

## Notes for First-time Attendees

- 1) Print a Stanford map showing the meeting location, the [Peterson Building](#), so you can find it easily. If you get lost, call Kevin Appert at 650/678-0532
- 2) Browse the [additional meeting information](#).
- 3) Park in one of these suggested locations: along Santa Teresa Street, in the lot near the intersection of [Santa Teresa Street and Lomita Drive](#), or in any "A" or "C" designated areas. There is no need to feed the parking meters on the weekends. Do NOT park in places marked with 24/7 restrictions! Most other spots are okay on Saturdays. **When in doubt, read the signs!**

## Links to Other SVFIG Information

- [Meeting Notes Archive](#) includes PowerPoint slides, PDFs, meeting videos, source files, and links
  - [Past Meeting Announcements Archive](#)
  - [Next Meeting Details and a link to the Speakers' Schedule](#)
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# Meeting Schedule

### 10:30 --- Toward High IPC (Inter-process Communications) Stack Processors - Matthias Schirm

"In this talk I will give a short introduction to the evolution of Niagara from its beginning extending the Retroforth VM (Virtual Machine), over a holistic VLIW (Very Long Instruction Word) - to a dual stack, zero operand MISD (Multiple Instruction, Single Data) ISA (Instruction Set Architecture) which allows the implementation of both efficient software environments as well as high performance processor arrays with low resource requirements. This includes a short outlook of the current state of a future processor implementation whose most prominent feature will be the combined execution of whole instruction sequences within a single clock cycle."

### 11:30 --- Lunch

We will enjoy lunch at the [Treehouse](#)

### 13:00 --- IoT for Fun at Maker Faire - CH Ting

Ting has applied for a booth at the upcoming Maker Faire with this description: "Silicon Valley Forth Interest Group (SVFIG) is a non-profit organization dedicated to promoting the Forth programming language since 1978. The recently available low-cost WiFi-ready chip ESP8266 makes it possible for makers to build and program their own IoT (Internet of Things) projects. We propose to run tutorials and employ open experiment benches to teach makers how to build their own IoT projects, and give them hands-on experience to program and test devices that are remotely connected over the Internet."

**13:20 --- Forth Haiku - Brad Nelson**

Brad will flip through a few of the latest haiku.

**13:45 --- CES 2017 Focus on Cars - Dennis Ruffer**

Dennis will tell us about the mostly-automotive-related sightings and presentations that caught his attention at CES.

**14:10 --- Break**

**14:20 --- Particle Systems in Forth - Brad Nelson**

"Particle systems are a simple but powerful simulation approach, a natural fit for Forth. We'll explore techniques for expressing them cleanly in Forth, and apply them to simulate interesting dynamic behavior."

**15:10 --- Selected Lessons Learned Building KCP53000 - Samuel A. Falvo II**

"While working on the Kestrel-3 and its CPU (KCP53000), I encountered a number of problems, and there are problems that remain to this day. These problems could have been avoided (or at least reduced in scope) with some hindsight, and I would like to discuss a selection of them in this talk."

**16:00 --- Clean Up and Adjourn**

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While we try to adhere to this meeting schedule, sometimes a presentation unexpectedly runs long or the presentation starting times need to be reassigned. If you're desperate to see a particular presentation at a specific time, please alert us and we'll do our best to accommodate your needs.

This schedule may be reformatted or line-justified, but please transmit it verbatim or preferably provide a link to it.

No Newsgroup posts or other media distribution please! Contact Kevin if you desire to advertise SVFIG meetings.

The [Speakers' Schedule posted on GitHub](#) was produced by [Kevin Appert](#), the SVFIG Program Chair and is distinct from the information posted on the SVFIG and FIG websites which are authored by the SVFIG Webmaster, Dave Jaffe. Your comments, corrections, and suggestions are always welcome.

