

Neural Network Tools (II)

## Universal Control Structures

Ada Multiprocessor Real-Time Kernel

## FAST Forth Native-Language Embedded Computers



Harris RTX 2000 ${ }^{\text {tm }}$ 16-bit Forth Chip
$\cdot 8$ or 10 MHz operation and 15 MIPS speed.
-1-cycle $16 \times 16=32$-bit multiply.
-1-cycle 14-prioritized interrupts.
-two 256 -word stack memories.

- 8 -channel I/O bus \& 3 timer/counters.

SC/FOX PCS (Parallel Coprocessor System) $\cdot$ RTX 2000 industrial PGA CPU; $8 \& 10 \mathrm{MHz}$. -System speed options: 8 or 10 MHz .

- 32 KB to 1 MB 0 -wait-state static RAM.
-Full-length PC/XT/AT plug-in (6-layer) board.
SC/FOX VME SBC (Single Board Computer)
-RTX 2000 industrial PGA CPU; 8, $10,12 \mathrm{MHz}$.
- Bus Master, System Controller, or Bus Slave.
- Up to 640 KB 0-wait-state static RAM.
$\cdot 233 \mathrm{~mm} \times 160 \mathrm{~mm} 6 \mathrm{U}$ size (6-layer) board.


## SC/FOX CUB (Single Board Computer)

 - RTX 2000 PLCC or 2001A PLCC chip. -System speed options: 8,10 , or 12 MHz . - 32 KB to 256 KB 0 -wait-state SRAM.$\cdot 100 \mathrm{~mm}$ by 100 mm size (4-layer) board.

SC32 ${ }^{\text {tm }}$ 32-bit Forth Microprocessor
-8 or 10 MHz operation and 15 MIPS speed.
-1-clock cycle instruction execution.
-Contiguous 16 GB data and 2 GB code space.

- Stack depths limited only by available memory.
- Bus request/bus grant lines with on-chip tristate.

SC/FOX SBC32 (Single Board Computer32)
-32-bit SC32 industrial grade Forth PGA CPU.
-System speed options: 8 or 10 MHz .

- 32 KB to 512 KB 0 -wait-state static RAM.
$\cdot 100 \mathrm{~mm}$ by 160 mm Eurocard size (4-layer) board.
SC/FOX PCS32 (Parallel Coprocessor System32)
-32-bit SC32 industrial grade Forth PGA CPU.
-System speed options: 8 or 10 MHz .
- 64 KB to 1 MB 0 -wait-state static RAM.
-Full-length PC/XT/AT plug-in (6-layer) board.
SC/FOX SBC (Single Board Computer)
-RTX 2000 industrial grade PGA CPU.
-System speed options: 8,10 , or 12 MHz .
- 32 KB to 512 KB 0 -wait-state static RAM.
$\cdot 100 \mathrm{~mm}$ by 160 mm Eurocard size (4-layer) board.

For additional product information and OEM pricing, please contact us at: SILICON COMPOSERS INC 208 California Avenue, Palo Alto, CA 94306 (415) 322-8763

## Features

## f <br> 9 <br> Universal Control Structures Kevin Haddock

Forth's execution control can be difficult to learn, and control structures can be an area of concern even for the enlightened. Forth has all the advantages of an interpreted language like BASIC but, again, the control structures get in the way.

One thing that creates difficulty with Forth, compared to C , is eliminating unnecessary tests: If any one of a series of ANDed conditions is not true, there is no need to waste time extracting and evaluating subsequent conditions. This article attempts to provide an integrated solution that is uniform, clean, simple, and Forth-like.

## Neural Network Words Tim Hendtlass

Neural networks are good at certain tasks the human brain is good at, like pattern recognition, often outperforming traditional computing techniques. A neural network is an interconnection of special processing elements that is based on the brain; each element has inputs and outputs, and is termed a "neurone." Neurones are arranged in logical layers, and a collection of such layers comprises a network.

The code in this article allows construction of a class of simulated networks, and their training by the widely used technique called "back propagation." It is portable to many environments, and other training techniques may be incorporated. (The second of two articles.)

## Ada Multiprocessor Real-Time Kernel Hoyt A. Stearns, Jr.

A fully pre-emptive, priority driven, multiprocessing real-time kernel is required in certain applications, and the Ada rendezvous method is simple, straightforward, and elegant. Ada multitasking will work on multiple processors or on a single processor. A task can be local to a particular processor, or can be distributed.

The listing is the kernel for a system with three processors, but the kernel works fine on a singleprocessor system.

## Departments

| 4 | Editorial <br> Letters | ... Object-oriented Forth, China Forth exams, keeping energized. <br> ... Black-belt exhaustion, mastering metacompilation, Forth on the Macintosh, U.K. contest winners (probability of matching birthdays, one-screen full-screen editor), PDE erratum, answer to a dream. |
| :---: | :---: | :---: |
| 22 | Advertisers Index |  |
| 23 | President's Letter | ..Financial report, board of directors, publicity. |
| 38 | Best of GEnie | ...Jef Raskin, Alan Furman, Bill Muench, Guy Kelly, Dean Sanderson, Roy Martens \& Glen Haydon, Charles Johnsen. |
| 40-43 | reSource Listings | . FIG, ANS Forth, classes, on-line connections, FIG chapters |

Alast reminder: $F D$ has announced a call for papers about object-oriented programming. The closing date is September 16 for prize consideration-see our ad on page 37 for prize information, and refer to the editorial in issue XIII/ 1 for other details that may be helpful when preparing your paper. (Papers received after the deadline will still be evaluated for publication.)

## So, Why Don't You Write?

$F D$ is also looking for many new articles from its readers to fill the editorial coffers. This is, after all, a reader-written publicationthis means you! We are looking for a wide range of topics, as eclectic as our readership. Chances are, if you find something interesting, clever, or challenging, many others will, too. If you aren't sure, just drop me a note that outlines the idea, and I will try to give meaningful feedback to you. (And check out our "Author Recognition Program," details of which are reprinted on page 35.)

If you aren't sure if your idea is really a new one, or can't remember whose original work it derives from-to say nothing of the times when you wonder whether the blank wall you've been staring at has been scaled
already by some other Forth explorer-there are some FIG reference tools you might want to get. Look on the FIG Mail Order Form for the "We're Sure You Wanted to Know..." series. The Forth Bibliography from Rochester also provides Forth reference information from outside the FIG domain, but hasn't been updated since January 1987. Get a copy anyway for an inspirational look at how much Forth material really has been circulating through the years.

## Chinese <br> National Exam of Forth Programmers

China has officially announced a national examination to be held in September. Great importance is often attached to the results of such examinations. As Dr. Ting relates, the Chinese love examinations, a tradition of 2000 years. For your interest, following is a translation of part of the announcement:

To accelerate the application of Forth in various technical fields, to enlarge the ranks of Forth programmers, to raise the expertise in the Forth language, and to satisfy the increasing need for Forth users, the Chinese Scientific Instrumentation Society, the Chinese Software Association, and the Chinese Forth Interest Group will
jointly sponsor a National Examination of Forth Programmers on September 8, 1991 in various provinces and cities across China. The sponsors will grant certificates to programmers passing this examination. They will also select the best ten Forth programmers and thirty excellent Forth programmers, based on the examination, to receive special certificates and awards, to be recommended to participate in Forth projects, and to carry out advanced studies abroad.

## Subjects of Examination

1. Computer Fundamentals (20\%)
Number systems and conversions
Internal representations of numbers
Major components in a computer and their relationships
Instructions and instruction sets
Characteristics and functions of registers
Characteristics and properties of I/O devices
Data structures (stacks and lists)
Operating system fundamentals
Software engineering fundamentals
2. Elementary Knowledge of
(Continued on page 34.)

## Forth Dimensions

Volume XIII, Number 3
September 1991 October

Published by the<br>Forth Interest Group<br>Editor<br>Marlin Ouverson<br>Circulation/Order Desk<br>Anna Brereton

Forth Dimensions welcomes editorial material, letters to the editor, and comments from its readers. No responsibility is assumed for accuracy of submissions.

Subscription to Forth Dimensions is included with membership in the Forth Interest Group at $\$ 40$ per year (\$52 overseas air). For membership, change of address, and to submit items for publication, the address is: Forth Interest Group, P.O. Box 8231, San Jose, California 95155. Administrative offices: 408-277-0668. Fax: 408-286-8988. Advertising sales: 805-946-2272.

Copyright © 1991 by Forth Interest Group, Inc. The material contained in this periodical (but not the code) is copyrighted by the individual authors of the articles and by Forth Interest Group, Inc., respectively. Any reproduction or use of this periodical as it is compiled or the articles, except reproductions for non-commercial purposes, without the written permission of Forth Interest Group, Inc. is a violation of the Copyright Laws. Any code bearing a copyright notice, however, can be used only with permission of the copyright holder.

## The Forth Interest Group

The Forth Interest Group is the association of programmers, managers, and engineers who create practical, Forth-based solutions to real-world needs. Many research hardware and software designs that will advance the general state of the art. FIG provides a climate of intellectual exchange and benefits intended to assist each of its members. Publications, conferences, seminars, telecommunications, and area chapter meetings are among its activities.
${ }^{\text {"Forth Dimensions (ISSN 0884- }}$ 0822) is published bimonthly for $\$ 40 / 46 / 52$ per year by the Forth Interest Group, 1330 S. Bascom Ave., Suite D, San Jose, CA 95128. Second-class postage paid at San Jose, CA. POSTMASTER: Send address changes to Forth Dimensions, P.O. Box 8231, San Jose, CA 95155."

# ムetters 

Letters to the Editor-and to your fellow readers-are always welcome. Respond to articles, describe your latest projects, ask for input, advise the Forth community, or simply share a recent insight. Code is also welcome, but is optional. Letters may be edited for clarity and length. We want to hear from you!

## Black-Belt Exhaustion

 \& Lean, Mean FIGDear Phil Koopman, Jr.:
I was sorry to hear that you decided to drop your FIG membership /seeFD XIII/ 2]. You are one of the very visible and highly acclaimed Forth programmers. It is a great loss to FIG, not having you continue your membership.

You correctly pointed out that FIG is having financial troubles. The main problem I see is that we have been losing members since 1984, when the membership peaked at about 4,500 . The current membership of about 2000 cannot support the operation of FIG using a professional support organization like ADC.

The membership loss has puzzled me all these years. There are more computers, more users, but why is FIG getting leaner and leaner? We have tried many different things to attract more new members, as well as to keep the old ones, but nothing seemed to work. There were substantial improvements on the appearance and substance in $F D$. We put lots of effort into getting the GEnie Forth RoundTable
going. We had gone out of our way to promote Forth to the outside, but to no avail. Harris put in great amounts of money and energy to promote Forth, and even assembled a very high-powered team of Forth blackbelts, including yourself, to develop the turbo-charged Forth engines. Why did Harris abandon its efforts? I really hope that somebody will tell me what's going on and what we have to do to change the public perception of the inadequacies of Forth and FIG.

FIG has always been an organization of volunteers. The founders of FIG expended great energy to craft figForth, which propelled FIG into a viable association. Other members like Mike Perry, Henry Laxen, Bob Smith, and Tom Zimmer improved on fig-Forth and contributed F83 and F-PC, which have kept FIG going. I am fully aware of your contributions to Forth, in MVP-FORTH and the WISC technology. However, may I ask what you have contributed to FIG as a member? You complained that there is not a FIG chapter in your area. Every chapter is started
by somebody; have you considered starting one yourself? You certainly have the capability, the energy, and the charisma to lead a large group and to teach lots of newcomers. Maybe the reason that FIG is shrinking is due to the exhaustion of leaders like you?

I had not served on the FIG Board of Directors until last year. However, I have regularly attended the business meetings and watched FIG's operations forten years. One thing I can say for the Board is that every dollar is accounted for andthe records are kept accurately. All expenses are openly discussed and must be approved at these meetings. The books are available at the FIG office for members to inspect. Forth Dimensions will also be publishing financial reports. [See the President's Letter in this issue. -Ed.]

I appreciate that you took the trouble to write and tell us what you think of FIG. I fully understand your frustration, and respect your
countryside. He asked the old man why he was crying. The old man said, "I presented this piece of rock to your grandfather because it contains a large jade. Your grandfather let his jade expert inspect it, and he said, 'It's just a stone.' So, your grandfather cut off my right leg. Then I presented it to your father. Your father had it inspected, and he cut off my left leg. I cannot walk any more. Then Heaven brought you here so I can see you. Here is the rock. It's yours, if you can use it." The King of Tsu ordered his jade worker to saw it open and found the largest and the most beautiful jade ever seen in China. It was from this jade that the First Emperor of China made his imperial seal.

Forth is like this piece of jade in a rock. We know it is the best tool for human beings to control computers. We tried to give it to the world, but what did we get back? Both feet cut off. What can we do? t think all we can do is stick together, keep warm, and keep the pot

> There's no reason why metacompilers should be hard to learn, use, or understand...
decision notto continue your membership. However, I think the greatest challenge we Forth programmers all face is not just to survive the onslaught of C and Unix, but to pool our resources together and find the niche where Forth can still grow and shine.

Let me repeat an ancient Chinese story to make my point. The King of Tsu met an old man, crying in the
stirred. If Forth is really that good, it will shine eventually. When? I do not think anybody knows. FIG is the only place where we can keep ourselves warm.

Dr. C.H. Ting
San Mateo, California

## Mastering <br> Metacompilation

Dear Editor,
Metacompilation is get-
ting a bum rap.
Recent postings on GEnie, and an item by Dr. C.H. Ting in Forth Dimensions ("How Metacompilation Stops the Growth Rate of Forth Programmers," $F D$ XIII/1), have re-opened one of the dark closets of Forth: metacompilation.

Metacompilation is probably the most arcane aspect of Forth, and is usually considered the province of the Forth "priesthood." I sympathize. But I think it's a mistake to abandon metacompilation for assembly language source, as Dr. Ting advocates. This is like going back to gas lighting because people get shocked by (and don'tunderstand) electricity.

Education is the answer.
For the last four years, I've been saying that the problem is not that there aren't good Forth metacompilers, but that there isn't any good documentation for metacompilers.

I've come up through this the hard way. My first experience with a metacompiler (cross-compiler) was in 1982, when I tried to use a commercial product. The documentation was incomprehensible, the compiler obscure. Finally, I was able to complete my project by hacking my code onto the kernel supplied with the compiler, and by abandoning any attempt to use defining words, IMMEDIATE words, or vocabularies. (Rather like programming in C, come to think of it.)

My second experience was with a version of Cassady's Metaforth, implemented and modified by a friend of mine. I began to


## INVITED SPEAKERS

Forth and Space at the Applied Physics Laboratory
Ben Ballard and John Hayes The Johns Hopkins University, Applied Physics Laboratory, Baltimore, MD
"Tunnel Vision," the "Evil Eye" and Other Impediments to Scientific Vision Daniel B. Rayburn, PhD. Department of Respiratory Research, Division of Medicine, Walter Reed Army Institute of Research, Washington, DC
A Noteworthy Discussion of the Tradeoffs Between Forth and C for Instrumentation
Peter Helmers Musical Dynamics, Inc., Rochester, NY
Forth and Medical Imaging: A Perfect Fit
Phillipe Briandet Research Director, Sopha Medical, Baltimore, MD
Zeus the All Seeing: Image Analysis with a MacIntosh, an IBM, and Two Kinds of Forth
F. MacIntyre A/S Pixelwerks, Bergen, Norway

## PROCEEDINGS AVAILABLE

$\$ 30$. plus $\$ 5 . S / H$
Send name, full address and phone
number. Check or money order in
US Funds or VISA/MC number
and expiration date.

Send request to:
Forth Institute
70 Elmwood Avenue
Rochester, NY 14611
Voice: (716) 2350168 FAX: (716) 3286426
EMail: GEnie L. Forsley internet: 72050.2111@compuserve.com compuserve: 72050,2111

Figure One. The probability of matching birthdays.

```
GIGSIZEMAX Ejze (am even mumber of bytes) af biggest mumber needed.
EIG area of memory which toolds a multipiemgength umsigmed
mumber, two bytes lomger than EIGGIZEmAx simoe the most sigmificant
&&l! i s used to monitor overflow.
BIGLOW Eddress of least sjgnificant cell of EIG number..
EIG! (u - - ) EEt= EIG number to u,
BIGi] (--- u ) fetches u from least significoant cell of EIG number.
ETGGIZE (--- u) returns thes Eize (roumded up to an even number of
bytes) of EIG mumber, Excluding leading zeroes.
EIG* (u --- ) multiplies ETG mumber by un prachumt íg mew EIG mumber.
EIG/MDD ( ul -- uF ) dividEE ETG mumtaEr by ul: quotient is new EIG
rumber", memaimoder i= u-*
FEOFl.E ( T -..- ) calculatesm and displays the probability of at least. one
pair of matctijrg birthdays in a group of n people
```

```
\ Eig Number Arjutmetic o The Frobability of Matchimg Ejrthdays
100 constant EIGSIZEMAX ( whatever you meed but must be even )
Variatale IGGG bigeizeman allot big bigsizemax + coristant. ElGL_OW
" EIG! (u w--. ) big Eiggizema% erasembiglow ! "
: EIGM ( - u- u) biglow %
: EIGSIzE ( --- u) big z+ bigsizema% g ckip nip 1+ -2 and :
" BIG* (u- - ) g biglowbigeize - biglow
```



```
    2drop big i) abort" *** big multiply overflow ***" ;
: ECG/MOD (uI mon u2 ) 0 biglow 2+ dup bicgize -
    Tdo over i ia moot um/mod i ! 2 +loop nip y
```




understand the workings of the compiler, but even with my friend as a resource, I couldn't decipher how to add defining words or vocabularies.

The breakthrough came on my third attempt: the polyFORTH target compiler. And the reason for the breakthrough is not that the compiler is that much simpler (although it's certainly elegant!), but that I was able to attend the polyFORTH ad-
vanced course and have someone teach me how metacompilers work. Enlightenment!

I then proceeded to test my understanding of the theory and practice of metacompilers, by writing one of my own. (I had other reasons for this-I needed features that no one else was offering.) But, remembering how much difficulty I had in acquiring the essential concepts, I resolved to keep notes and record my thoughts as Iwrote the compiler. Ithink

I may be the first person to document the process of discovery this way.

In December of 1988 I condensed these notes into a one-hour presentation for our local FIG chapter, and was amply rewarded. Even though I delivered it at warp speed, several people came up to me afterward and told me that they understood metacompilation for the first time.

The lesson I learned from giving this talk is this: there's
no reason why metacompilers should be hard to learn, use, or understand. The concepts aren't complicated; it's just that everyone who understands metacompilers takes them for granted!

Frank Sergeant, in Fortb DimensionsXII/6, has started to dispel the mystery of metacompilation, by presenting a clear description of an elegant compiler. Like eForth, Pygmy isbeing ported to many CPUs in the embed-
(Continued on page 15.)

# HARVARD SOFTWORKS <br> NUMBER ONE IN FORTH INNOVATION 

(513) 748-0390

P.O. Box 69, Springboro, OH 45066

## MEET THAT DEADLINE ! ! !

- Use subroutine libraries written for other languages! More efficiently!
- Combine raw power of extensible languages with convenience of carefully implemented functions!
- Yes, it is faster than optimized C!
- Compile 40,000 lines per minute!
- Stay totally interactive, even while compiling!
- Program at any level of abstraction from machine code thru application specific language with equal ease and efficiency!
- Alter routines without recompiling!
- Use source code for 2500 functions!
- Use data structures, control structures, and interface protocols from any other language!
- Implement borrowed feature, often more efficiently than in the source!
- Use an architecture that supports small programs or full megabyte ones with a single version!
- Forget chaotic syntax requirements!
- Outperform good programmers stuck using conventional languages! (But only until they also switch.)

HS/FORTH with FOOPS - The only full multiple inheritance interactive object oriented language under MSDOS!

Seeing is believing, OOL's really are incredible at simplifying important parts of any significant program. So naturally the theoreticians drive the idea into the ground trying to bend all tasks to their noble mold. Add on OOL's provide a better solution, but only Forth allows the add on to blend in as an integral part of the language and only HS/FORTH provides true multiple inheritance \& membership.

Lets define classes BODY, ARM, and ROBOT, with methods MOVE and RAISE. The ROBOT class inherits:

## INHERIT> BODY

HAS $>$ ARM RightArm
HAS> ARM LeftArm
If Simon, Alvin, and Theodore are robots we could control them with:
Alvin 's RightArm RAISE or:
+5-10 Simon MOVE or:
$+5+20$ FOR-ALL ROBOT MOVE
Now that is a null learning curve!

## WAKE UP ! ! !

Forth is no longer a language that tempts programmers with "great expectations", then frustrates them with the need to reinvent simple tools expected in any commercial language.

## HS/FORTH Meets Your Needs!

Don't judge Forth by public domain products or ones from vendors primarily interested in consulting they profit from not providing needed tools! Public domain versions are cheap - if your time is worthless. Useful in learning Forth's basics, they fail to show its true potential. Not to mention being s-l-o-w.

We don't shortchange you with promises. We provide implemented functions to help you complete your application quickly. And we ask you not to shortchange us by trying to save a few bucks using inadequate public domain or pirate versions. We worked hard coming up with the ideas that you now see sprouting up in other Forths. We won't throw in the towel, but the drain on resources delays the introduction of even better tools. Don't kid yourself, you are not just another drop in the bucket, your personal decision really does matter. In return, we'll provide you with the best tools money can buy.

The only limit with Forth is your own imagination!

You can't add extensibility to fossilized compilers. You are at the mercy of that language's vendor. You can easily add features from other languages to HS/FORTH. And using our automatic optimizer or learning a very little bit of assembly language makes your addition zip along as well as in the parent language.

Speaking of assembly language, learning it in a supportive Forth environment turns the learning curve into a light speed escalator. People who failed previous attempts to use assembly language, conquer it in a few hours or days using HS/FORTH.

HS/FORTH runs under MSDOS or PCDOS, or from ROM. Each level includes all features of lower ones. Level upgrades: $\$ 25$. plus price difference between levels. Source code is in ordinary ASCII text files.
All HSFORTH systems support full megabyte or larger programs \& data, and run faster than any 64 k limited ones even without automatic optimization -. which accepts almost anything and accelerates to near assembly language speed. Optimizer, assembler, and tools can load transiently. Resize segments, redefine words, eliminate headers without recompiling. Compile 79 and 83 Standard plus F83 programs.

## PERSONAL LEVEL

## $\$ 299$.

NEW! Fast direct to video memory text \& scaled/clipped/windowed graphics in bit blit windows, mono, cga, ega, vga, all ellipsoids, splines, bezier curves, arcs, turtles; lightning fast pattern drawing even with irregular boundaries; powerful parsing, formatting, file and device I/O; DOS shells; interrupt handlers; call high level Forth from interrupts; single step trace, decompiler, music; compile 40,000 lines per minute, stacks; file search paths; format to strings. software floating point, trig, transcendental, 18 digit integer \& scaled integer math; vars: A B * IS C compiles to 4 words, $1 . .4$ dimension var arrays; automatio optimizer for machine code speed.
PROFESSIONAL LEVEL
$\$ 399$.
hardware floating point - data structures for all data types from simple thru complex 4D var arrays - operations complete thru complex hyperbolics; turnkey, seal; interactive dynamic linker for foreign subroutine libraries; round robin \& interrupt driven multitaskers; dynamic string manager; file blocks, sector mapped blocks; x86\&7 assemblers.
PRODUCTION LEVEL \$499.
Metacompiler: DOS/ROM/direct/indirect; threaded systems start at 200 bytes, Forth cores from 2 kbytes;
C data structures \& struct+ compiler, TurboWindow-C MetaGraphics library, 200 graphid'window functions, PostScript style line attributes \& fonts, viewports.
ONLINE GLOSSARY
PROFESSIONAL and PRODUCTION LEVEL EXTENSIONS:
FOOPS+ with multiple inheritance $\$ 79$. $\begin{array}{ll}\text { TOOLS \& TOYS DISK } & \$ 79 \\ \text { 286FORTH or 386FORTH }\end{array}$ 16 Megabyte physical address space or gigabyte virtual for programs and data; DOS \& BIOS fully and freely available; 32 bit address/operand range with 386. ROMULUS HS/FORTH from ROM \$89. FFORTRAN translator/mathpak $\$ 79$. Compile Fortran subroutines! Formulas, logic, do loops, arrays; matrix math, FFT, linear equations, random numbers.
Shipping/system: US: \$7. Canada: $\$ 19$. foreign: \$49. We accept MC, VISA, \& AmEx

# Universal Control Structures 

Kevin Haddock Chico, California

When I first started working with Forth, it's execution control was one of the most difficult aspects to learn. The basic words weren't difficult to understand-butIwasencountering frustration trying to make my programs fit into the mold those words wanted to impose on them.

After seeing the wisdom of everything else Forth has to offer--simplicity, factoring, bottom-up design, performance, and economy, to name a few-I see evidence that control structures are an area of concern even for the enlightened few.

Indications of this can be found in the numerous efforts at case statements: Parnas' IT $\ldots \mathrm{TI}$ ( $F D \mathrm{VI} / 1$ ), and security-less (e.g., poly-) Forth's gymnastic mixing and matching of incompatible types of control operators by bouncing offset addresses around on the stack at compile time.

I have always claimed that Forth has all the advantages of an interpreted language like BASIC, but here again the control structures get in the way. In BASIC, you can always whip up a quick loop or check the logic of your code with a conditional typed at the command line.

I have seen this interpre-
tative situation addressed to some degree, but to date I have not seen an integrated solution that is uniform, clean, simple, and "Forth-like." (If I can judge what is "Forthlike"; the debate rages on!).

I was discussing the situation with a Forth-wiser friend of mine when I made the first step in stumbling upon the solution. Sometimes the hardest solution to see is the one that is right in front of your nose. (This happens a lot with me, especially when it comes to Forth.)

He said, "You know, Kevin, there really are only two control operators in

Forth, in comparison with C, for example, was crafting the code to eliminate unnecessary tests. If any one of a series of ANDed conditions is not true, there is no need to waste any more time extracting and evaluating subsequent conditions. Similarly, if any one in a series of ORed conditions is true, processing can continue in the body of the condition. Since realtime is one of Forths' strengths, how come it falls so flat in this area? Control structures!

While writing my on-line classified adsystem in Bourne Shell, I recall having fun

> Since real-time is one of Forth's strengths, how come it falls so flat in this area? Control structures!

Forth, BRANCH and OBRANCH (and ?BRANCH in the newer versions). You can do whatever you want with those." Well, I knew that on an intellectual level, but this forced me to focus, it got my wheels turning. I know the wheels of my mind grind exceedingly slow, but I would like to think they grind exceedingly fine!

One of the things that created the most difficulty working with traditional
seeing how many of my conditional needs could be met with the $|\mid(O R)$ and $\& \&$ (AND) operators, which would drop out of the current line of script if the previous command returned true or false, respectively. I was pleasantly surprised at how comfortable it was working withthis arrangement, although it required a slightly different mode of thought than is conventionally used in programming. I could appease
my old modes of thought by coining the pet names: if so for $\&$ and ifnot for 1 . (Since there was little collision with Forth words, I opted for the single-character versions.) You can use whatever emotional crutch feels the most appropriate to you, or none at all.

It was apparent that just falling out of the line (or colon definition) was inadequate for my needs. Most people aren't willing to factor their definitions to that level, nor should they have to. There was also the question of looping, restarting the execution at some previous point in the code.

Those operators \& and । marked a point of departure, but there needed to be something (other than : and ;) to mark the points of return. In other words, if \& and I caused you to break out of the execution of the code, based on some condition, where do you land? Other languages have their endifs, thens, line terminators, etc. After serious deliberation, I decided to confiscate C's block delimiters $\{$ and $\}$.

I know, I hear you saying, "Here is just another character trying to make Forth more C-like," but trust me, the icons are where the similarity ends. Years ago, someone suggested they look like profiles of faces, and I took it one step further by saying that, together, they resembled the faces, comedy and tragedy, of live theater.

With this worked out, conditionals | and \& from which to branch, and layers of balanced control block delimiters, I had discovered how to lay down the OBRANCH control ops. The only thing left to determine was how to lay down the

| Icon | Say | Description |
| :---: | :---: | :---: |
| \{ | comedy | Begins a new control block. |
| \} | tragedy | Ends the most recently open block. |
|  |  | (Note: these can nest to any depth) |
| \& | ifso | Falls or "sinks" out of the current control block if the top of stack is false. |
| 1 | ifnot | Same as above if TOS is true. |
| $\wedge$ | float 1 | Rises back to the top of the current control block. |
| $\wedge \wedge$ | float 2 | Rises back to the beginning of the control block before this one. |
| ^ヘ^ ^ヘ^^ |  | Logical extension of the above. |
| VV | $\operatorname{sink} 2$ | Falls through or "sinks" to the end of the second subsequent control block. |
| VVV VVVV ... |  | Logical extension of the above. |

BRANCH ops responsible for looping and skipping the ELSE part of a conditional construct.

My solution was to use combinations of one or more carets ( $\wedge$ ) and lower-case v's (you can use upper case, if your system converts case automatically) to specify that execution was to float or sink that many control block levels. (Actually, "levels" is probably the wrong word here. You are actually "denesting" if you are thinking in terms of C , but what matters is whether you de-nest toward the top or the bottom of the program.)

Let me see if I can bring this all together for you in Figure One. You will notice later that sink 1 (v) is never needed: the default behavior is to fall out of the current block at the end. \& and serve the purpose of falling
out of the current block before the end, based on a condition.

Some of you may find that a caret ( $\wedge$ ) is difficult to enter with your editor, especially if yours is patterned after the Forth, Inc. line editor, which uses it as a delimiter. Two alternatives exist. Presented in the order of my personal preference:

- Change your editor's delimiter to something else; my suggestion would be a line feed; this character shouldn't have too adverse
an affect on any video updating and, since it's non-printable, you would be highly unlikely to want to enter it into your source code.

Or:

- Change the icon used for the float ( ${ }^{\wedge}$ ). You coulduse the tilde ( $\sim$ ), which on most CRT's looks like a bubble, and that could be your memory aid: The more bubbles, the higher it will float.

Another thing you will

[^0]```
\ UCS -- interpretative scanning
: @CHAR (S - c) >IN @ BLK @ ?DUP IF BLOCK + C@ ELSE
    TIB + C@ THEN ;
: ACCOUNT (S cnt chr - cnt') DUP ASCII | = IF DROP 1+
    ELSE ASCII } = IF 1- THEN THEN ;
: >NEXT (S n) 0< IF 1 ELSE -1 THEN > IN +! ;
: I~ (S n) -1 >IN + ! DUP 0< IF 1 ELSE -1 THEN
    BEGIN @CHAR ACCOUNT 2DUP = 0= WHILE OVER >NEXT
    REPEAT 2DROP ;
```

: I\&| BEGIN @CHAR ACCOUNT ?DUP WHILE -1 >NEXT REPEAT
$1>$ IN + ! ;
\ Universal Control Structures
: ( $(S$ - a 0$)$ STATE @ IF HERE 0 THEN ; IMMEDIATE
: \& (S a - a) STATE © IF COMPILE ?BRANCH HERE SWAP, ELSE
$0=1 F 1$ I\& 1 THEN THEN ; IMMEDIATE
$: 1(S \mathrm{a}-\mathrm{a}) \mathrm{STATE}$ @ IF COMPILE 0= [COMPILE] \& ELSE
IF 1 I\&। THEN THEN ; IMMEDIATE
: \} (S a a) STATE @ IF BEGIN ?DUP WHILE DUP @ SWAP
HERE SWAP ! REPEAT DROP THEN ; IMMEDIATE
$\backslash$ Universal Control Structures
: ( S - a _) BL WORD NUMBER DROP ; IMMEDIATE
: ~ (S al a2 a1 a2 ... n) STATE @ IF COMPILE BRANCH >R SP@
R@ ABS 2* $2^{*}+\mathrm{R}>0<\mathrm{IF}$ HERE OVER @ , SWAP !
ELSE $2+$ @ , THEN ELSE I~ THEN ; IMMEDIATE
$: \sim(S \mathrm{n}$, CREATE , DOES> @ [COMPILE] ~;
: ~S: (S e s _ _ ...) DO I ~: IMMEDIATE LOOP ;
5-4 ~S: VVVVV VVVV VVV VV ^ ^^ ^^^ ^^^^ ^^^^^^
$\backslash$ DUMP
16 CONSTANT WIDE
: . CHAR (S c) $\{\{\operatorname{DUP} 32<1$ DUP $126>\mid$ VV $\}$
DROP ASCII . \} EMIT ;
: .ASC (S a c) \{ OVER C@ .CHAR 1- SWAP 1+ SWAP ?DUP \& ^ \}
DROP ;
: . HEX (S a c) \{ OVER C@ $0<\# \# \# \#>$ TYPE SPACE 1-
SWAP 1+ SWAP ?DUP \& ^ \} DROP ;
: .ADR (S a) 0 <\# ASCII : HOLD \#\#\#\#\#> TYPE ;
: .DUMP is a c) $\{$ ( KEY? | CR BASE @ >R OVER .ADR
2DUP . HEX SPACE .ASC R> BASE ! VV \} ABORT \} ;
: DUMP ( S a c) $>\mathrm{R}$ \{ R@ \& \{ DUP WIDE MOD ?DUP | WIDE \}
R@ MIN R> OVER - >R 2DUP .DUMP + ^ \} R> 2DROP ;
@CHAR returns the next char in the input stream
ACCOUNT -- given the current level count and char returns the
adjusted count
(Continues.)
notice is that the branch operators ( $\wedge$ and $v$ ) and their derivatives (^^, ^^^, vo, vvo, etc.) will always appear just before tragedy (). There may be some elegant way of combining the two, but I haven't found any. Also, since comedy and tragedy don't really take up any compiled memory, there is no real loss.

Some may argue that the universal control structures (UCS) are nothing more than an elaborate GOTO scheme. Albeit that some may use this tool to take Forth further down the path of obfuscation by creating even more poorly factored and deeply nested eyesores, I must defend the utility by saying that with the requirement to keep the control delimiters balanced, you would have a very difficult time branching wildly into the center of some block of code without clearly indicating your intentions.

I hope this utility will be a win-win situation for all users. The vertical-obfuscat-ed-code people will be able to nest deeper than ever before (if that can be considered a win); the horizontal factorers can construct the structure to exactly what they want; and the poly-, cmand Pygmy-Forth types can get even wilder than before (this utility is great for passing control out of one word and into the middle of another, for example).

As you will see, there are actually two versions of each word, one for executing and another for interpreting. These are combined into one state-smart word where applicable; this may be one of the few places where a statesmart word may be excusable, since there is only a slight compile-time penalty and no run-time penalty.

A note of caution is in
order here: the interpreted version just does a string search for the balancing control delimiter, so remember: if you use braces-er, facesin your words or in comments, always use them in balance. For instance, you could name a word \{FRED \} but try to avoid (FRED unless it is to be followed almost immediately with something like FRED \}. This is the only minor un-Forth-like limitation on syntax, but I feel it is a small price to pay for the benefit received.

Possible uses for the interpreted version could be compilation scripts, non-timecritical routines, memorycritical programs, debugging with zero compile time, and my favorite: a late-binding, object-oriented system where methods could exist in different vocabularies that know how to deal with their own data types. You select the context by determining what kind of data object you are dealing with, then process the actual, reusable algorithm interpretatively from disk. With the old FIG-style hierarchical vocabularies, you could essentially build superclasses.

And now for the moment you have all been waiting for: on to the code!

Due to the drastically different behavior of the compiled and interpreted operators, I will describe them separately, even though some versions of Forth will bundle them into the same state-smart word.

It goes withoutsaying that all control structures are compiler directives and, therefore, have precedence in whatever mechanism is appropriate for your Forth.

While compiling, UCS holds a pair of addresses on the stack for each open con-

```
>NEXT -- adjusts the input stream pointer based on the
    sign of the given number
I~ -- adjusts the interpretative pointer to the given control
        block. Negative control block offsets move the adjust the
        pointer forward. Positive or zero backward.
I\&1 -- adjust the interpretative pointer to the end of the current control block.
1 -- denotes the start of a control block.
\& -- falls out of the current control block if given value is false
| -- falls out of the current control block if given value is true
\} -- marks the end of a control block. When compiling, this word resolves all the forward references for this block.
```


## UCS - Pygmy version.


FORTH
comment:
UCS -- pygmy version
f opens up a conditional structure. When in doubt use a lot of these. Make sure to close them. Check your stack after compiling to make sure you got them all.
\& takes an argument off the stack and if false branches to the end of the current conditional structure. Does a boolean and but can be thought of as (and pronounced) 'if so'.
I does the same as above except branching when true. Does a boolean OR. Can be thought of as (and pronounced) 'if not'.
) resolves all the exit conditional branches for this structure and marks the exit location. These must balance the open braces above. Check your stack after compiling when not sure
comment;

```
    ( Universal control structure 901127 KAH ) COMPILER
: ` ( _ - a) 32 WORD NUMBER ;
: ~ ( a1 a2 a1 a2 ... n _) COMPILE branch
    DUP ABS BEGIN ?DUP WHILE 2SWAP PUSH PUSH 1-REPEAT
    DUP PUSH 0< IF HERE dA @ - SWAP, ELSE OVER, THEN
    POP ABS BEGIN ?DUP WHILE POP POP ROT 1- REPEAT ;
```



```
: vv -1 \ ~ ; : vvv -2 \ ~ ;
: vvvv -3 \ ~ ; : vvvvv -4 \ ~ ;
FORTH
comment:
UCS -- PYGMY VERSION
- allows you to specify the branch direction and number inline
    while compiling, ie ` -5 ~ branches down 5 levels (not
    including the one you are in)
~ lays down an inline branch backward (for positive numbers)
    or forward (for negative numbers) the given number of levels.
~'s and v's are shorthand for forward and backward branches
comment;
( Interpretive universal control structures) FORTH
: { ; : } ;
: @CHAR ( - c) { { >IN @ BLK @ ?DUP & BLOCK + C@ vv }
    TIB @ + C( ) ;
: ACCOUNT ( cnt chr - cnt') { { DUP '{ = & DROP 1+ vv }
    '} = & 1- } ;
: >NEXT ( n) { { 0< & 1 vv } -1 } >IN +! ;
: \~ (n) -1 >IN +! { { DUP 0< & 1 vv } -1 }
    { @CHAR ACCOUNT { 2DUP = { OVER >NEXT ^^ } } 2DROP ;
: ~ \~ ;
comment:
UCS -- PYGMY VERSION
{ and } are just markers in interpretative mode
@CHAR returns the next char from the appropriate input stream
ACCOUNT returns a possibly adjusted brace level counter for
    the given char and counter
>NEXT adjusts >IN in the proper direction for the given level
\~ scans for the given number of uneven braces, backward for
    positive numbers and forward for negative
\~ is the compiliable version, ~ is the user interface
comment;
( Interpretive Universal Control Structures)
:& ( n) { | 1 { @CHAR ACCOUNT ?DUP & -1 >NEXT ^ }
    I >IN +! } ;
the Pygmy version, this gives us our first live example of compiled UCS in action. I also leave it as an exercise to re-code the compilable part of UCS for metacompiling (andeven your Forth nucleus, if you get so inspired). I'm not saying a Forth should not have the traditional control structures, just that it might be a more elegant solution defining them and Forth in terms of UCS, rather than the other way around.

Included in the listings are some quick examples: a case statement in the form of what could be a key/command loop, both in interpreted and compiled form; a memory dump utility; and a simple utility to create memory headers that will go out and interpretively load blocks, saving memory and compilation time (also with possible object-oriented implications as mentioned earlier).

As a final note, remember to keep an eye on your stack depth changes before and after compiling. The UCS have, and need, no compiler security. Too many comedies will grow your stack and too many tragedies will shrink it. Minimalists should already be familiar with this technique.

I hope you will have as much fun playing with, and improving upon, the UCS as I did discovering them. Enjoy!
```

: | ( n) { \& 1 { @CHAR ACCOUNT ?DUP \& -1 >NEXT ^ }
1 >IN +! } ;
:^ 0 \~ ; : ^^ 1 \~; : ^^^^ 2 \~ ;
: vv -1 \~ ; : vvv -2 \~ ;
: vvvv -3 \~ ; : vvvvve -4 \~ ;

```
comment:
UCS -- PYGMY VERSION
These have basically the same meaning and usage as the
    compileable ones
comment ;
( \(\backslash\) \REM \COMP ED: and SCAN)
DEFER \}
: \COMP CREATE BLK @ , POP DROP DOES> @ LOAD ;
: \REM >IN @ \(64 / 1+64 \star>\) IN ! ;
- \REM IS \(\backslash\)
: ED: (_) ' \(3+\) @ EDIT ;
: SCAN ['] \COMP [ ' \ 1+ ] LITERAL !
    THRU ['] \REM [ ' \(\backslash 1+\) ] LITERAL ! ;
comment:
UCS -- PYGMY VERSION
\ during loading acts as a interpretative comment marker and
        during scanning compiles the following word which loads
        the affected screen
\COMP is the version that compiles the loading word
\REM is the comment (REMark) marker
Initially set the vector to remark
ED: <name> edits the screen for the loading word <name>
SCAN acts like thru except scanning and creating loading words
comment;
( Compiled sample key input/case loop)
: TEST
    \{ 1
        CR ." ENTER A LETTER:" KEY
        \{ DUP \(27=\& . "\) BYE " vvv \}
        DUP 'A \(=\& . "\) ALPHA " vv \}
        \{ DUP 'E = \& ." EDWARD " vv \}
        \{ DUP 'I = \& ." IDA " vv \}
        \{ DUP \({ }^{\prime} O=\& . "\) OCEAN " vv \}
        \{ DUP 'U = \& ." UNION " vv \}
comment:
UCS EXAMPLE -- PYGMY VERSION
An example of a compiled case structure
hit escape to quit
comment;
( DUMP ) 16 CONSTANT WIDE
:.CHAR (c) { { DUP 32< | DUP 126> | vv }
    DROP '. } EMIT ;
:.ASC ( a c) { SWAP C@+ .CHAR SWAP 1- ?DUP & ^ }
    DROP ;
: .HEX ( a c) { SWAP C@+ 0 <# # # #> SPACE
        SWAP 1- ?DUP & ^ } DROP ;
:.ADR ( a) 0 <# ': HOLD # # # # #> ;
: .DUMP ( a c) ?SCROLE CR BASE @ HEX PUSH OVER .ADR
    2DUP .HEX SPACE .ASC POP BASE ! ;
: DUMP ( a c) PUSH { I & { DUP WIDE UMOD ?DUP | WIDE }
    I MIN POP OVER - PUSH 2DUP .DUMP + ^ | POP 2DROP ;
comment:
UCS EXAMPLE -- PYGMY VERSION
WIDE returns how many bytes to dump per line
.CHAR prints the given char if printable else prints an elipse.
.ASC prints the given count number of chars at the given addr
.HEX prints c hex bytes at the given addr
.ADR prints the given address
.DUMP prints one line of the dump for the given addr and count
DUMP dumps c bytes of memory at the given addr.
comment;
\TEST interpreted sample key input/case loop
1 {
    CR ." ENTER A LETTER:" KEY
    { DUP 27 = & ." BYE " vvv }
    { DUP 'A = & ." ALPHA " vv }
    { DUP 'E = & ." EDWARD " vv }
    { DUP 'I = & ." IDA " vv }
    { DUP 'O = & ." OCEAN " vv }
    { DUP 'U = & ." UNION " vv }
    CR BEEP DUP EMIT ." IS NOT A VOWEL! "
    } DROP
    } DROP
```

```
```

        CR BEEP DUP EMIT ." IS NOT A VOWEL! "
    ```
```

        CR BEEP DUP EMIT ." IS NOT A VOWEL! "
        } DROP ^
        } DROP ^
    } DROP ;
    ```
    } DROP ;
```

(Pascal, C, C++, MacApp, Assembler), MDS Assembler, Language Systems Fortran, MacFortran, Prograph, Smalltalk, Think C, TLM Pascal, True Basic, Lisp, Modula2, Simula, MacForth (still on sale), Mach2 Forth (no more advertised), Neon (become public domain), MasterForth (not supported for a long time), Pocket Forth, Hypercard, and... as well as the many dBase-like languages like Omnis, 4th Dimension, etc.-sure the Mac "speaks many languages," but the environments are "oh, so different."

Macintosh programmers are busy generating standalone applications and adhering rigidly to Apple's programming guidelines. Forth is not concerned with that. (How could it be?)

The majority of Forth code exchanged within the Forth community in journals like Forth Dimensions and the Journal of Forth Application and Research doesn't deal with standalone applications,

Forth community lost its last meeting platform. Whatever happened to the MacForth user group? The last newsletter I got was sometime in late 1986!

The Forth community is, naturally, busy debating Forth. And it is mostly machine independent.

The Forth compiler and interpreter is a powerful tool, in itself, that is always present. This is perhaps the major and most important difference between Forth and any other language, and is immensely hard to explain to non-Forthers. A lot of Forth's power lies precisely in its accessibility: the ability to extend the compiler and interpreter, to add to it, to use or abuse it (refer to Defector's reference to anarchy) within new definitions. The fact is that the processor is there for you to use, directly from an interactive console or via some prior compilation.

On the Mac, we are expected to program so that, ultimately, the Forth layer

> There is no reason why the vast programming utilities of MPW could not become avail. able to the Forth environment.
and only a very few times do they approach the subject of a Mac-like graphical user interface. Never, Ibelieve, have they dealt with Macintosh issues in particular.

The Macintosh community is, naturally, debating Macintosh issues with all it's virtues and quirks. When Mactutor's Forth column was stopped in favor of C++ and MacApp, the Macintosh's
will not be present. No more definable words, no more vocabularies, no more Forth interpreter! Some of Apple's programming guidelines also affect the available Forths directly. For example: not to use any of the 68000 trap mechanisms, and not to use in-line variables in code segments (due to future instruction caching). Words like

A FAST FORTH,
OPTIMIZED FOR THE IBM
MERSONAL COMPUTER AND
MSOS COMPATIBLES.
BTANDARD FEATURES INCLUDE:
-79 STANDARD

- DIRECT I/O ACCESS
- FUTL ACCESS TO MS-DOS FILES AND FUNCTIONS
- ENVIRONMENT SAVE \& LOAD
-MULTI-SEGMENTED FOR IARGE APPIICATIONS
-EXTENDED ADDRESSING
- MEMORY ALIOCATION CONFIGURABLE ON-IINE
-AUTO LOAD SCREEN BOOT
- LINE \& SCREEN EDITORS
-DECOMPILER AND
DEBUGGING AIDS
-8088 ASSEMBLER
©GRAPHICS \& SOUND
-NGS ENHANCEMENTS
-DETAIIED MANUAL
-INEXPENSIVE UPGRADES
ONGS USER NEWSLETTER
A COMPLETE FORTH DEVELOPMENT SYSTEM.

PRICEB BTART AT $\$ 70$
NEW - HP- 150 \& HP- 110 VERSIONS AVAILABLE


NEXT GENERATION BYBTEMS P.O.BOX 2987 BANTA CLARA, CA. 95055 (408) 241-5909

# Neural Network Words 

Tim Hendtlass<br>Hawthorn, Victoria, Australia

The Last of Two Parts

1n part one of "Neural Network Words," a number of words were developed to build and train neural networks. Words are now presented to save a network layer to disk. Most networks will learn from real life data rather than from an artificial example, such as the XOR relationshipused in part one. The advantages of scaling the real life input and output values to suitable internal values for the neurones used was also demonstrated in part one.

Working out scaling factors is tedious, at best, and words are now presented to

## The more complicated the relationship to be learned, the

 more neurones will be needed.
## Errata

In the firstpant of this anicle (FD XIIL/2), we erroneously printed an incorrect figure. In that issue, the Font fileprinted as Figure Eight (beginning on page 20) actually contains words related to disk UO, which is discussed in this install ment. Please refer to it for that code. In turn, the file NNDEMO.SEQ which should bave been printed last time as Figure Eight, appears herewith. We apologize to the autbor and to any readers who were inconvenienced.

The autbor also wisbes to note that in DISKNN.SEQ (the file which appeared last sime as Figure Eight), be used a couple of outdated word names in the opening comment: $N L->D I S K$ instead of SAVE-LAYER, and DISK $\rightarrow N L$ instead of LOAD-LAYER.
$-E d$.
read a data file, calculate the required scaling factors, and save these to disk. Finally, a general-purpose word to train a network is presented. It is customised to a particular application by assignments made to four deferred words. An example that uses these words is given.

> Words to copy a network layer to and from disk SAVE-LAYER
> This word saves a neural
layer to disk. The address of the layer to save must be on the stack on entry, and the name of the file to create must immediately follow SAVE-LAYER on the current input line. If a file by this name already exists, the user is asked for permission to overwrite it.

## READ-LAYER

This word restores a neural layer from disk. The layer must have been created before the word is called. The address of the layer to copy to must be on the stack on entry, and the name of the file to read must immediately follow READ-LAYER on the current input line. No check is made to see that the layer being read to has the same dimensions as the layer on disk.

## Words to read a data file

Data files used by this software are simple ASCII files, with one set of input values and one set of output values per line. No explicit provision is made in this version for comments, although anything on a line after the specified number of inputs and outputs will be skipped over. The variable $\mathrm{N}-\mathrm{IN}$
specifies the number of inputs to the whole network, the variable N -OUT the number of outputs from the whole network. READ\# reads one number from the current input and scales it into internal form.

The convenient word PROCESS-FILE saves information about what we were doing and moves up one handle on the handle stack. It then opens a new file, processes it, closes it, and returns to what we were doing when the PROCESSFILE word was encountered. PROCESS-FILE must be followed directly by the name of the file to use, and processing continues immediately after the filename. The actual processing done is controlled by the deferred word (PROCESS-FILE) which can be set to anything at all. SET-SCALES and TEACH-NETWORK (see below) differ mainly in what (PROCESS-FILE) is equated to.

Scaling words
SET-SCALES reads the file specified immediately following this word. It finds the maximum and minimum input and output numbers,

Figure Three. A small sample data file (XOR.DAT).

| 0 | 0 | 0 | \input input output |
| :--- | :--- | :--- | :--- | :--- |
| 1 | 0 | 1 | I input input output |
| 0 | 1 | 1 | I input input output |
| 1 | 1 | 0 | \input input output |

siderable interest in some situations. The error display is very simple, just printing the error for the four examples per line; since there are four possible examples, only this provides a simple way to see how the network is behaving. Alternatively, a simple four-graph plotting routine would make trends easier to observe.

## Conclusion

The words presented here extend those in part one. Together, they provide a toolkit for constructing nonlinear feed-forward networks and for training themby back propagation. They demonstrate again the power of Forth to produce a special language for a task. Following the approach used here, words could simply be produced to implement other types of neural layers, such
as self-organising Kohonen layers. By standardising on passing data between layers on the stack, networks of arbitrary complexity can be readily constructed, trained, and used.

## The generic training word

TEACH-NETWORK is the top generic training word. It expects the number of training passes to be on the stack on entry, but if the stack is empty it defaults to 1000 cases. It must be followed by the name of the data file to use. It does not establish the scale factors-these must have been just calculated, or a previously generated set must have been read from disk.

TEACH-NETWORK cOntains four deferred words, and these must be pointed to the routines for the specific task in hand. The deferred words, and their functions, are:

## DO-INIT

This may do any initialization of disk, display, or anything else required.

## FORWARD-WORD

This performs one evaluation pass of the network, updating the internal activa-
tion of each output. It expects the inputs, already scaled, on the stack on entry. Once a network is trained, this is the basic word for using it in recall mode.

## TRAIN-WORD

Given a set of scaled outputs on the stack, this word must calculate the errors in the outputs and back-propagate these to the network inputs, updating the weights as it goes. It leaves nothing on the stack.

## DISPLAY-INFO

This is a word to display any information about the state of the network that may be desired. It is called after each training example.

With these words, TEACHNETWORK will train for the specified number of cases or until a key is pressed. Figure One shows the code for the words described above. /See Errata onpage 17; the Figure One referred to here was printed as Figure Eight in the last issue.-EdJ

## Example

Figure Two shows an example that uses TEACHNETWORK, and Figure Three provides a suitable data file. Once again, the exclusive-or relationship is used so that the file is short, but any other could be substituted. The more complicated the relationship to be learned, the more neurones will be needed. If too few are used, the network either will not stabilise or will learn only the general features of the relationship and fail to learn the detail. Of course, this ability to generalize may be of con-

Tim Hendtlass has used Forth for years, and teaches it to about 100 students per year at the Swinburne Institute of Technology. He relates, "Roughly half the class are taking Computer Science as their co-major and, by the time they get to me, have one year of programming and several (nearly) working programs under their belts; they are sure they know it all. The other half are taking Medical Biophysics and generally are not sure what a computer is, are sure they don'tlike it, and have a fear that touching the keyboard will result in the machine taking over their minds.
"Despite this difference in background, within about one semester they are all controlling instruments in the laboratory using multi-tasking and hardware interrupts, all written in Forth. They like it, to their collective surprise. I would be delighted to hear from anyone else teaching Forth as a primary language of choice and as a vehicle to let students achieve some non-computing-related objective (scientific instrumentation systems, in my case).
"I have developed a series of experiments that I would be glad to share, and I would welcome hearing about others' experiences. Write to me in care of the Swinburne Institute of Technology, P.O. Box 218, Hawthorn 3122, Australia; or fax to 8195454 ."

```
    2 n-in ! 1 n-out ! \ define number of network inputs and outputs
    1 3 layer TOP
    top initialize \ top (output) layer
    3 2 layer BTM
    btm initialize \ bottom (input) layer
    clipping off
: --> ( -- )
    btm compute \ use stack data and evaluate the bottom layer
    btm get-outputs \ load bottom's output to stack
    top compute \ use bottom layer outputs, evaluate top layer
;
, --> IS FORWARD-WORD
:<-- (On On-1 .... 01-- )
    top calc-load-errors \ calculate and load the errors
    s0.3 top train \ train top layer, learning rate of 0.3
    btm load-errors \ load back propagated errors
    s0.8 btm train \ train bottom layer, learning rate of 0.8
    n-in @ 0 do 2drop loop \ lose back-propagated errors @ network inputs
    1 icount +! ; \ increment itteration count
< <-- IS TRAIN-WORD
: GET-ERROR ( n -- S# ) \ get nth output UNSCALED error
    dup get-oeadr 2@ \ get the internal error
    rot get-intact 2@ s'fn(x) s/ \ convert to external form
    o-s 2@ s/ \ and unscale it
;
:.ERROR \ show errors each set of inputs
    icount @ 1- 4/mod swap 0= if \ time for an new error line?
        crlf ." Set # " 1+ 4 .r ." - errors "
    else drop then 1 get-error s.\ \ print abs error
;
' .ERROR IS DISPLAY-INFO
: MISC-INIT O icount ! ;
' MISC-INIT IS DO-INIT
\ EXAMPLE
    set-scales xor.dat crlf .( Conversion scales computed ) crlf
    save-scales xor.sca
    .( Scales saved) crlf
    500 teach-network xor.dat crlf.( Network teaching terminated)
    top save-layer xortop.lyr .( Top layer saved) crlf
    btm save-layer xorbtm.lyr .( Bottom layer saved) crlf
```

$\backslash$ Four examples of simple artificial neural networks. Tim January 1990

comment:
These examples all use simple non-linear feedforward networks and are trained by back propagation. For simplicity, they use very simple inputs. Examples 2 to 4 all learn to impliment the exclusive or relationship, which practically would never be generated this way! Needs BASICNN to have been loaded, the line below will check and load this file if it is not already loaded. comment;

```
\ ******************************************************************************
```

                            ( ===> ) NEEDS BASICNN.SEQ ( <==== )
    
anew program
\ Example One, single layer learning two relationships.
12 layer TEST $\backslash$ build network with 2 inputs and 1 output
: EX1 ( -- ) \ the word that puts example 1 together
test initialize $\quad$ initialize layer
1 icount ! $\quad$ initialize itteration counter
clipping off $\mid$ clipping not required
begin
crlf ." Pass " icount @ .
S0.5 S0.5 $\backslash$ first use inputs of 0.5 and 0.5
test compute $\quad$ do one forward pass
test get-outputs $\backslash$ output to stack
2dup ." Output " s. $\$ print result we got
S-0.5 2 swap d- $\quad$ calculate error, result should be -0.5
test load-errors $\backslash$ load the error
S0.8 test train $\quad$ update weights using a learning rate of 0.8
2drop 2drop $\quad$ only 1 layer,backpropagated errors not needed
S-0.5 S1 $\backslash$ second use inputs of -0.5 and 1
test compute $\backslash$ do one forward pass
test get-outputs $\backslash$ output to stack
2dup ." Output " s. \print result we got
S0.5 2swap d- \ calculate error, result should be 0.5
test load-errors | load the error
S0.8 test train $\quad$ update weights using a learning rate of 0.8
2drop 2drop I don't need the backpropagated errors
1 icount + ! \ bump the iteration counter
key? if key drop exit then $\$ keep going until we get bored again
;
\ Example Two, two layers, XOR, standard input
13 layer test2 $\backslash 1$ output, 3 inputs The top layer
32 layer testl $\backslash 3$ outputs, 2 inputs The bottom layer
defer docases $\quad$ the difference between examples 23 and 4
defer top-layer
defer bottom-layer
: One-pass ( correct-output input2 input1 -- error )
testl compute $\backslash$ forward process the bottom layer
testl get-outputs $\quad$ I get its outputs ready for the next layer
test2 compute \forward process the top layer
test2 get-outputs $\backslash$ get its outputs ready for the error calc
2dup s. d-
\ print actual output, compute error

```
    test2 load-errors
    s0.3 test2 train
    test1 load-errors
    s0.8 test1 train
    2drop 2drop
;
: Ex2-docases
    s0 s0 s0 one-pass
    S1 S0 S1 one-pass
        S0 S1 S1 one-pass
        S1 S1 S0 one-pass
;
: (Ex2-4)
    1 icount !
    clipping off
    begin
        crlf." Outputs "
        docases
        ." after pass " icount @
        1 icount +!
        key?
        if
            crlf top-layer .layer
            crlf bottom-layer .layer
            key upc ascii Q =
            if exit
            else begin key?
                    until key drop crlf \ ..until they tell us that we may go on
            then
        then
    again
;
: Ex2
    test1 initialize
    test2 initialize
    \ initialize our...
    \ ...two layers
    ['] test1 is bottom-layer
    ['] test2 is top-layer
    ['] ex2-docases is docases
    (ex2-4)
;
\ Example Three, two layers, XOR, scaled inputs and outputs
: Ex3-docases
    S-0.5 S-1 S-1 one-pass \ do scaled 0 0 case
    S0.5 S-1 S1 one-pass \ do scaled 0 1 case
    S-0.5 S1 S1 one-pass \ do scaled 1 1 case
    S0.5 S1 S-1 one-pass \ do scaled 1 0 case
;
: Ex3
    test1 initialize \ initialize our...
    test2 initialize \ ...two layers
    ['] test1 is bottom-layer
    ['] test2 is top-layer
    ['] ex3-docases is docases
    (ex2-4)
;
```

```
\ Example Four,two layers, XOR, special geometry,scaled inputs and outputs
1 layer test4 \ 1 output, 3 inputs The top layer
1 2 \text { layer test3 \ l outputs, 2 inputs The bottom layer}
: MOD-ONE-PASS ( correct-output input1 input2 -- error )
    4dup test3 compute
    test3 get-outputs
    test4 compute
    test4 get-outputs
    2dup s. d-
    test4 load-errors
    s0.3 test4 train
    >r >r 2drop 2drop r> r>
    test3 load-errors
    s0.8 test3 train
    2drop 2drop
;
: Ex4-docases
    S-0.5 S-1 S-1 mod-one-pass \ do scaled 0 0 case
    S0.5 S-1 S1 mod-one-pass \ do scaled 0 1 case
    S-0.5 S1 S1 mod-one-pass \ do scaled 1 1 case
    S0.5 S1 S-1 mod-one-pass \ do scaled 1 0 case
;
: Ex4
    test3 initialize \ initialize our...
    test4 initialize \ ...two layers
    ['] test3 is bottom-layer
    ['] test4 is top-layer
    ['] ex4-docases is docases
    (ex2-4)
;
```


Advertisers Index
FORML ..... 44
Forth Interest Group ..... 35, 37
Harvard Softworks ..... 8
Journal of Forth Application \&Research6
Miller Microcomputer Services ..... 22
Next Generation Systems ..... 16
Offete Enterprises ..... 34
Silicon Composers ..... 2


Organization, Issues, Actions, and Explanations.

## Organization

 Business GroupSummary of recent business meeting minutes for July 21, 1991 (full minutes of the Business Group are avail-
able upon request):
Membership dues (correction): In the last $F D$, I stated that the dues for the new student membership were \$24 per year. That was one
of the figures suggested, however the correct figure that was approved by the business group was a student membership of $\$ 18$ per year.
$A D C$ Reports: There are now 1511 FIG members to date. This is an increase from last year at this time. This turn-around has been a long time in coming, and I expect the upward trend to continue.

## Board of Directors

The nomination process for the Board of Directors has taken place. According to the FIG by-laws, a nomi-
(Continued on page 26.)

| Who would |
| :--- |
| complain if |
| Forth were |
| required in |
| their project? |


| Forth Interest Group <br> Statement of Change in Financial Position April 30, 1990 to April 30, 1991 |  |  |  |
| :---: | :---: | :---: | :---: |
|  | 4/30/90 | 4/30/91 | Change* |
| ASSETS: |  |  |  |
| Current Members | 2106 | 1831 | -275 |
| Current Assets: |  |  |  |
| Foothill Bank, Money Market | 15,925.38 | 24,865.91 | 8,940.53 |
| Foothill Bank, Checking | 636.43 | 700.14 | 63.71 |
| Pending Foreign Clearing | -102.00 | 51.67 | 153.67 |
| Returned Checks Pending | 0.00 | 72.00 | 72.00 |
| FORML, Money Market | 15,894.11 | 16,916.46 | 1,022.35 |
| FORML, Checking | 1,926.01 | 1,236.64 | -689.37 |
| Total Current Assets: | 34,279.93 | 43,842.82 | 9,562.89 |
| Inventory: |  |  |  |
| Inventory at cost | 34,147.53 | 26,601.17 | -7,546.36 |
| Total Inventory: | 34,147.53 | 26,601.17 | -7,546.36 |
| Other Assets: |  |  |  |
| Deposit, United Parcel Service | 200.00 | 200.00 | 0.00 |
| Second Class Postal Account | 156.31 | 192.41 | 36.10 |
| Accounts Receivable | 3,166.20 | 2,099.00 | -1,067.20 |
| Total Other Assets: | 3,522.51 | 2,491.41 | -1,031.10 |
| TOTAL ASSETS: | 71,949.97 | 72,935.40 | 985.43 |
| LIABILITIES: |  |  |  |
| Sales Tax due | 33.89 | 35.58 | 1.69 |
| $F D$ Dues Alloc to future months FD | 37,487.30 | 41,518.51 | 4,031.21 |
| TOTAL LIABILITIES: | 37,521.19 | 41,554.09 | 4,032.90 |
| Financial Reserve: | 34,428.78 | 31,381.31 <br> *Increase Decrease | $-3,047.47$ |

# Ada Multiprocessor Real-Time Kernel 

Hoyt A. Stearns, Jr.
Phoenix, Arizona

Afully pre-emptive, priority driven, multiprocessing real-time kernel is required in certain applications (process control, in this case). After evaluating several schemes, I decided that the Adarendezvousmethod is simple, straightforward, and elegant. Ada multitasking will work on any configuration of multiple processors that can communicate, or on a single processor. A single task can be localized to a particular processor, or can be distributed among many.

The listing is the kernel for a system with three processors. In the actual implementation, one of the three processors isofa completely different type, which is handledbyhavingtwo code fields in each Forth word. All processors share the same memory.

Having a processor of a different type added some complications in the listing not normally necessarysuch as the constant wS for word size-since the processors were of different addressability.

The kernel works fineon a single-processor system, in which case all references to semaphoresmayberemoved.

Memory is allocated from the top down, starting
at the contents of variable RAMPTR. First the system variables are allocated, then task control blocks (TCB's), as tasks are registered to the systemwith REGIS.EachTCB has three link fields, one for each processor,soatask may beregistered tobescheduled among one to three processors.

Although this kernel implements the Ada rendezvous system, the syntax is, of course, different.

The Ada selective-wait structure is invoked in this system with the word SELECT (Sbit_mask timeout_value -

Ada :in message, or where to copy the data if it is an Ada :out message.

The sender parameter is the address of the caller's TCB, and is used as a parameter to the word RELEASE, which is equivalent to the AdaEND_SELECTconstruct.

Ada entry calls are implemented here with theword CALL(Smsg...timeout_value entry_\# TCB_addr - msg... status) wheretimeout_value is how many clock ticks to wait for receipt of the message before returning with \#call_expired status. The other parameters are which

> Ada's multitasking works on multiple processors that can communicate, or on a single processor.
sender \&msg entry_\#), where each 1 bitin bit_mask representsan Adaentry that is open (ADA ACCEPT) and the timeout_value is how long in clock ticks to wait for a messageononeof theopen entries (ADA DELAY). The entry_\# return parameter is which entry the message camein on, or the totalnumber ofentries on this task if it times out. The \&msg is where to get data if this is an
entry on which task to call. The message parameters must be explicitly dropped after the call, since CALL has no way of knowing how big the message is.

Otherfunctionsavailable are pure DELAY and IXMIT, which sends a message without stalling the caller (mainly used to send messages from interrupts). In Ada, interrupt messagesare received by tasks just as if

Ada listing.
they werefromanother task. Improper structuring of tasks in a system may easily result in deadlocks. The word DEDLOK may be invoked periodically to break any deadlocks by returning the parameter \#BROKEN to one of the CALL's involved. This shouldn't be necessary in a properly programmed embedded system.

There should be a MAS -TERtask-registered toonly one of the processors in the system-which registersthe rest of the tasks, then calls slave tasks on the other processors, which start up their own schedulers.

Interrupts were handled in the target system by revectoring NEXT in the variable NP (next pointer) on a particular processor to highlevel code to post an IXMIT message, then revectoring NEXT back to normal. NEXT may also be vectored for tracing.

Thereisan ancillary task, SYSTAT, which takes a snapshot of theentiresystemand prints a report of taskstatus, message queues, run-time statistics, etc.-but thatis for another article.

```
Scr # 1
    \ load screen real time kernel has063091
    decimal
    variable ramptr sp0 @ 2000 - ramptr ! \ top of tcb space
    (S d addr--)
    : d+! dup >r 2f d+ r> 2! ;
    : umin (S n1 n2--n3) 2dup u> if swap then drop ;
    2 constant ws
    4 constant dws \ word size--addr increments/word
    vocabulary kernel kernel definitions
    2 28 thru
    \ forth definitions
    # 2
        \
        has063091
    : access dup create , does> @ + ;
    : sys dup create , does> @ ramptr @ + ;
    3
    4 \ receiver definitions
    1 constant open 2 constant refus 12864 + constant pused
    64 constant used 32 constant rsemaphore
    0 access rlist ws + access rstate ws + constant rcvrsz
    \ task definitions
    1 constant selbt 2 constant endobt 4 constant delbt
    128 constant stkbt 64 constant suspbt 32 constant tsemaphore
    16 8 or constant proc_lock
    O constant #call_ok 2 constant #broken
    1 constant #refused 3 constant #call_expired 0 constant id
    15
Scr
# 3
    \
    0 sys event
    dws - sys clock dws - sys start_time
    ws 3 * - : up0 [ dup ] literal id + ramptr @ + ;
    4 ws - ramptr @ + constant first_tcb_top
    0 upo !
    (S bits addr--) : set tuck @ or swap ! ;
        : reset tuck @ swap not and swap ! ;
        (S word true false--flag)
        : logic over or -rot xor and 0= ;
        : up@ up0 @ ; : up! up0 ! ;
```

nating committee consisting of two members of the Board of Directors (John Hall and Dennis Ruffer) was appointed. After a search for candidates, three were selected to fill the vacant positions. They are Mike Elola, current board member; Nick Solntseff, southern Ontario FIG Chapter; and Jack Woehr. Since there were insufficient write-in petitions (25) for any other candidate, an election by ballot by the membership is not required. A unanimous vote for the nominees will be cast by the Secretary at the annual meeting of the Board of Directors in November. These candidates will submit personal statements in the November-December issue of Forth Dimensions.

## Issues, Actions and Explanations

Treasurer has prepared review of FIG financial situation. The treasurer has prepared, and the Board has approved, the financial statement presented in this $F D$. In explanation, April 30 is the end of FIG's fiscal year and the figures reflect those dates. The form is similar to a "Statement of Change in Financial Position" changed to reflect the year-ends for 1990 and 1991 with comparison. This form shows Assets and Liabilities, and the differences. Most categories are clear, with the exception of the ${ }^{\text {a }}$ FD Dues Alloc. to future months $F D$ " and "Financial Reserve." "FDDues Alloc to future months $F D^{n}$ is money received by FIG, budgeted to complete a member's year of Forth Dimensions (this is the amount FIG is obligated to put aside to produce fu-

```
Scr #4
    0 \ low level
    1 \ message definitions and call_frame definitions
    2 0 access mlink ws + access dest access cdest
    3 ws + access dest_rcvr access centry
    4 ws + access sender access cdelay dup constant csize
    5 ws + dup constant mdata constant msize
    6 0 : link dup up0 <> if [ rot dup ] literal
    7 + id + [ -rot ] then ;
    8 ws 3* + access nrcvr ws + access priority ws + access tstate
    9 ws + access maxtim ws + access actime dws + access urp
    10 ws + access usp ws + access usp0 ws + access urp0
    11 ws + access uip ws + access unp ws + access totime dws +
    12 access spmin ws + access rpmin ws + access splim ws +
    13 access rplim ws + access tname 2 dws * + access rcvrs
    14 ws + access _dp
    15 ws + constant tcbase_size
Scr # 5
    \ user functions has062991
    1
    2 (S rcvr_addr--) \mlink must be 0 before free
    : rsem_clr rstate rsemaphore swap reset ;
    (S rcvr_addr--) \ wait for semaphore, then set it
    : ?rsem rstate begin dup e rsemaphore and 0= until
        rsemaphore swap set ; \ must be done under lock
    : idlock ( semaphore to lock a processor's task list) ;
        : idunlock ;
        : di ( disable interrupts on this processor) ;
        : ei ( enable interrupts on this processor) ;
        11
        12 : ?tsem ( test and set a task linking semaphore) ;
        13 : tsem_clr ( clear tsemaphore) ;
    14 : sys_stks ( set stack pointers to system area) ;
    15 : 'next (address of "next" in target for NP, the next vector);
Scr # 6
        0\
                                    has062991
        1 (S up -- f, true if activation time)
    : ?actime actime 2@ ( di) clock 2@ ( ei) d- nip 0< ;
        3
    4 (S start_addr number item_size--item_size,tor- end_a start_a)
        5 : <b dup >x * dup >r over + 1 r> ?negate - r> -rot swap ;
        6
        7(S up offset -- size end start) \ setup loop over receivers
        : rbnds over rcvrs + swap nrcvr @ rcvrsz <b ;
        9
    10 (S up bit_mask--)
    11 : sbclr swap 0 rstate
        12 rbnds do over i reset dup +loop 2drop ;
    13 (S rcvr_addr--flag, open with msg)
    14 : ?msg dup dup >r ?rsem dup rstate & open and 0<>
        15 swap rlist @ and r> rsem_clr ;
    has062991
```

```
Scr # 7
    \\
    has062991
    (S data up--)
    : tpush usp dup @ ws - dup rot ! ! ; \ push parm to up task
    3
    4 (S up rcvr__addr--rcvr_number)
    5 : ra>r# swap rcvrs - rcvrsz / ;
    (S up rcvr_number--rcvr_addr)
    : r#>ra rcvrsz * swap rcvrs + ;
    9
10 (S rcvr_addr--)
11 : rcv_msg dup dup ?rsem rlist dup di @ tuck @ swap ! ei
12 -1 swap ! ( rec'd) on rsem_clr ;
13
    (S rcvr_addr--msg_addr)
    : rcv>msg rlist @ ;
Scr # 8
    0 \
                            has062991
        (S rcvr_addr--)
    : mark_used rstate pused swap set ;
    (S msg_addr--)
    : clr_sender_delay sender @ tstate delbt swap reset ;
    (S &tstate--tstate flag) \ True if not...
                            \ ...suspended, tsem=0, stk ok
    ?runnable dup @ swap ?tsem over [ stkbt suspbt or ] literal
        and or 0= ;
    (S --up')
    : robin? up@ dup link @ swap priority @ over priority @
        <> if drop up0 & then ;
    : setnxt (S up--) tsemaphore over tstate ! di up! ei
        clock 2@ start_time 2! ;
Scr # 9
    0\
                            has112590
    (S rsize end_addr rcvr_addr--rsize end_addr rcvr_addr'|false)
        find_msg >r >r r@ swap false swap r> r> do i ?msg if
        nip i swap leave then dup +loop -rot ;
    (S up msg_addr--)
    : msgexi - 2dup sender @ swap tpush 2dup mdata + swap tpush
        dest_rcvr @ swap tpush ;
    (S up rcvr_addr--)
    : gotsy over open sbclr dup mark_used dup rcv>msg
        swap rcv_msg dup clr_sender_delay msgexi ;
    (S up--data)
    : tpop usp dup @ @ ws rot +! ;
```

what I mean by mid-level manager, are project leaders, at all levels of industry; and researchers in R\&D organizations, whether universities or R\&D divisions of larger companies.

How do we entice them?
The primary way will be with articles in all appropriate trade publications about the innovative ways that Forth is being used to solve current problems. Highlighted will be individuals or teams or applications or Forth vendors' products or standards or places where Forth already is used but the fact is generally unknown. The emphasis will be on innovative ideas and innovative people!

Second, by volume we let them know that they have always been surrounded by Forth and that the idea of using it is not novel-in fact, that it is required and that there have always been people near them who are willing to help.

Third, that FIG is one of their conduits for information about Forth, whether it is for literature, training, people, or direction toward vendor products. We will emphasize all the Forth resources available to make them and their projects successful.

How do we do all this? This is a multi-path attack, and one of the places it starts is with you!

1. If you will soon be writing a technical article about your project, you will see an article in the next $F D$ about the style and approach that FIG would like you to add to your article. Horace Simmons has been compiling a list of the publications that will be the vehicles for this endeavor.
```
Scr # 10
    O
    1
    (S rcvr_addr--used_flag)
    : ?used` rstate @-used and ;
    4
    5 (S rsize end_addr rcvr_addr--rsize end_addr rcvr_addr'(false)
    6 : &unused >r over r}>>+2dup u> if begin
        find_msg dup dup if ?used then
        0= until else drop false then ;
    (S up--up rcvr_size rcvr_end_addr rcvr_addr|false)
    : first_msg dup 0 rlist rbnds find_msg ;
    (S up rcvr_size rcvr_and_addr rcvr_addr--up rcvr_addr)
    : acquire_used dup >r &unused dup if r> drop nip nip
        over swap else 2drop drop dup used sbclr r> then ;
Scr # 11
    0\
        has062791
        (S &mlink &rlist--)
        : msg_unlink dup dup >r ?rsem rlist 2dup @ <> if @ 2dup mlink
        @ <> if mlink @ then then swap @ swap ! r> rsem_clr ;
        4
        (S &msg_frame--&rlist) \ precursor to find predecessor to msg
        : msg>rlist dup dest @ swap dest_rcvr @ r#>ra rlist ;
    (S up--)
    : cancel_msg usp @ mdata - dup mlink
        swap msg>rlist msg_unlink ;
    (S up--up )
    : caltxm dup cancel_msg #call_expired over tpush ;
    14
    15 : texi (S --) r> drop up@ uip @ >r ;
```

    Scr \# 12
    \(0 \backslash\)
    (s up--up)
    : seltxm dup open sbclr dup nrcve @ over tpush ;
    (S up state--up )
    : time_activate dup selbt and if drop seltxm else
        endobt and if caltxm then then ;
    (S up state--up true|false)
    : timechk over \(>r\) dup delbt and \(r>\) ?actime and
        if time_activate true else drop false then ;
    (S up--up true 1 up false)
    : inscan first_msg dup if dup ?used if acquire_used else
        nip nip then over swap gotsy true else nip nip then ;
    ```
Scr # 13
    0\
                            has062991
    (S up--up' truelfalse)
    : look dup tstate ?runnable if dup
        if tstate up dup selbt endobt logic
        if swap inscan if nip true else swap timechk then
        else timechk then 0= then else drop true then ;
    (S up--)
    : do_task dup setnxt dup urp @ rp! dup urp0 & rp0 !
        dup usp0 @ spo ! usp @ sp! texi ;
    (S --)
    : sched sys_stks up@ tsem_clr robin?
        begin dup tsem_clr link @until
        do_task ;
    # 14
    \ user functions has062991
    : save_state r> r> up@ uip ! sp@ dws + up@ usp !
        rp@ up@ urp ! >r ;
    (S rcvr_addr--flag)
    : ?refused rstate @ refus and ;
    (S rcvr_addr--last_msg_link_addr)
    : find_end rlist begin dup @ ?dup while nip mlink repeat ;
    9
    10
    11
    12
    13
    14
    15
    Scr # 15
    \ registration
                            has063091
    (S stack_size,words pgm_size,w #rcvr--size,words)
    : tcb_size 0 swap r#>ra swap ws * + swap ws * +
        [ 7\overline{0} ws * 80 + ] literal + ;
    (S stack_size pgm_size #rcvr top_addr--bot_addr)
    : top>bot >r tc\overline{b}_size r> swap - ;
    7
    (S up &string--)
    : get_tname tname 8 cmove ;
    10
    11 (S up--) \ interactive version for testing
    I : get_tname tname dup 8 blank bl word count swap -rot cmove
    13
    14
    15
```

2. If you are on a project that is new and interesting, and you are interested in letting others know and can write about the technical parts, we will find a writer that can reshape it into an article that will complement the technical aspects with general-interest aspects of Forth and will help find an appropriate place to get it published.
3. As articles are published, we will coordinate Forth- and FIG-related advertisement to be placed in those issues along with the articles.

Why are we emphasizing mid-level managers? They are the people who specify or can direct the technical details of a project. They are the people who put together a project team. They are the people who would most likely pale at new innovation without justification. They are the people who make a project succeed or fail. They are the people we have failed to reach. Who of us would complain if Forth were required in your project?

I am always available for comments.
-John Hall
415-535-1294 JDHALL on GEnie

CREATE, CREATE DOES>, and, in some Forths, also VARIABLE, will need to be recoded. As of today, if you are using Forth and you want your application to be compatible with future hardware and system software, you have to refrain from using some very natural Forth mechanisms. This is all very un-Forth-like!

Forth could and should, if only some more people bothered, evolve on the Macintosh. There is no reason why the vast programming utilities of MPW, its many tools, its great multiscrollbar editor, could not become available to the Forth environment. I myself am depending more and more on the MPW environment and, yet, it still lacks the one thing taken for granted in any Forth environment: interactivity-the kind you get by being able to compile small entities and immediately execute and debug them.

I think that if Forth has proved to be a disappointment on the Mac, one has only oneself to blame. With Forth's simple and open "architecture," the compiler is not the limit, the user is.

Conrad Weyns
Bjerkebakken 62D
0756 Oslo 7

## Norway

## U.K. Contest Winners

 Dear Marlin,I have enclosed two of the winning entries in the Forth Programmer competition [see "Letters," last issueEdJ, along with some comments from the entrants.

```
Scr # 16
    0 \
    1
    (S stack size pgm size #rcvr up--next up)
: set_stacks dup >r swap
    r#>ra dup r> swap >r >r + 80 + tuck + 140 + dup re - 1+ r@
    swap erase tuck dup r@ urp0 ! r@ urp ! r@ splim ! 100 - dup
    r@ rplim ! 20 - dup r@ usp !
    r@ usp0 ! r> _dp + r> swap ! ;
    Scr # 17
        \ Link in a new task has111390
    (S up0@--&predecessor)
    : find_predecessor dup >r begin dup link @ dup r@ <> while
        nip repeat r> 2drop ;
    (S &pred up0 &new &link--&pred up0 &new &link link flag)
    : pri<? 2dup link @ dup priority & rot priority @ u< ;
(S &pred up0 &new &link link--)
: do_links rot tuck link ! swap dup >r link !
@ swap dup r> = if 2drop else link ! then ;
(S &new up0 up0@--)
: link_next >r dup r@ find_predecessor swap
        2swap pri<? not if begin nip pri<? over r@ = or
        until then r> drop do_links ;
Scr # 18
    \ New task linking cont'd. has111390
    (S &new up0--)
    : link_task dup @ ?dup if link_next else over dup
        link ! ! then ;
    \ Tcb linking test words
    (S priority--)
    : doit here swap , 0 , up0 link task ;
    (S --)
    : sl up0 @ dup >r begin cr dup u. link @
        dup dup u. r@ = key? or until r> 2drop ;
```

```
Scr # 19
    0\
    1
    (S parm st_ip pri maxt up--)
: set_stuff >r r@ maxtim ! r@ priority ! r@ uip !
    clock 2@ r@ actime 2! suspbt r@ tstate ! r@ spmin on
        r@ rpmin on r@ tpush r@ get_tname 'next unp ! ;
    (S top_adr &tname parm st_ip pri maxt stksz pgm_size #rcvr id
        --top_of_next_tcb up)
    : regis idlock 8 roll over >r
    >r 3dup r> top>bot dup >r set_stacks rd set_stuff
    r@ up0 link_task r> dup r> swap nrevr ! idunlock dup ws -
    swap ;
13
14
15
Scr # 20
    \
    1
    2 (S rcvr_addr call_frame_addr--)
    : m_lin\overline{k}}\mathrm{ mlink dup of}\overline{f}\mathrm{ over ?rsem over di find_end !
        rsem_clr ei ;
    (S call_frame_addr--)
    : set_ctime - cdelay @ dup 0>= if 0 clock 2@ d+
        up@ actime 2! [ delbt endobt or ] literal else
        drop endobt then upd tstate set ;
    (S call_frame_addr--)
    : set_sender up@ swap sender ! ;
    (S #parms--) \ pop in current task
    : trash_parms ws * up@ usp +! ;
Scr # 21
    0\
                            has062991
    1 : setparms (S --) clock 2@ 2dup start_time 2@ d-
        up@ totime d+! start_time 2!
        sp@ up@ splim @ u< rp@ up@ rplim @ u< or
        sp@ up@ usp0 @ u> rp@ up@ urpO @ u> or or
        stkbt and up@ tstate set
        sp@ up@ spmin @ umin up@ spmin !
        rp@ up@ rpmin @ umin up@ rpmin ! ;
        : enter (S --) r> save_state >r setparms ;
11 (S [data..] delay entry# dest_task--[new_data..] ret_parameter)
        12 : call enter 3 trash_parms sp@ [ msize csize - ]
13 literal - dup >r sp! r@ cdest @ r@ centry @ r#>ra dup
14 ?refused if drop #refused up@ tpush else
        15 r@ m_link r@ set_ctime r@ set_sender then r> drop sched ;
10
```

Golden River. More recently, I wrote an implementation of Forth-83 for the Transputer. I started working on the Matching Birthdays problem as a result of Gil's blatant provocation in asserting it was a formidable calculation. My first attempt performed the entire calculation on the stack and ran to six screens of source code. My general thoughts about Forth are that it is a good thing.

Big Number Arithmetic and the Probability of Matching Birthdays
In the editorial of Forth-
write 56 , Gil gave the formula for calculating the probability of finding at least one pair of matching birthdays in a group of $n$ people as $(1-\mathrm{P})$, where P is found by multiplying all the integers in the range (366-n) to 365 inclusive, then dividing by $365 n$ times.

To obtain four decimal places using integer arithmetic, we must multiply by 10,000 and subtract the result from 10,000 . All the multiplication must be performed before the division, giving rise to very big intermediate values. It is only necessary to multiply and divide by single-length numbers, so the algorithms for "short multiplication" and "short division" are used; the product or quotient is written, cell-by-cell, back to the same memory locations that held the corresponding cells of the multiplicand or dividend.

It is interesting to compare the results of this program with those which Gil obtained using the Monte Carlo method. The big num-

```
Scr # 22
    \
    (S up--)
: enable dup >r begin dup tstate suspbt swap reset
    link @ dup r@ = until r> 2drop ;
\S *****
(S --)
: startup id " MASTER" drop 1- find 0= abort" No Master!"
    255 0 30 0 2 first_tcb_top
        regis MASTER
        up@ tpush up@ enable sched ;
```

    12
    13
    14
    15
    Scr \# 23
01
1
(S receiver_specifier 'set or 'reset bit--)
: sr_all_revr rot up@ 0 rstate rbnds rot drop do
3dup $1^{-}$and * i rot execute u2/ rcvrsz +loop 2drop drop ;
5
6
7
8
9
10
11
12
13
14
15
Scr \# 24
\accept has112090
(S delay_parameter 0|selbt --)
: setdel swap dup if dup $0<i f>r$ up@ actime $2 @ r>-1 d-$
else 0 clock 2@ $d+$ then up@ actime 2 ! delbt then
or up@ tstate set ;
(S rcvr_specifier--)
: open_rcvrs ['] set open sr_all_rcvr ;
(S receiver_specifier delay_specifier -- \&sender \&data rcvr\#)
: select
enter 2 trash_parms selbt setdel open_rcurs sched ;
(S delay_specifier -- Dclock)
: delay enter 1 trash_parms 0 setdel sched ;
\ functions
1.
(S sender--flag) \ true if msg not from interrupt (ixmit)
: ?~ix_msg tstate @ [ delbt not ] literal <> ;
4
(S sender--) \ ADA end of critical section
: release enter 1 trash_parms dup ?~ix_msg if \#call_ok
over tpush then tstate endobt swap reset sched ;
(S rcvr_specifier--)
: busy
enter 1 trash_parms ['] set refus sr_all_rcvr sched ;
: unbusy
enter 1 trash_parms ['] reset refus sr_all_rcvr sched ;
15
Scr \# 26
\ Ixmit has063091
1
(S entry_\# dest_task \&msg--status) \ send msg frm interrupt
: ixmit >r 2dup swap r\#>ra dup ?refused
if r> 2drop 2drop \#refused
else rot r@ dest_rcvr ! ( dest_rcvr)
swap r@ dest ! ( dest) r@
[ 0 tstate 0 mlink - ] literal - r@̣
sender ! ( pseudo-sender) r> m_link \#call_ok then ;

```

```

\S Message structure: link, dest, dest_rcvr\#,
pseudo_sender, data... mlink is set
to -1 and ~delbt (fffb) when rec'd. When released
endobt is cleared ( fffg).
The sender field is set so that the
mlink field appears as tstat

# 27

0 \ break
has062991
(S up--up f)
2 : ?breakable dup dup usp @ mdata - mlink @ 1+ swap
tstate @ endobt delbt logic and 0<> ;
5 (S up|false--up'|false)
: >dest
dup if ?breakable swap usp @ mdata - dest @ and then ;
(S up-- up'|false)
: bchase dup dup >r begin over >dest 2dup <> dup
if drop rot drop false -rot over r> link @ dup >r <>
over and then while rot drop swap
repeat 2drop r> drop ;
14 : ppad (S --addr) _dp @ 80 + ;
15

```
```

```
Scr # 25
```

```
```

Scr \# 25

```
```

has063091
1.
3
4
cr
r
6
7
10
11
12
13

```
usually can relate. This has worked very well for us. (As a point of interest, we have had over 12,000 Forth applications distributed world wide.)

Another R\&D session looked long and hard at C. I obviously missed something. All that syntax!

Yet another period of R\&D produced the one-page full-screen editor. We were working on text manipulation at the time, so I lifted some of the code and worked on minimizing it.

I would nothave believed that any sort of screen editor would have fit into 1 K of source code-"packed jumble" or not-especially as the editing facilities offered are quite respectable. The average words per definition worked out to fewer than six, and only an improvement to the word TYPE makes any significant improvement to performance.

I found the competition a worthwhile exercise and, indeed, have imposed the one-screen restriction on several pieces of code since. This approach forces a rethink and a re-work of just about every word used. Perhaps Gordon Charlton has unwittingly opened up a whole new programming technique? I cannot, however, recommend the regular use of the style used in my example! /See Figure Two on page 36.J
—Mike Lake

\section*{PDE Erratum}

Dear Mr. Ouverson,
In my article on PDE screen management ("Add and Delete Screens in PDE,"
(Continued on page 37.)
```

Scr \# 28
\
has063091
\ Break a task deadlock
(S up_to_resume--flag, true if broken)
: break dup bchase ?dup if
over cancel_msg
usp @ [ 0 dest_rcvr mdata - ] literal + @ over tpush
\#broken over tpush
tstate endobt swap reset true else drop false then ;
(S --)
: dedlok up0 @ dup >r begin dup break drop link @ dup
r@ = until r> 2drop ;
12
13
14
15

```
(Editorial, from page 4.)
Forth (50\%)
Program design
Stack operations
Postfix notation
Forth compilation (definitions, conditionals, loops)
Number types and operations
Constants, variables, arrays Dictionary structures
I/O operations
Control structures
3. Forth Programming (20\%)
4. Forth and Professional English (10\%)

\section*{Conscientious} Computing
Omni magazine's May 1991 issue contains a guide to energy-efficient behavior, something many technophiles appreciate as a kind of self-preservation. Among their suggestions related to office work: use machines with controls that keep them from running at full power during work lulls; save your laser printers for output that demands their quality output (they require ten times as much energy as other printers); use a fax without a
thermal print mechanism; use laptop computers, which are more efficient than desktop models; use smaller screens; use recycled paper; and carpool or, better yet (take my word for it), telecommute.

For quantity reprints of
the complete Omni Energy Efficiency Guide, write to Omni Energy Guide, 1965 Broadway, New York, NY 10023.
-Marlin Ouverson
Editor

\section*{eForth Implementations}

Subrouitne Threaded eForth for \(68 \mathrm{~K}, 68 \mathrm{HC} 11\) and 8086, Richard Haskell- \(\$ 25.00\)

\section*{New F-PC Releases}

2102
2106
2111
F-PC V3.55 and TCOM, Tom Zimmer- \(\$ 25.00\) The Forth Course, Richard H. Haskell- \(\$ 25.00\) F-PC User Contributions, 80 files- \(\$ 25.00\)

\section*{Others}

\section*{1002}

1003
1011
2103
4004
4008
4113
9006

eForth Model and 8086 Implementation, Bill Muench and C.H. Ting-\$25.00 8051 eForth, C.H. Ting- \(\$ 25.00\) 68000 eForth, Richard H. Haskell- \(\$ 25.00\) 32-Bit 8086 eForth, R. VanNorman- \(\$ 25.00\) Z80 eForth, Ken Chen- \(\$ 25.00\)
-

Offete Enterprises, 1306 South B Street San Mateo, CA 94402 (415) 574-8250

\section*{BACK IN PRINT!}

\title{
THINKING FORTH A Language and Philosophy for Solving Problems
}

\author{
BY LEO BRODIE
}

\section*{SEE FIG MAIL ORDER FORM IN THIS ISSUE!}

Attention Fohth Authors!
 PROGRAK

To recognize and reward authors of Forth-related articles, the Forth Interest Group has adopted the Author Recognition Program.

\section*{Articles}

The author of any Forth-related article published in a periodical or in the proceedings of a non-Forth conference is awarded one year's membership in the Forth Interest Group, subject to these conditions:
a. The membership awarded is for the membership year following the one during which the article was published.
b. Only one membership per person is awarded in any year, regardless of the number of articles the person published in that year.
c. The article's length must be one page or more in the
. magazine in which it appeared.
d. The author must submit the printed article (photocopies are accepted) to the Forth Interest Group, including identification of the magazine and issue in which it appeared, within sixty days of publication. In return, the author will be sent a coupon good for the following year's membership.
e. If the original article was published in a language other than English, the article must be accompanied by an Engish translation or summary.

\section*{Letters to the Editor}

Letters to the editor are, in effect, short articles, and so deserve recognition. The author of a Forth-related letter to an editor published in any magazine except Forth Dimensions is awarded \(\$ 10\) credit toward FIG membership dues, subject to these conditions:
a. The credit applies only to membership dues for the membership year following the one in which the letter was published.
b. The maximum award in any year to one person will not exceed the full cost of the FIG membership dues for the following year.
c. The author must submit to the Forth Interest Group a photocopy of the printed letter, including identification of the magazine and issue in which it appeared, within sixty days of publication. A coupon worth \(\$ 10\) toward the following year's membership will then be sent to the author.
d. If the original letter was published in a language other than English, the letter must be accompanied by an English translation or summary.

```

    ONE SCREEN FULL SCREEN EDITOR
    Mike Lake
    ```

```

        O VARIABLE KT 5l2 ALLOT : Y R# @ C/L / ; : X R# @ C/L MOD ;
    : RANGE OVER + DUP O< OVER 1023; OR 0= IF SWAP THEN DROP ;
    : MOV R# @ SWAP RANGE R# ! ; : LEFT -1 MOV ; : RIGHT l MOV ;
    : UP -64 MOV ; : DOWN C/L MOV ; : HOME O R# ! ; : >E C/L X - ;
    : LO SCR @ (LINE) ; : AD 0 LO DROP R# @ + ; : LAD Y LO ;
    : Y.L D OVER AT LO TYPE ; : .L Y Y.L ; : CEOL AD >E BLANKS .L ;
    : .ED 16 O DO I Y.L LOOP CR SCR ? ; : CRLF X MINUS R# +! DOWN ;
    : CL LAD BLANKS .L ; : >S UPDATE SCR +! .ED ; : N l >S ;
    : PUT LAD PAD SWAP CMOVE ; : !SCR HOME AD 1024 BLANKS .ED ;
    : GET PAD LAD CMOVE .L ; : TOGG KT l TOGGLE ; : P -1 >S ;
    : CLR BL AD >E l- 2DUP OVER l + ROT ROT CMOVE + C! .L ;
    : !K DUP AD KT @ IF DUP DUP 1+ >E l- -CMOVE C! .L ELSE C! DUP
        EMIT THEN RIGHT ; : DEL LEFT CLR ; : UNDO EMPTY-BUFFERS .ED ;
    : SET CFA SWAP 2* KT + 2+ ! ; : SETS 256 0 DO I , !K SET LOOP ;
    : GO BEGIN X Y AT KEY DUP 2* KT + 2+ @ EXECUTE O< UNTIL ; SETS
    : ED FLUSH SCR ! HOME .ED GO UPDATE ; : TAB 8 X OVER MOD - MOV ;
    ```

```

    CORE WORDS
    ```

```

    KT (...ADDR) FIRST 2 BYTES=FLAG 256*2 BYTES FOR KEY ACTIONS
    X ( ...N) X POSITION OF CURSOR
    Y ( ...N) Y POSITION OF CURSOR
    RANGE ( CURSOR N ...CURSOR) LEAVES CURSOR+N OR ORIGINAL CURSOR POS
    MOV (N ...) MOVES CURSOR BY N
    \E (...N) CHARACTERS FROM CURSOR TO END OF LINE
    LO ( N...ADDR C/L) ADDRESS OF START OF EDITING LINE N
    AD ( ...ADDR) ADDRESS OF CURSOR
    LAD ( ...ADDR C/L) ADDRESS OF START OF CURRENT LINE
    Y.L ( N...) LOCATE AND PRINT LINE N
    .L ( ...) PRINT CURRENT LINE
    .ED ( ...) PRINT EDITING SCREEN
    >S ( N ...) MOVE ON N SCREENS
    GO ( ...) MAIN KEY LOOP ;
    ED (N ...) INVOKES EDITOR USING SCREEN N
    SET ( N PFA ...) SET KEY N TO PERFORM WORD ACTION
                        USE AS IN... 13' CRLF SET
        SETS ALL KEYS TO DEFAULT.. (STORE KEY PRESS)
    ```

    WORDS TO ASSIGN TO REQUIRED KEY PRESS

    CLR \(* * * * * * * * * * * * * * * * * * * * * * * * * *=\) CLEAR CHARACTER UNDER CURSOR
    DEL * ALL THESE WORDS ARE \(*=\) CLEAR CHARACTER TO LEFT OF CURSOR
    CEOL * ASSIGNED TO KEYPRESSES.* = CLEAR FROM CURSOR TO END OF LINE
    CL * ADDING EXTRA ACTIONS * = CLEAR LINE
    \(!S C R \quad *\) IS A DODDLE. A NEW \(\quad *=\) CLEAR EDITING SCREEN
    UNDO * ACTION HAS THE KEY \(\quad *=\) RESTORE EDITING SCREEN TO ORIGINAL
    N * AVAILABLE ON THE STACK \(*=\) NEXT SCREEN
    P * AND MUST LEAVE A VALUE * = PREVIOUS SCREEN
    PUT \(\quad\) * ON THE STACK. A MINUS \(*=\) PUT CURRENT LINE TO PAD
    GET * VALUE SIGNALS TO \(\quad\) * GET CURRENT LINE FROM PAD
    TOGG * EXIT THE EDITOR. \(\quad *=\) TOGGLE INSERT/OVERWRITE MODES
    ! K * ************************ = STORE KEY PRESS (DEFAULT ACTION)
    MINUS \(\quad=\) FORTH WORD USED TO EXIT EDITOR
    LEFT RIGHT UP DOWN HOME CRLF TAB = MOVE CURSOR

    INSTRUCTIONS . . THE ABOVE EXPECTS THE FOLLOWING COMMON FORTH WORDS.
        -CMOVE ( ADDI ADD2 COUNT ...) AS CMOVE BUT FROM HIGH TO LOW MEMORY.
        AT ( \(\mathrm{X} Y \ldots\)...) LOCATE CURSOR AT X Y.
    LOAD THE SCREEN AND ASSIGN ACTIONS TO THE REQUIRED KEY PRESSES.
    13 , CRLF SET 8 , TAB SET \(27^{\prime}\) MINUS SET ETC.
    THE CONFINES OF ONE SCREEN RESULTS IN THE PACKED JUMBLE ABOVE
    HOWEVER DEFINITIONS ARE VERY SMALL AND EASY TO FOLLOW.

FD XIII/1), I supplied words that would enter the PDE editor automatically if a load error occurred-(ERROR), (WHERE), etc. on screen 11. At the time, I thought these words crashproof. I spoke too soon. Since then, I've discovered a rather obscure bug that will crash the system like a fireworks display.

This happens when you first load Forth from the CP/M or DOS command line and decide to provide as an argument a filename for Forth to open. Should you misspell the filename or give one that does not exist, an open-file error occurs, the PDE editor is entered, and crash.

Yet the same open error done from the Forth command line results in the expected error message and no problems.

The trouble starts back when you take the pristine Kernel83 and add the extensions and assembler. As a kindness, the load screen ends by doing the SAVESYSTEM for you. Because of this, the variable BLK is saved as having a value of one, as this is the load screen number.

When you first load in Forth, I found that the boot routine is providedby START, which in turn calls DEFAULT. DEFAULT searches CP/M's DMA (on my computer) for a filename to open. Normally, this happens smoothly, and the sequence exits through QUIT. Butin an open file error, it exits through ERROR. Unfortunately, nowhere is BLK reset to zero beforehand. Now (ERROR)
in my PDE uses a non-zero BLK as a flag that the error occurred during a load, so it enters the editor. This causes the system to crash.

However, in setting up the Forth command line, QUIT early on resets BLK to zero, so an error there displays the error message normally.

You can solve the problem at several levels.
1. Simplest is to redo the SAVE-SYSTEM from the Forth command line. This ensures that BLK is zero.
2. You can rewrite START to include 0 BLK ! before calling DEFAULT. It's easy to patch in, as BOOT is a deferred word. Then, future additions to the program using this load screen to save the system won't cause problems.
3. Ultimately, correcting DEFAULT to reset BLK and recompiling would guarantee that this gremlin won't return to haunt you.

Yours truly,
Walter J. Rottenkolber
P.O. Box 936

Visalia, California 93279

\section*{Answer to a Dream Dear Sir,}

In answer to John G. Derrickson's letter requesting a low-cost, better Forth ("Dreaming That It's Forth," FD XIII/2), I would like to recommend Upper Deck Forth, available from Upper Deck Systems (P.O. Box 263342, Escondido, California 92026).

I have found it to be exceptionally fast and easy to use. It is a text-based system and incorporates such
\$ Contest Announcement \$

\section*{Call for Papers!}

Forth Dimensions is sponsoring a contest to encourage authors of articles about Forth and
"Object-Oriented Programming"
\(\begin{array}{ll}\text { 1st prize: } & \$ 500 \\ \text { 2nd prize: } & \$ 250 \\ \text { 3rd prize: } & \$ 100\end{array}\)

Deadline: September 16, 1991
See editorial in issue XIII/1 for details!
features as mouse control for the full-screen editor, direct access to DOS functions from the terminal input stream, full memory usage, disas-
during compilation, so determination of the exact location of the interrupt routine was made more difficult. This was solved by readjust-

\section*{I would not have believed that any sort of screen editor would have fit into \\ \(1 K\) of source code.}
sembler, assembler, and support for generating headerless, turnkey applications.

The only difficulty I encountered when using Upper Deck was while writing an assembler interrupt routine that required absolute addressing. The dynamic memory allocation readjusted segment and offset addresses
ing the code within the interrupt routine after compiling.

I highly recommend this MS-DOS Forth system.

Sincerely, Glen \(F\). Ingle
1585 Samedra Street
Sunnyvale, California 94087

\section*{Best}

In the February column I promised not to delay so long between recaps of guest conferences. Before making good on that promise, I want to call your attention to the improved appearance of the GEnie Forth RoundTable. Topics and entire categories are much cleaner. The rat's nests of messages, many of which my ForthNet message ports created, are largely gone. All this can be attributed to our newest SysOp, Elliott Chapin. Elliott came on board knowing he would have to pick up after the likes of me, so he is either one heck of a good guy or a glutton for thankless jobs. I happen to believe the "nice guy" theory will stand the test of time.

Here, in his own words, is Elliott's introduction: "I was born in 1942 in New York City, just five days before my better known half-nephew Harry. A couple of years later, my father James Chapin, an established artist, moved the family out to rural northwest New Jersey. Now I have a family of my own in Toronto. I studied math at Princeton (starting my acquaintance with computers there) and Columbia in the 60 s . Since then, I have found various kinds of work in the arts and education, but I also drive a taxi when necessary. I am enjoying the learning oppor-
tunities available on the GEnie's Forth RoundTable, while doing "slash and stash" messagebase edits as the latest assistant SysOp." (Note: ELLIOTT.C is Elliott's e-mail address.)

Welcome to the funhouse, Elliott.
* * *

It is true that revisits with our guests in this column never capture the intimacy of the Real-Time Conference where you pose the question, usually sparked by another comment, but they do serve to remind attendees of poignant exchanges and to let non-participants know what they missed. Transcripts of the complete conferences are archived in the GEnie Forth RoundTable's Software Library 1. Please make a note, as you scan this column, to download those you find of value, because the conferences as presented in this column are reduced to the guests' opening remarks.

Guest conferences reviewed here include Jef Raskin, "What Happened to the Cat?"; Alan Furman, "ACMSIGForth Update"; Bill Muench, "Embedded with eForth"; Guy Kelly, "Forth and Industry"; Dean Sanderson, "Addressing Management Concerns" (regarding use of Forth); Roy Martens and Glen Haydon, "MVP is Alive and, well..."; Charles Johnsen, "Mutable

Instruction Set Computers."
Jef Raskin, creator of the Mac and Canon Cat, asked "What happened to the Cat?" in reference to the marketing failure of the Canon Cat. This initial discussion rapidly gave way to a more thorough look at what the interface between computer and man should be-certainly an area, it can be safely argued, in which Jef Raskin is one of the ranking authorities, if not the authority. 10/17/90

JefRaskin: I am pleased to be here, and I think that it might be best to just stant with questions. When I see the directions the questions take, I can make more extended comments.

Cool CATJAX: First I were a Programmer... Now I am a Project Engineer... Jef, how long do I have to wait before I start having Ideas? :-)

Jef Raskin: Thank you Mr. Cat. My real interest these days is in interfaces. For example, windows are dumb, icons wrong, and mouses a nuisance.

Dennis Ruffer: Sticking to that "heresy," let me ask how/ why did you start the Mac project?

Jef Raskin: I can't stand the usual hand-to-mouse exist-
ence. The real question is why we have to bother with all those "features" when we are trying to get something done. I started the Mac project in 1979. I have learned something in the intervening decade. In those days I was (correctly, I think) inspired by the work at PARC. But one mustn't confuse "better" with "good" The Mac was better, but one can go a lot better.

WilBaden: Please tell us what you think happened to the CAT.

JefRaskin: It was, I have been told, a victim of internecine warfare within Canon and a lack of marketing support. I am sure of the second hypothesis.

Gary Smith: There was excitement here after John Bumgarner was in conference with Steve Roberts about the possibility of a portable, personal Forth laptop then bingo/the CAT is on sale in Service Merchandise. That's a lot of smoke and mirrors. Any further comment?

Jef Raskin: They are, by the way, still available. A guy, David Wing, in San Diego has kept track of them. Canon did not know what it had, and tried to sell it through its electronictypewriter division. This was a mistake. Then they tried selling it for twice the design price. That was a mistake. We did make some prototype portables, I have one here and it works (and runs Forth), but our Vulture Capitalists had lost their stomachs (and minds).

Dr. Alan Furman, independent software and electronics consultant, presented an
update on a sister onganization, SIGForth ACM. As a principle of this group's genesis, Dr. Furman was more than qualified to do so. 11/15/90

I should begin by acknowledging the tremendous contribution of George Shaw, who has been the driving force in SIGForth from the beginning. My main role was as a crusader, in late 1988, for some kind of organization that would specialize in professional issues and the commercialization of Forth. FIG's constituency is a mixture of professionals and hobbyists. And many of those professionals didn't care who admired Forth; they were in a position of having discretion over programming language. Whenever you have a group of people together, you have to stick to things that they are all interested in at the same time, which in FIG's case is Forth technology and sharing of ideas.

I will close with some exciting news. The Forth community in Leningrad would like to put on a conference either Fall 1991 or Spring 1992, which will be run in cooperation with SIGForth.

Bill Muench, president of Ontologic and coauthor of eForth, discussed eForth, the .asm Forth kernel for the 90s. 12/13/90
eFORTH is derived from my commercial version bFORTH. Both are ANS Forth subsets. I designed eFORTH for ease of implementing new systems, to easily fit in 8 K bytes, to have a minimum of machine code and some debugging tools. My debug tools are TX!, the transmit primitive, . S,DUMP, andwORDS. I use a simple metacompiler rather than MASM, which
means I first write an assembler. That is how I get to know the processor.

After the new system is running, I optimize it, first loading the assembler, then new code words. When working, they are added to the kernel, repeat 'til done.

I use my host file server, bHOST, to upload source text files to the target system. MASM was chosen by Dr. C.H. Ting as a vehicle to communicate eFORTH. It is commonly available and on a widely used platform, the PC. But any assembler will do. Possible alternatives are spreadsheets or word processors with macros.

Guy Kelly expanded on bis November 1990 FORML theme, "Forth in Industry." Guybas a vast arrayofForthdriven industrial projects to bis credit to support bis views that Forth is an excellent platform for such endeavors. 1/17/91

Experiences as a Forth producer, user, and teacher convince me that it's almost impossible to get a professional programmer to use Forth and almost impossible to discourage a professional engineer from using Forth.

Now that Laxen, Perry, and Zimmer have handled the "high end" and Dr. Ting is attacking the "low end," how about the "Forth as a hardware development tool" area?

Dean Sanderson, software engineer with FORTH Inc., discussed "Addressing Management Concerns over use of Forth as an Applications Platform." 2/21/91

Because of the power that Forth has given us (to keep projects small and quick), we have been able to avoid learning what others have had to about software devel-
opment. Those who have grown up with Forth do management by intuition. We have trouble communicating with those who've been successful using Fortran, C, or assembler. It's as if we speak different languages. As projects escalate, we find we have not killed the dragon, only maimed him. As we ready for battle, we find our pride has left us with few new weapons.

For Forth to survive as a respected language, it must prove its adaptability and change enough to support the concerns of management. These include: Integration, Maintenance, Documentation, Declining cost, Q.A., Configuration, and Scheduling.

Though we've started late, we can survive by capitalizing on what others have learned.

This conference's invited coguests were Roy Martens, president of Mountain View Press, with Glen Haydon, author of MVP Forth. Glen discussed the fate of MVP in the 1990s market. 3/4/91

Thank you for inviting Mountain View Press. We are alive, but changes are in store. Roy has sold his home and is moving to San Francisco. He is taking an apartment April 1. We have agreed for me to take over Mountain View Press. As some of you know, Phil Koopman, has taken a new job which requires him to divest himself of WISC Technologies. Epsilon Lyra is my company. I am bringing WISCTechnologies in as a division. Roy and I agree to doing the same with Mountain View Press.

I plan on carrying the public-domain versions of Forth which I included in my new edition of All About Forth: fig-FORTH, MVP-

FORTH, F83, and F-PC. I will also carry available documentation.

We will also carry professional implementations from vendors who will make appropriate distributor's agreements. For the present, we will keep the same mailing address and phone numbers. We look forward to continuing our support of Forth users.

Charles Jobnsen, President of MISC, Inc. (Mutable Instruction Set Computer, formerly Minimum Instruction Set Computer) was our invited guest. Cbarles was joined by Dr. David Fox, MISC's Software Engineer as the two discussed this new silicon engine. 4/18/91

Thanks for the welcome. I wanted to speak about the Silicon Palimpsest this evening. That is the processor without a fixed instruction set. MISC originally stood for Minimum Instruction Set Computer. It was a tiny company (and still is), set up to create a Forth stack engine.

We wanted to do something different from Novix. We wanted a processor for embedded control, not desktop computing. That guided our efforts and our business. Today we have changed our name to Mutable Instruction Set Computer, Inc., because we have an even better idea. By using FPGA (field-programmable gate arrays), we believe we can create a processor with a mutable instruction set. The advantage of a mutable instruction set is that custom instructions can be designed for improved performance.
-Gary Smith
GARY-S on GEnie

Please send updates, corrections, additional listings, and suggestions to the Editor.

\section*{Forth Interest Group}

The Forth Interest Group serves both expert and novice members with its network of chapters, Forth Dimensions, mail-order services, and on-line activities. For membership information, or to reserve advertising space, contact the administrative offices:

Forth Interest Group
P.O. Box 8231

San Jose, California 95155
408-277-0668
Fax: 408-286-8988

\section*{Board of Directors}

John Hall, President
C.H. Ting, Vice-President

Mike Elola, Secretary
Dennis Ruffer, Treasurer
Wil Baden
Jack Brown
David Petty
Dennis Ruffer

Founding Directors William Ragsdale Kim Harris
Dave Boulton Dave Kilbridge

\section*{In Recognition}

Recognition is offered annually to a person who has made an outstanding contribution in support of Forth and the Forth Interest Group. The individual is nominated and selected by previous recipients of the "FIGGY." Each receives an engraved award, and is named on a plaque in the administrative offices.

1979 William Ragsdale 1980 Kim Harris 1981 Dave Kilbridge 1982 Roy Martens 1983 John D. Hall 1984 Robert Reiling 1985 Thea Martin 1986 C.H. Ting 1987 Marlin Ouverson 1988 Dennis Ruffer 1989 Jan Shepherd 1990 Gary Smith

\section*{ANS Forth}

The following members of the ANS X3J14 Forth Standard Committee are available to personally carry your proposals and concerns to the committee. Please feel free to call or write to them directly:
\begin{tabular}{ll} 
Gary Betts & Charles Keane \\
Unisyn & Performance Pkgs., Inc. \\
301 Main, penthouse \#2 & 515 Fourth Avenue \\
Longmont, CO 80501 & Watervleit, NY 12189-3703 \\
303-924-9193 & \(518-274-4774\) \\
& \\
Mike Nemeth & George Shaw \\
CSC & Shaw Laboratories \\
10025 Locust St. & P.O. Box 3471 \\
Glenndale, MD 20769 & Hayward, CA 94540-3471 \\
301-286-8313 & 415-276-5953 \\
Andrew Kobziar & \\
NCR & David C. Petty \\
Medical Systems Group & Digitel \\
950 Danby Rd. & Cambridge, MA 02140-2311 \\
Ithaca, NY 14850 & \\
607-273-5310 & \\
Elizabeth D. Rather & \\
FORTH, Inc. & \\
111 N. Sepulveda Blvd., & \\
suite 300 & \\
Manhattan Beach, CA 90266 \\
213-372-8493 &
\end{tabular}

213-372-8493

\section*{Forth Instruction}

Los Angeles-Introductory and intermediate three-day intensive courses in Forth programming are offered monthly by Laboratory Microsystems. These hands-on courses are designed for engineers and programmers who need to become proficient in Forth in the least amount of time. Telephone 213-306-7412.

\section*{On-Line Resources}

To communicate with these systems, set your modem and communication software to 300/1200/2400 baud with eight bits, no parity, and one stop bit, unless noted otherwise. GEnie requires local echo.

GEnie
For information, call 800-638-9636
- Forth RoundTable
(ForthNet*)
Call GEnie local node, then
type M710 or FORTH
SysOps:
Dennis Ruffer
(D.RUFFER),

Scott Squires
(S.W.SQUIRES),

Leonard Morgenstern
(NMORGENSTERN),
Gary Smith (GARY-S)
- MACH2 RoundTable Type M450 or MACH2 Palo Alto Shipping Company SysOp: Waymen Askey
(D.MILEY)

BIX (ByteNet)
For information, call 800-227-2983
- Forth Conference Access BIX via TymNet, then type j forth
Type FORTH at the : prompt
SysOp:
Phil Wasson (PWASSON)
- LMI Conference

Type LMI at the : prompt
LMI products
Host:
Ray Duncan
(RDUNCAN)

CompuServe
For information,
call 800-848-8990
- Creative Solutions Conf.

Type !Go FORTH
SysOps: Don Colburn,
Zach Zachariah, Ward
McFarland, Jon Bryan,
Greg Guerin, John
Baxter, John Jeppson
- Computer Language

Magazine Conference
Type !Go CLM
SysOps: Jim Kyle, Jeff
Brenton, Chip
Rabinowitz, Regina Starr Ridley

Unix BBS's with forth.conf (ForthNet* and reachable via StarLink node 9533 on TymNet and PC-Pursuit node casfa on TeleNet.)
- WELL Forth conference Access WELL via CompuserveNet or 415-332-6106 Fairwitness: Jack Woehr (jax)

PCBoard BBS's devoted to Forth
(ForthNet*)
- British Columbia Forth Board 604-434-5886 SysOp: Jack Brown
- Grapevine 501-753-8121 to register 501-753-6859 StarLink node 9858 SysOp: Jim Wenzel
*ForthNet is a virtual Forth network that links designated message bases in an attempt to provide greater information distribution to the Forth users served. It is provided courtesy of the SysOps of its various links.
- Real-Time Control Forth Board
303-278-0364
StarLink node 2584 on TymNet
PC-Pursuit node coden on TeleNet
SysOp: Jack Woehr

Other Forth-specific BBS's
- Laboratory Microsystems, Inc.
213-306-3530
StarLink node 9184 on TymNet
PC-Pursuit node calan
on TeleNet
SysOp: Ray Duncan
- Knowledge-Based

Systems
Supports Fifth
409-696-7055
- Druma Forth Board

512-323-2402
StarLink node 1306 on TymNet
SysOps: S. Suresh, James
Martin, Anne Moore

Non-Forth-specific BBS's with extensive Forth libraries
- DataBit

Alexandria, VA
703-719-9648
PCPursuit node dcwas
StarLink node 2262
SysOp: Ken Flower

International Forth BBS's
- Melbourne FIG Chapter (03) 809-1787 in Australia
61-3-809-1787
international
SysOp: Lance Collins
- Forth BBS JEDI

Paris, France
3336431515
7 data bits, 1 stop, even parity
- Max BBS (ForthNet*)

United Kingdom
0905754157
SysOp: Jon Brooks
- Sky Port (ForthNet*) United Kingdom 44-1-294-1006 SysOp: Andy Brimson
- SweFIG

Per Alm Sweden
46-8-71-35751
- NEXUS Servicios de Informacion, S. L.
Travesera de Dalt, 104-106, Entlo. 4-5
08024 Barcelona, Spain
+ 3432103355 (voice)
+ 3432147262 (modem)
SysOps: Jesus
Consuegra, Juanma
Barranquero
barranenexus.nsi.es (preferred)
barran@nsi.es
barran (on BIX)

This list was accurate as of August 1991. If you know another on-line Forth resource, please let me know so it can be included in this list. I can be reached in the following ways:

Gary Smith
P. O. Drawer 7680

Little Rock, Arkansas 72217
Telephone: 501-227-7817
Fax (group 3): 501-228-9374
GEnie (co-SysOp, Forth RT and Unix RT): GARY-S
Usenet domain.: uunet!ddi1!Irark!glsrk!gars

\section*{FIG Chapters}

The Forth Interest Group Chapters listed below are currently registered as active with regular meetings. If your chapter listing is missing or incorrect, please contact Anna Brereton at the FIG office's Chapter Desk. This listing will be updated regularly in Forth Dimensions. If you would like to begin a FIG Chapter in your area, write for a "Chapter Kit and Application."

Forth Interest Group
P.O. Box 8231

San Jose, California 95155

\section*{U.S.A.}
- Alabama

Huntsville Chapter
Tom Konantz
(205) 881-6483
- ALASKA

Kodiak Area Chapter
Ric Shepard
Box 1344
Kodiak, Alaska 99615
- ARIZONA

Phoenix Chapter
4th Thurs., 7:30 p.m.
Arizona State Univ.
Memorial Union, 2nd floor
Dennis L. Wilson
(602) 381-1146
- CALIFORNIA

Los Angeles Chapter
4th Sat., 10 a.m. Hawthorne Public Library 12700 S. Grevillea Ave. Phillip Wasson
(213) 649-1428

\section*{North Bay Chapter}

2nd Sat.
12 noon tutorial, 1 p.m. Forth 2055 Center St., Berkeley
Leonard Morgenstern (415) 376-5241

\section*{Orange County Chapter}

4th Wed., 7 p.m.
Fullerton Savings
Huntington Beach
Noshir Jesung (714) 842-3032
Sacramento Chapter
4th Wed., 7 p.m.
1708-59th St., Room A
Bob Nash
(916) 487-2044

\section*{San Diego Chapter}

Thursdays, 12 Noon
Guy Kelly (619) 454-1307

Silicon Valley Chapter 4th Sat., 10 a.m.
Applied Bio Systems
Foster City
John Hall
(415) 535-1294

Stockton Chapter
Doug Dillon (209) 931-2448
- COLORADO

Denver Chapter
1st Mon., 7 p.m.
Clifford King (303) 693-3413
- FLORIDA

Orlando Chapter
Every other Wed., 8 p.m.
Herman B. Gibson
(305) 855-4790
- GEORGIA

Atlanta Chapter
3rd Tues., 7 p.m.
Emprise Corp., Marietta
Don Schrader (404) 428-0811
- ILLINOIS

Cache Forth Chapter
Oak Park
Clyde W. Phillips, Jr.
(708) 713-5365

Central Illinois Chapter
Champaign
Robent Illyes (217) 359-6039
- INDIANA

Fort Wayne Chapter
2nd Tues., 7 p.m.
I/P Univ. Campus
B71 Neff Hall
Blair MacDermid
(219) 749-2042
- IOWA

Central Iowa FIG Chapter
1st Tues., 7:30 p.m.
Iowa State Univ.
214 Comp. Sci.
Rodrick Eldridge
(515) 294-5659

Fairfield FIG Chapter
4th Day, 8:15 p.m.
Gurdy Leete (515) 472-7782
- MARYLAND

MDFIG
3rd Wed., 6:30 p.m.
JHU/APL, Bldg. 1
Parsons Auditorium
Mike Nemeth
(301) 262-8140 (eves.)
- MASSACHUSETTS Boston FIG
3rd Wed., 7 p.m.
Bull HN
300 Concord Rd., Billerica
Gary Chanson (617) 527-7206
- MICHIGAN

Detroit/Ann Arbor Area
Bill Walters
(313) 731-9660
(313) 861-6465 (eves.)
- MINNESOTA

MNFIG Chapter
Minneapolis
Fred Olson
(612) 588-9532
- MISSOURI

Kansas City Chapter
4th Tues., 7 p.m.
Midwest Research Institute
MAG Conference Center
Linus Orth (913) 236-9189
St. Louis Chapter
1st Tues., 7 p.m.
Thornhill Branch Library
Robert Washam
91 Weis Drive
Ellisville, MO 63011
- NEW JERSEY

New Jersey Chapter
Rutgers Univ., Piscataway
Nicholas G. Lordi
(908) 932-2662
- NEW MEXICO Albuquerque Chapter 1st Thurs., 7:30 p.m.
Physics \& Astronomy Bldg
Univ. of New Mexico
Jon Bryan (505) 298-3292
- NEW YORK

Long Island Chapter
3rd Thurs., 7:30 p.m.
Brookhaven National Lab
AGS dept.,
bldg. 911, lab rm. A-202
Irving Montanez
(516) 282-2540

Rochester Chapter
Monroe Comm. College
Bldg. 7, Rm. 102
Frank Lanzafame
(716) 482-3398
- OHIO

Columbus FIG Chapter
4th Tues.
Kal-Kan Foods, Inc.
5115 Fisher Road
Terry Webb
(614) 878-7241

Dayton Chapter
2nd Tues. \& 4th Wed., 6:30 p.m.
CFC
11 W. Monument Ave. \#612
Gary Ganger (513) 849-1483
- PENNSYLVANIA

Villanova Univ. Chapter
1st Mon., 7:30 p.m.
Villanova University
Dennis Clark
(215) 860-0700
- TENNESSEE

East Tennessee Chapter
Oak Ridge
3rd Wed., 7 p.m.
Sci. Appl. Int'l. Corp., 8th Fl.
800 Oak Ridge Turnpike
Richard Secrist (615) 483-7242
- TEXAS

Austin Chapter
Matt Lawrence
PO Box 180409
Austin, TX 78718
Dallas Chapter
4th Thurs., 7:30 p.m.
Texas Instruments
13500 N. Central Expwy. Semiconductor Cafeteria Conference Room A
Warren Bean (214) 480-3115

\section*{Houston Chapter}

3rd Mon., 7:30 p.m.
Houston Area League of
PC Users (HAL-PC)
1200 Post Oak Rd.
(Galleria area)
Russell Harris
(713) 461-1618


\title{
FORML CONFERENCE
} for professional Forth programmers, managers, vendors, and users.

Following Thanksgiving, November 29-December 1, 1991

> Asilomar Conference Center Monterey Peninsula overlooking the Pacific Ocean Pacific Grove, California U.S.A.

\section*{Theme: Simulation and Robotics}

Papers are invited that address relevant issues in the development and use of Forth in simulation and robotics. Virtual realities, robotics, and graphical user interfaces are topics of particular interest. Papers about other Forth topics are also welcome.
Attendees are invited to enter a robot in a robotics contest where the robot solves a puzzle.
Mail abstract(s) of approximately 100 words by September 1, 1991 to FORML Conference, Forth Interest Group, P.O. Box 8231, San Jose, CA 95155.

Completed papers are due November 1, 1991.

\section*{Conference Registration}

Registration fee for conference attendees includes conference registration, coffee breaks, and note-book of papers submitted, and for everyone rooms Friday and Saturday, all meals including lunch Friday through lunch Sunday, wine and cheese parties Friday and Saturday nights, and use of Asilomar facilities.
Conference attendee in double room- \(\$ 350\) • Non-conference guest in same room- \(\$ 200\) • Children under 17 years old in same room- \(\$ 140\) - Infants under 2 years old in same room-free - Conference attendee in single room- \(\$ 450\) Forth Interest Group members and their guests eligible for ten percent discount on registration fees.
Register by calling the Forth Interest Group business office at (408) 277-0668 or writing to: FORML Conference, Forth Interest Group, P.O. Box 8231, San Jose, CA 95155.

\title{
FORTH
}

INTERESTGROUP```


[^0]:    Kevin Haddock's first Forth-a FIG model adapted from CP/M-came from an OASIS users group. After switching himself and his customers over to MS-DOS andUnix, he worked in a network access control system done in native polyFORTH, a spinoff of the King Kahlid Airport in Saudi Arabia. On the side, he cannibalized some Commodore 64s and built a native fig-FORTH system on the C-64's motherboard, using a small daughterboard; this provided SCSI, RS-232, and Centronix capabilities almost entirely in software. Lately he has been finishing up a Forth written in C , not feeling that previous efforts to do this really captured the essence of Forth. He says, "Usually the inner interpreters were unconventional (e.g., case statements, jump tables, etc.) and provided little portability or comfort to Forthers seeking familiarity in an alien (e.g., Unix) environment."

