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This program generates fractal landscapes, with height-based coloring and hidden-surface elimination. Fractals are used to describe various geometrical shapes, especially natural phenomena, that are not strictly one, two, or three-dimensional.

Our new columnist discusses the growing importance of 32-bit machines, and presents a proven scheme for implementing Forth on them. This method allows programs to run unchanged on either 16-bit or 32-bit machines without penalizing the newer architectures.

Michael Ham interviews the president of FORTH, Inc. about the history of Forth and of the first company to deal in Forth systems and applications. She speaks frankly of successes, lean times, and public perception.

System-crashing stack errors make for frustrated students who aren't excited about getting over Forth's learning curve. Run-time stack checking is a good, temporary aid for Forth beginners plagued by mysterious system crashes.

Ever since humans began numbering days and years, devising a perennially accurate calendar has been a problem. This article presents definitions that calculate the Gregorian day and weekday in fixed-point, double-precision Forth.

The headers of words that are needed only a few times, for definitions of higher-level words, just take up valuable dictionary space. This compiler can be loaded into any Forth-79 system, compiles code with or without headers, and can be forgotten when it is no longer needed.
Welcome to a new volume of *Forth Dimensions*. During the past few months, we have thought a lot about the people who use Forth, and the interesting, progressive things they do with it. We felt it was time to bring our publication’s physical appearance more in line with its contents. Cynthia Berglund, who does our design and physical production, met with me and advertising manager Kent Safford to discuss a new design. She translated our conceptual abstractions into the tangible pages you hold in your hands. Meanwhile, Kent worked overtime with suppliers, support materials, and advertisers, among other things.

Fortunately, desktop publishing makes this upgrade far less costly than it looks. Thanks to an intensive team effort, our budget has barely budged; but we think the final result looks more pleasing and contemporary. We hope you agree.

A new columnist debuts in this issue. Mitch Bradley is the author of our “Forth to the Future” series. His first installment discusses the importance of 32-bit compatibility to Forth’s future, and includes a proposed wordset glossary.

Forth, Inc.’s president, Elizabeth Rather, called with some comments about last issue’s “State of the Standard,” in which a CBEMA motion for an ANSI Forth was reported, along with George Shaw’s concerns about that motion and his own IEEE proposal. Ms. Rather found the criticism of the CBEMA proposal inaccurate. She maintains that CBEMA grants its Technical Committees a good deal of latitude in scheduling their activities, and that voting membership and size of such a committee is not restricted at all (some continuity is required of members). And the proposing body — far from being biased in favor of a single vendor — includes Ray Duncan of Laboratory Microsystems, Don Colburn of Creative Solutions, and several of those major vendors’ clients (IBM, MCI, Oak Ridge National Laboratory).

Elizabeth added that participation is open; anyone who wants to be informed about upcoming, Forth-related CBEMA events can contact Elizabeth Rather at Forth, Inc. (Manhattan Beach, CA), or Chris Colburn at Creative Solutions (Rockville, MD).

Since that conversation, Ms. Rather wrote that CBEMA’s “...final vote for the establishment of X3J14, the Technical Committee for ANSI Forth was favorable: 36-1-1 ... An organizational meeting has been scheduled for August 3-4, 1987 at CBEMA headquarters.” A newsletter will be sent to all ANSI members, a press release was to have been mailed on May 1, and the 100+ people on the Forth Vendors Group mailing list will be notified.

If you still haven’t had enough about standards, be sure to read the letter in this issue from Guy Kelly, chairman of the Forth Standards Team. And for more about Elizabeth Rather and Forth, Inc., you will find the interesting interview, “Starting Forth, Inc.” Enjoy!!

—Marlin Ouverson
Editor
Dear Mr. Ouverson,

Are we all trapped by power-of-two chauvinism?

There seems to be an impression that the next logical advance in Forth is from a 16-bit word to a 32-bit word. Can we take a moment to consider 24 bits?

One of the beauties of Forth is that addresses and data are interchangeable, since both use 16 bits. Forth's 64K address-space limit is beginning to seem a disadvantage, and its 16-bit single numerical precision is limiting. Expanding both data and address to 32 bits is easy on a 32-bit microprocessor, but I feel that four gigabit addressing and numerical precision is wasted in most applications. Real memories are unlikely to exceed eight megawords for a considerable time, and the ability to individually address every byte on a hard disk is hardly necessary in a block-oriented language. Meanwhile, a 32-bit Forth engine is either going to have twice as many pins and twice as much silicon as a 16-bit engine, or it is going to multiplex data and addresses and slow down.

So try 24 bits. The real estate and pin count has only risen by 50%. The address space has increased to 16 megawords, though I grant that we lose the ability to address bytes directly. Unsigned numerical precision has increased to the equivalent of seven decimal digits. We can even pack a meaningful floating-point number into 24 bits. Double-precision numbers now have 14 decimal digits of precision, which should be enough for most of us.

Food for thought, or just a red herring?

Kind regards,
Tom Napier
Dresher, Pennsylvania

**WITHIN Words**

Newcomers to Forth from other languages sometimes complain about the relative lack of standardization, particularly the use — by different versions of the standard, and by different implementations of standard and non-standard Fortths — of the same word name to mean different things. Some of these complaints are poorly justified.

In my opinion, as a relative newcomer to Forth (a year and a half as a professional Forth programmer, after working in the Pascal world), the changes perpetrated by the Forth-83 Standard are improvements I wouldn't want to live without. But, as with Pascal (which had the "advantage" of being standardized at its inception, rather than after a decade of practical use like Forth), the standard wordset is never enough. Other words are included in different implementations, and they spread through the Forth community if programmers find them useful and memorable. A good name is not the least important feature of such a word.

One useful non-standard, but popular, word is **WITHIN**, which takes three numbers from the stack and returns a flag — that indicates whether the first number is within the range defined by the second and third. The stack picture is (n low high -- flag), where the flag is true if and only if low <= n <= high.

In a recent revision of their F83 model implementation, however, Laxen and Perry have changed the meaning of the word **WITHIN**. It retains the same stack picture, but the significance of the flag is changed so that it is true if and only if low <= n < high. In other words, where previously the flag was true in the case n = high, in the new version it is false. Laxen and Perry have kept the old version of **WITHIN**, too, renaming it **BETWEEN**. But this seems, to me, to confuse things even more: as a native speaker of English, I don't see any reason to choose **BETWEEN** as a better name for the old **WITHIN**, nor do I feel any natural affinity for either **WITHIN** or **BETWEEN** as the name of a range test that includes one endpoint and not the other.

As an alternative to the pair of word names **WITHIN** and **BETWEEN**, I would like to introduce a set of four names. Probably nobody, with the possible exception of Wil Baden, would want to implement all of these words (one of them — any one, for that matter — would be enough), but perhaps these names could be taken as something like unofficial "controlled reference words." All of them have the same stack picture as **WITHIN** and **BETWEEN**, and are listed below with the truth condition of the flag:

**WITHIN** low <= n <= high
**WITHIN** (low <= n < high)
)**WITHIN** low < n < high
)**WITHIN** (low < n <= high

---

**LETTERS**

32 is a Bit Too Many

Volume IX, Number 1

Forth Dimensions
My **WITHIN** is the same as the Guy Kelly and original F83 **WITHIN** and the new F83 **BETWEEN.** **WITHIN** is the same as F83’s new **WITHIN**. In all cases, an open parenthesis in a word name indicates whether the respective endpoint of the low-to-high number interval is excluded from the range of true values. This convention of open parentheses corresponds to a familiar mathematical convention (many of us learned it with the “new math” a few decades ago) that represents ranges on the real number line. Perhaps the greatest advantage of these word names is that, when you’ve once seen the stack picture, it is impossible to forget which is which.

If my suggestion meets with the approval of the Forth community, perhaps we can squash **BETWEEN** and the new **WITHIN** before they infect too much written code.

Richard Astle
La Jolla, California

A Couple of CASEs

Dear Marlin,

Wil Baden, in his “Ultimate CASE Statement” (FD VIII/5), did an excellent...
job describing problems with the many case proposals that have been published to date. He also stated requirements for further proposals and gave his own version of a sweetened case statement. After reading the article and looking at his examples, I felt that CASE could use just one more lump of sugar. The stack operators and redundant ORs should be eliminated, if possible. My method for eliminating them is to use \texttt{I} to separate the tests within a case clause. This separator is also used to hide the stack and OR operations.\texttt{ENDCASES} is used as a synonym for \texttt{DROP}. It is optional, and is not used when the default code needs the value on top of the stack. Also, the \texttt{EXIT THEN} sequence at the end of each case clause is compiled as a macro (\texttt{ENDOF}).

I thought \texttt{BREAK} might be a better name for the \texttt{ENDOF} macro, and I've included it in one of the examples. One of the advantages of using \texttt{BREAK} is that it could be used outside of the case statement, to replace other \texttt{EXIT THEN} sequences.

It would be great to see an article similar to Wil Baden's case article, but discussing requirements for Forth database elements. Also, I hope there will be more articles on classes in future issues.

Ed Petsche
Greenport, New York

Dear Marlin:

I'm about "cased out," so when I saw not one, but two articles on case statements in the last issue, I proceeded

with reluctance. I have only this to say about Wil Baden's Ultimate Case: if there was ever a subject about which nothing is ultimate, surely it is the case statement!

Happily, Allen Anway's "Tinycase" provided something to think about. Here is something a little different, and with a twist I sort of liked. In this case (is that a pun?), there is a more extended comment I'd like to make.

Allen's statement that, "It takes some stack gymnastics for the high-level word to work out..." is an understatement. I haven't seen such convoluted stack maneuvering since I last looked at some of the first Forth code I ever wrote. See the enclosed screens for an example of how to do it with no stack gymnastics.

Allen's original example becomes:

\begin{verbatim}
4 TRANSCASE TEST \#begin
  2,341,7,899,18,181,97,979, \{ nocase = default \}
\end{verbatim}

As can be seen, the only difference is that the default is not entered as part of the case-translation entries.

By the way, the \texttt{BEGIN WHILE UNTIL} (Anway screen 80) combination will give some Forth compilers trouble (e.g., my CP/M F83 version 2.0.1). Anyway, it seems to me that both a mid-pass test and a post-pass test for completion of an indefinite loop isn't needed. It was this problem that actually got me started on the rewrite, resulting in the enclosed screens.

Sincerely,

Gene Thomas
Little Rock, Arkansas
By the way, the BEGIN WHILE UNTIL (Anway screen 80) combination will give some Forth compilers trouble (e.g., my CP/M F83 version 2.0.1). Anyway, it seems to me that both a mid-pass test and a post-pass test for completion of an indefinite loop isn’t needed. It was this problem that actually got me started on the rewrite, resulting in the enclosed screens.

Sincerely,
Gene Thomas
Little Rock, Arkansas

FST Accepts Proposals

Dear Marlin,
Thanks for the fine job you’re doing at Forth Dimensions. Perhaps your readers might be interested in some thoughts about a few of the events at the 1986 FORML Conference.

There were a variety of working groups again this year, and one that I attended discussed both the possible creation of an ANS Forth and the activation of the Forth Standards Team’s (FST) technical proposal phase. There was also a presentation (by a small panel) concerning a request made to CBEMA to sponsor a Forth standards effort. Having been a member of both the working group and the panel, I discussed my impressions of them with the local FIG Chapter, and also with a variety of other members of the Forth user community (including several FST members). My feeling now is that a majority of Forth users are in favor of an ANSI Forth Standard and that they would like the FST to begin its technical proposal phase.

There seemed to be general agreement that (1) any ANSI effort should be to formalize existing practice and should be limited to resolving any restrictions and any machine or operating system dependencies of the Forth-83 Standard, and (2) any FST effort should be to address extensions to the standard.

Proposed issues to be addressed by any ANSI effort would be:

- Arithmetic and logical operators
- Flow-of-control structures
- Input and output operators
- Memory and mass-storage operators
- Exception handling
- Vectored execution
- Compiler-extension operators
- Data-description operators
- ROM-based applications

Proposed extensions to be addressed by FST technical proposals might include:

- Floating-point operators
- String operators
- Communications operators
- Graphics operators
- Data-structure operators
- File operators
- Operating-system-interface methods
- Multi-tasking methods

There continues to be discussion as to whether it would be better to proceed toward an ANS Forth via CBEMA or IEEE (or even via a joint effort between the two). There is, unfortunately, also a feeling on the part of some members of the Forth community that something could go wrong. Some comments implied that:

- The Forth Standards Team, working in secret, produced a minority document.
- The group that submitted the request for an ANS Forth worked in secret and will produce a minority document.
- The CBEMA group will work in secret (or will cost too much to participate in) and will produce a minority document.
- All of the above groups have done, or are going to do, something that was, or will be, undesirable.

My responses to these comments are:
Having attended and worked at all of the Forth Standards Team meetings since the 1979 Catalina meeting (which I attended as a non-voting observer), I can assure anyone that there was no intent on the part of the FST to be secretive or to produce something undesirable. All meetings were publicized well in advance, observers were welcomed and encouraged to participate in discussions and straw votes, the meeting results were reported...
and discussed in *Forth Dimensions*, and a draft proposed standard was published and distributed.

The FST members were (and are) dedicated to advancing the Forth language and its use. I can understand debate as to the quality and desirability of the various standards, but am puzzled by questions regarding the intent of the group.

The group that submitted the request to CBEMA was (and is) composed of users, vendors, and FST members. They have made copies of the submittal available to any interested party (I asked for a copy, and have received and read it). They also seem dedicated to furthering the cause of Forth.

A CBEMA working group may cost more to participate in than an IEEE group or the FST, but it takes a great deal of dedication of both time and money to travel to and attend standards meetings, no matter who sponsors them. The best most of us can hope for is that all meetings will be open to public observation, that the proceedings of the meetings will be widely published, and that input from the Forth community will be seriously considered. If so, then each of us can help determine the quality of any resulting Forth standard.

Please, let’s all work together to support those who have the time and energy to attempt to enhance the status of Forth. If you can’t directly participate in the formal efforts, you can follow and respond to the published standards committee reports, proposals, and draft standards. Also, remember the other avenue open for input by the Forth community, the FST technical proposal.

As a result of input from FORML and elsewhere, the FST is soliciting technical proposals. The technical proposals should be of two general categories:

1. Proposed extensions to the Forth-83 Standard.

These should be as generally useful as possible, and should attempt to conform to practices currently accepted by the majority of the Forth community. The intent should be to codify proven techniques, not to generate new and untried approaches (submit those to FORML). The extensions should not be considered as mandatory additions to the standard, but as optional add-ons. Their main purpose should be to allow communication of techniques in a standard, transportable format. They should be more useful for the transport of technology between programmers than for incorporation into all commercial applications. They should be more concerned with defining commonly accepted names for standard operations than with locking a vendor or user into a single method of accomplishing a task.


These should only be submitted if it is necessary to remove ambiguities or to generalize the standard in order to incorporate the standard extensions. They should not be attempts to “fix” an offending word or to change the intent of the existing standard. They should provide input to, or should attempt to conform to, any ANSI effort.

The forms and procedures for preparing technical proposals are published in the Forth-83 Standard. The following plan has been adopted to help with the creation and publication of submittals:

1. Become involved with a particular effort. Select one or more topics of interest. Contact others in your area who are interested in the topics. Contact the FST and get on the appropriate interest lists. The FST will attempt to place all interested parties in contact. One of the interested parties should become the group leader for the topic.

2. Prepare and distribute preliminary copies of the proposal. Copies should be sent to any person(s), publication, or bulletin board the working group deems appropriate. Telecommunication should also be considered.

3. Submit a final version to the FST. The FST will collect all the proposals for yearly publication.

---

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Interested parties should, at any time, be able to obtain copies of proposals from the appropriate working groups or from the FST. The FST plans to publish updated lists of working groups, with the name, address, and telephone number of the group leader, in *Forth Dimensions*.

An additional, planned activity of the FST is to solicit technical information and documents from each Forth vendor, for review and comparison with the techniques and methods proposed by the working groups. This will provide additional input concerning currently accepted practices. Any vendor that wishes to participate in the sharing of such material should send the documents to:

Forth Standards Team
P.O. Box 4545
Mountain View, California 94040

or to:

George W. Shaw
P.O. Box 3471
Hayward, California 94540

Vendors with questions should contact George Shaw at 415-276-5953. Other questions should be sent to the FST address, or call me evenings or weekends at 619-454-1307.

Sincerely,
Guy M. Kelly, FST Chairman
La Jolla, California

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**DASH, FIND & ASSOCIATES**

Our company, DASH, FIND & ASSOCIATES, is in the business of placing FORTH Programmers in positions suited to their capabilities. We deal only with FORTH Programmers and companies using FORTH. If you would like to have your resume included in our data base, or if you are looking for a FORTH Programmer, contact us or send your resume to:

DASH, FIND & ASSOCIATES
808 Dalworth, Suite B
Grand Prairie TX 75050
(214) 642-5495

---

**Final Exam: F101**

**Name:**

**Date:**

**Part 1 — Subroutines**

1. Define an algorithm to produce an output that is a reverse mirror image of itself. Find and justify 10 political applications for the algorithm.

2. Write a random-number generator to output an infinitely non-repeating series; do not use any conditionals. Construct a simulation to prove its efficacy for predicting the outcome of everything.

**Part 2 — History and Philosophy**

3. Explain in detail the philosophy of Forth as it relates to Fortran, COBOL, Pascal, Ada, LISP, C, and the morality of nuclear war. Give an example in each language, with a translation to BASIC, and note specific differences and similarities between Forth and nuclear war.

4. Outline the history of Forth, including each commercial and public-domain version of each standard. Draw a comparison of your outline with the history of the world. Be concise.

**Part 3 — Practical Application**

5. Write a program to prove the big-bang theory, including a routine to produce a big bang, creating a universe in the place of your choice. Do not expend more than 5 amps.

6. Prepare a thesis in pseudo-code on the topic, "Real-time control of rioting crowds with Forth." Be prepared to defend your thesis. A rioting crowd will be provided.

Gene Thomas
Little Rock, Arkansas

---

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the appropriate graphics-mode initialization word on line 9 of screen 28 you should be able to run this program with any graphics display. Playing with the scale factor on screen 18 and/or the horizontal scale factors commented out in screen 21 may make the pictures more pleasing on some displays.

References


Recommended Reading


Phil Koopman, Jr. is the vice-president and chief engineer for WISC Technologies. The program discussed in this article was originally developed as a demonstration for the WISC CPU116.
I n the last issue of *Forth Dimensions*, I presented a program that used the
Bresenham algorithm to draw lines on a computer graphics display. Now I will
build upon those definitions to create a program that draws pseudo-randomly
generated fractal landscapes with height-based coloring (including a sea-level) and
hidden-surface elimination.

What's a Fractal?
The word fractal is short for "fractional dimension," and is used to describe vari-
sious geometrical shapes that are not strictly one, two, or three-dimensional
objects. Interestingly enough, most naturally occurring objects (such as coast-
lines) and natural phenomena (such as Brownian motion) are best described by
fractal geometry.

To get a better feel for what a fractal is, consider the western coastline of the
United States. From far away in space, the coastline looks more-or-less straight;
that is, it looks like a one-dimensional line having a fractional dimension near 1.
As an observer gets closer, the perceived length of the coastline and its fractional
dimension increases as more details such as inlets and peninsulas become visible.
In fact, with a very close look at an area like San Francisco Bay, the coastline
becomes a two-dimensional object with a fractional dimension near 2.

How To Draw Random Fractals
To use the fractal concept to draw a landscape, let's first look at how a fractal
can be used to draw a shape, starting from a line. We will look at a random fractal
line, but of course there are other ways to draw fractal lines (see the recommended
reading list).

SCREEN #15
0 \ Special 32-bit unsigned multiply
1 DECIMAL
2 \ This is a special-purpose unsigned multiply that returns only
3 \ the low-order 32 bits. For a more generalized multiply,
4 \ see: P. Koopman, MVP-FORTH INTEGER & FLOATING POINT MATH
5 \ MVP-FORTH series Vol. 3 (revised), Mountain View Press
6
7 : XD*   ( UD1 UD2 -> UD3 )
8 OVER 5 PICK U* >R >R
9 4 ROLL U* DROP >R
10 U* DROP
11 0 SWAP R> 0 SWAP D*  
12 R> R> D+ ;
13
14
15

SCREEN #16
0 \ 32-BIT BASED LINEAR CONGRUENT RANDOM NUMBER GENERATOR
1 DECIMAL \ From Knuth's Art of Computer Prog. vol. 2, page 170
2 DVARABLE SEED \ High 16-bits of SEED are actual random #
3 \ Store all 32-bits initially to re-seed generator
4 3141592653. SEED D+  
5 6 : RNDF ( -> N )
7 SEED D+ 3141592621. XD* 1. D+  
8 DUP ROT ROT SEED D+ ;
9
10 \ The below random number generator is very poor, but
11 \ produces interesting smoothed/rounded rolling hills
12 \ RNDF ( -> N )
13 \ [ SEED I* ] LITERAL @ 31417 U* 1. D+ DUP ROT ROT SEED D+ ;
14
15

SCREEN #17
0 \ FRAC TAL LAYOUT
1 DECIMAL
2 \ SQUARE \ P1-----x-----P2
3 \ LAYOUT: \ I | I | I | Roman #'s are square #'s
4 \ x-----x-----x \ P1..P4 are corner point #'s
5 \ I | IV | III |
6 \ P4-----x-----P3
7 \ In the recursive routines, Pl..P4 are the address of the
8 \ points within the layout array
9 \ Each recursion calculates the new "x" point height values
10 \ WALL ; \ Useful point for FORGETTING
11 \ RECURSE
12 \ LATEST PFA CPA ; \ IMMEDIATE
13
14 : CELL* 2* ; \ Change to fit bytes/cell of your machine
15 : CELL+ 2+ ; \ Change to fit bytes/cell of your machine
HARNESS THE POWER OF FORTH!

FORTH: A TEXT AND REFERENCE
by Mahlon G. Kelly and Nicholas Spies

The syntax, powerful architecture, and program design of FORTH make it the most progressive way to dramatically increase software productivity.

Both a highly approachable text and an all-inclusive, easily-accessible reference, this 512 pp., handbook on FORTH accommodates those with no prior computer knowledge as well as advanced programmers looking to delve more deeply into the language. It is designed for both classroom use and self-study.

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make the book an outstanding desk reference
and extended program, a generic screen editor, is used to explain the unique program-design procedures of FORTH. January 1986. Paper $21.95. Cloth $28.67

STARTING FORTH, 2nd Edition
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Considered to be the “bible” of the introductory books on FORTH, this all-inclusive presentation of the FORTH programming language and operating system allows the beginner a clear, effective understanding of the whole FORTH system.

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Each outstanding chapter concludes with a “Review of Terms”, practical study problems, “Handy Hints”, and features three detailed examples of FORTH in application.


Available at finer bookstores or direct from Prentice Hall, (201) 767-5937
First, consider the straight line segment shown in Figure One-a. We will take the midpoint of that line segment and pull it to one side, as shown in Figure One-b. Next, we will recursively take each midpoint of the resulting line segments and pull them randomly to one side or the other as shown in Figures One-c and One-d. As you can see, this process quickly results in a wandering line. For the most pleasing shape, the amount of “pull” applied is cut in half at each level of recursion, forming a smooth result.

In order to extend this concept to an area instead of a line, the “Landscape” program on screens 15-28 forms a two-dimensional array. Each cell in the array holds the height of a point above or below sea level. The word CALCULATE-SERVICE recursively breaks this array into smaller and smaller squares, using the addresses of the four corners of the array instead of four pairs of (X,Y) coordinates. SET-HEIGHTS sets the heights for the array cells at the midpoints of the sides of the current square and for the center of the current square, then breaks the square up into four sub-squares (see the diagram on screen 17 for a description of the nomenclature used by the subdividing algorithm). After the data array has a height associated with each point, the program uses the SEA-LEVEL word to reassign all negative heights to sea level.

After the heights are computed, the landscape is drawn on the screen. As each point of the array is drawn, it is assigned a color based on height and the number of colors available.

Screen-Drawing Tricks

I have used several tricks in “Landscape” to speed up the screen-drawing time. This drawing time would be prohibitively long if conventional, three-dimensional graphics techniques were used.

The most time-consuming part of many graphics drawing programs involves 3-D transformations, especially rotations. On the other hand, a top or side view of a fractal landscape would not be terribly exciting. I solved this speed-versus-prettiness dilemma using two techniques: a “sleazy” rotation to elevate the rear of the picture, and an

Forth Dimensions
unconventional point-connection scheme to eliminate the need for spinning the picture.

A standard rotation of a landscape to elevate the rear of the picture involves using the equation:

\[ \text{NEWY} = \text{yvalue} \times \sin(\text{angle}) + \text{height} \times \cos(\text{angle}) \]

for each height data point in the landscape array. In order to eliminate the scaled integer or floating-point arithmetic involved, I chose my rotation angle to be 30 degrees and changed the "* sin(angle)" term to a division by two. Then, to get rid of the cosine term, I decided to approximate \(\cos(30) = 0.866\) by 0.5 (division by two) and increased the original height value on line 7 of screen 28 to compensate. The elevation using this strategy is accomplished by Y-CONVERT on screen 19.

Even with the rear of the picture elevated, the result is pretty unexciting if points are connected by columns and rows. You would only see regularly spaced vertical lines with landscape profile lines wigging horizontally across the screen. In order to fix this, lines 10 through 12 of screen 21 connect points in sideways "V" patterns to form a picture composed of diagonal lines instead of mostly horizontal and vertical lines. The lines are drawn and colored by columns of points, front to back.

It turns out that hidden-surface elimination, a major computational drain on many graphics programs, comes at almost no charge when using the drawing technique described above. Since points are drawn from front to back, lines 5 - 7 of screen 21 simply ensure that each new Y value for a point to be displayed is further up on the screen than any previous Y values for that column.

Running The Program

Simply type LANDSCAPE from the Forth "OK" prompt. The program will draw a landscape and wait for a keystroke on a PC-compatible machine with a Color Graphics Adapter (CGA) display. If you change the constants on lines 2 - 3 of screen 18, redefine the coloring word on lines 9 - 14 of screen 20, and substitute (Continued on page 11.)
SCREEN #26
0 \ RECURSIVE PROCEDURE TO SET HEIGHTS FOR RANDOM 3-D TERRAIN
1 DECIMAL \ BASED ON SUB-DIVIDED SQUARE FRACTALS
2 : CALCULATE-SURFACE ( P1 P2 P3 P4 DELTA-H LEVEL# -> )
3 SET-HEIGHTS
4 DUP "TERMINAL ABORT" "BREAK IN CALCULATE-SURFACE"
5 IF ( non-zero level )
6 SQUARE1 RECURSE
7 SQUARE2 RECURSE
8 SQUARE3 RECURSE
9 SQUARE4 RECURSE
10 THEN
11 DDROP DDROP DDROP ;

SCREEN #27
0 \ SEA-LEVEL -- SET SEA LEVEL FOR NEGATIVE HEIGHT POINTS
1 DECIMAL
2 : SEA-LEVEL ( -> )
3 SQUARE-P1 SIZE 0 DO
4 SIZE 0 DO
5 DUP @ DUP O<
6 IF ( below sea level -- add fudge factor for waves )
7 1 AND I J ++ 7 AND OVER !
8 ELSE DROP THEN
9 CELL+ LOOP
10 LOOP DROP ;

SCREEN #28
0 \ MASTER PROCEDURE TO DRAW A RANDOM 3-D FRACITAL
1 DECIMAL \ BASED ON SUB-DIVIDED SQUARES
2 DVAR SEED-SAVE \ Saves random seed. Placing the saved
3 \ value back into SEED will re-create the same landscape
4 LANDSCAPE ( -> )
5 SEED D@ SEED-SAVE D! INITIALIZE-SQUARE
6 CR ." Computing new heights"
7 SQUARE-P1 SQUARE-P2 SQUARE-P3 SQUARE-P4
8 YMAX 2/ #LEVELS CALCULATE-SURFACE
9 CR ." Computing sea level" SEA-LEVEL
10 SET-EGA-MODE \ Change to SET-EGA-MODE or SET-CGA-HIRES-MODE
11 \ SEA-KEEP-SAVE D@ CR ." SEA-LEVEL" D. ( Optional SEED display )
12 DRAW-SURFACE
13 CR ." Press any key to continue" KEY DROP
14 SET-TEXT-MODE ;
32-bit machines are here to stay. Over the next few years, 32-bit machines will grow in importance. Forth must be able to use the full power of 32-bit machines.

This article presents a consistent, proven scheme for using Forth on 32-bit machines, based on several years of experience with 32-bit systems. It does not address the problems of simulating extended addressing on the 8086. The focus is on making the transition from 16-bit Forth systems to "real" 32-bit architectures like the 80386, the 68000, and the IBM RT.

Goals
1. Programs should run unchanged on either 16-bit or 32-bit machines.

2. The 32-bit machine must not be penalized. The full power of the 32-bit machine must be available.

Tradeoffs
1. Existing programs may have to be modified in order to make them run on either size machine.

2. A lot of new words are specified. These words are necessary because the existing words do not work right on 32-bit machines.

Justification
Forth will not succeed if it remains stuck at 16 bits while the world switches to 32 bits. Insisting that existing programs run unchanged on 32-bit machines penalizes the 32-bit implementation.

The wordset presented here penalizes neither 16-bit nor 32-bit machines. It adds no new functionality, it simply specifies a set of names for words whose behavior is independent of the machine size.

What is a 32-bit Machine?
The distinguishing factor is the size of the address arithmetic. The address arithmetic determines the size of an address that can be easily calculated. The 68000 is a 32-bit machine — even though its data path is only 16 bits wide, and even though the package has only 24 address pins — because it is easy to calculate 32-bit addresses. The 80286 is a 16-bit machine, even though it has more than 16 address pins; addresses outside a 16-bit bank are painful to calculate.

With the 80386, a 32-bit machine, a normal is 32 bits. The majority of all Forth operations are performed on normal numbers. A "character" is always an 8-bit number. A 16-bit machine, a longword is represented as two stack entries. On a 32-bit machine, a longword is represented as one stack entry.

Compatibility Problems
When moving code from a 16-bit Forth implementation, there are two major problems.

Most Forth programs contain lots of things like 2+ and 6+. This is fine if you are trying to add two or six to a number, but it causes problems if you are trying to increment an address to point to the next number. On most 32-bit machines, successive numbers are four addresses apart, not two.

16-bit numbers are inadequate for many purposes, so Forth has "double numbers," which are 32 bits, represented as two stack items. 32-bit systems do not need two stack items to represent a 32-bit number. Existing 16-bit programs use fancy stack manipulations to move the separate halves of a 32-bit number. Double-number operators like 2DUP are used both for pairs of single numbers and for 32-bit numbers. A 32-bit number is an entirely different thing than a pair of numbers. They just happen to have a similar representation on a 16-bit system. On a 32-bit system, this isn't true.

Solutions
Some brief words about the nomenclature used here:

A "normal" is a number that is represented as one stack entry. On a 16-bit machine, a normal is 16 bits. On a 32-bit machine, a normal is 32 bits. The majority of all Forth operations are performed on normal numbers.

A "longword" is always a 32-bit number. On a 16-bit machine, a longword is represented as two stack entries. On a 32-bit machine, a longword is represented as one stack entry.

A "word" is always a 16-bit number. On a 16-bit machine, a word is represented as a single stack entry. On a 32-bit machine, a word is represented as the low 16 bits of a stack entry, with the upper 16 bits set to zero.

A "character" is always an 8-bit number. A character is represented as the low eight bits of a single stack entry, with the remaining upper bits set to zero.

Address Incrementing Words
Changing all occurrences of 2+ to 4+ doesn't solve the problem, it just...
sweeps it under a different rug. What we really need is a way to increment an address by the right number, regardless of what machine we’re on. To do this, we define some names for the sizes of things.

/\N \n \n (\n \n ) “per-n”
The number of bytes in a "normal" number, which is a single stack entry. /\N is four on a 32-bit machine and two on a 16-bit machine.

/\L \n \n (\n \n ) “per-l”
The number of bytes in a 32-bit "longword." /\L is four on all machines.

/\W \n \n (\n \n ) “per-w”
The number of bytes in a 16-bit "word."/\W is two on all machines.

/\C \n \n (\n \n ) “per-c”
The number of bytes in an 8-bit "character."/\C is two on all machines.

The notation /\X for “the number of bytes in an X,” pronounced “per-x,” follows the recommendations in Kim Harris’s nomenclature guidelines (“Forth Coding Conventions,” Proceedings of the 1985 FORML Conference).

You might think, since /\L is always four, that the name “/\L” is not needed. Ditto for /\W and /\C. However, the symbolic name /\L clearly indicates that the code is dealing with the size of a longword, rather than the number four, which could be anything — perhaps the expected number of legs on a cow. Magic numbers make programs harder to understand and maintain!

Others have suggested the names CELL and LSIZE instead of /\N; however, the name “normal” and the mnemonic “N” will be useful to us later.

What will we do with these constants? One obvious answer is to replace occurrences of 2+ with /\N. Similarly, in cases where we want to step through an array of 16-bit words or 32-bit longwords, we might use /\W+ or /\L+.

Another use is to calculate the number of bytes to ALLOT for some data structure or array. For instance, if we need space for 100 normal numbers, we could write 100 /\N * ALLOT instead of 100 2* ALLOT.

A third use is to index into an array. Instead of writing the code in Figure One-a, for instance, we might use instead the definitions in Figure One-b. Notice that in all these cases, we have given up some efficiency! The word 2+ probably executes faster than the two words /\N + and 2* almost certainly is faster than /\N *. We will not tolerate such
inefficiency! Therefore, we define some more words, their existence amply justified by frequent use.

The functions in Figure Two are presently performed with 1+, 2+, and 4+, whose use does not work on all machines. Most occurrences of 2+ in existing Forth code can be replaced by NA~+ to make the code more transportable. The names stand for "normal-address-one-plus," etc., indicating that they increment an address to the next datum of a particular type.

Some machines do not directly address bytes. For instance, the Novix Forth chip is a word-addressed machine. Adding one to an address moves to the next 16-bit word, not to the next byte. For such machines, NA1+ is not equivalent to /N +. The real rule is that NA1+ should increment an address to point to the next item of a given type.

The words in Figures Three and Four find the address of the nth item in an array of items starting at addr. For instance, NA+ is equivalent to /N* + on most machines.

This may seem like a lot of words. It is a lot, but they are frequently used, which is the same justification used for words like 1+ and 2*.

Explicit 32-bit Operators

One solution to the double-number problem on 32-bit machines is to make double numbers 64 bits. This is attractive because it is compatible with existing code that manipulates double numbers as pairs of stack entries. On the other hand, it is inefficient. 64-bit arithmetic is slower than 32-bit arithmetic on most machines. While many applications require more than 16 bits of precision, few require more than 32.

I believe the best long-term solution is to define a set of words that explicitly operates on 32-bit data, regardless of the machine's word size. The names of these words begin with the letter L, indicating that they operate on "long" operands. Their implementation is simple. On a 16-bit machine, they are the same as the existing "D" words (e.g., D+) and the "2" words (e.g., 2DROP). On a 32-bit machine, they are the same as the regular single-number operators. The important point is that the "L" operators always operate on 32-bit longwords, regardless of machine size.

Long Arithmetic Operators

Some 32-bit arithmetic operators:

L+ (LI L2 -- L3) "l-plus"
Adds 32-bit longwords. On a 16-bit machine, L+ is the same as D+. On a 32-bit machine, L+ is the same as +.

L- (LI L2 -- L3) "l-minus"
Subtracts 32-bit longwords. On a 16-bit machine, L- is the same as D-. On a 32-bit machine, L- is the same as -.

L* (LI L2 -- L3) "l-times"
Multiplies 32-bit longwords. On a 16-bit machine, L* is the same as D* (which is not included in the standard). On a 32-bit machine, L* is the same as *.
L/ (L1 L2 -- L3) “i-divide”
Divides 32-bit longwords. On a 16-bit machine, L/ is the same as D/ (which
is not included in the standard). On a 32-
bit machine, L/ is the same as /.

I haven’t mentioned all the operators
that are needed, but the rest of them are
named in the obvious way. For instance,
L= compares two 32-bit longwords for
equality.

Stack Manipulations
The “2” stack operators, such as
2SWAP and 2DUP, are unsatisfactory
for manipulating 32-bit longwords. Such
operators were originally intended for
manipulating pairs of numbers, which are
distinctly different from 32-bit longwords.
I propose a set of 32-bit stack
manipulation operators whose names begin
with (you guessed it) the letter L.
Examples are LSWAP and LDUP.

Mixing 32-bit numbers and 16-bit
numbers on the stack poses problems. In
a 32-bit system, all stack entries are 32
bits, so this is not too bad. On a 16-bit
system, both 32-bit and 16-bit numbers
may need to coexist on the stack.
Currently, this is handled in an ad hoc
fashion, using operators like ROT to
separately manipulate the pieces of the
numbers. Programs that do this are not
portable to 32-bit machines (here I
assume that we have decided against using
64-bit numbers). What we need is a set of
operators for manipulating mixed stacks.
The needed operators mostly duplicate
existing functions, so we really don’t
need new capability, just new names! The
new names will clearly specify the sizes
of the operands.

LDUP (L -- L L) “i-dupe”
Duplicates a 32-bit longword. On a 16-bit
machine, LDUP is equivalent to
2DUP. On a 32-bit machine, LDUP is
equivalent to DUP.

LSWAP (L1 L2 -- L2 L1) “i-swap”
Exchanges 32-bit longwords. On a 16-bit
machine, LSWAP is equivalent to
2SWAP. On a 32-bit machine, LSWAP
is equivalent to SWAP.

LOVER (L1 L2 -- L1 L2 L1) “i-
over” Copies a 32-bit longword over a
32-bit longword. On a 16-bit machine,
LOVER is equivalent to 2OVER. On a
32-bit machine, LOVER is equivalent to
OVER.

LDROP (L1 --) “i-drop”
Removes a 32-bit longword from the
stack. On a 16-bit machine, LDROP is
equivalent to 2DROP. On a 32-bit
machine, LDROP is equivalent to
DROP.

LROT (L1 L2 L3 -- L2 L3 L1) “i-
rote” Rotates 32-bit longwords. On a 16-bit
machine, LROT is equivalent to
2ROT. On a 32-bit machine, LROT is
equivalent to DROT.

NLSWAP (L n -- L n L) “i-n-swap”
Exchanges a 32-bit longword with a
normal number. On a 16-bit machine,
NLSWAP is equivalent to ROT ROT.
On a 32-bit machine, NLSWAP is
equivalent to SWAP.

NLSWAP (n L -- L n) “n-i-swap”
Exchanges a normal number with a 32-bit
longword. On a 16-bit machine,
NLSWAP is equivalent to ROT. On a
32-bit machine, NLSWAP is equivalent
to SWAP.

LNOVER (L n -- L n L) “i-n-over”
Copies a 32-bit longword over a normal
number. On a 16-bit machine,
LNOVER is equivalent to 2 PICK
2 PICK. On a 32-bit machine,
LNOVER is equivalent to OVER.

NLOVER (n L -- n L n) “n-i-
over” Copies a normal number over a 32-bit
longword. On a 16-bit machine,
NLOVER is equivalent to 2 PICK
2 PICK. On a 32-bit machine,
NLOVER is equivalent to OVER.

L>R (L --) “i-to-r”
Moves a 32-bit longword to the return
stack.

L>R (-- L) “r-from”
Moves a 32-bit longword from the return
stack.

L>R and L>R are provided to help with
more complicated stack manipulations
(Continued on page 25.)
FIG
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The authoritative description of 83-Standard Forth. For reference, not instruction.

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<thead>
<tr>
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<th>TITLE</th>
<th>AUTHOR</th>
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</tr>
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involving mixed stacks. (However, it is usually preferable to try to avoid complex stack gymnastics, instead.)

Accessing Memory

We need some words for accessing memory items of various sizes. We already have C@, C!, 2@, 2!, 0, and 1. We also need some words to access exactly 16 bits and exactly 32 bits, so we add these words:

`w-fetch` Fetches the 16-bit word atadr. On a 32-bit machine, the result is padded with zero to form a 32-bit normal number on the stack.

`signed-w-fetch` Fetches the 16-bit word at adr. On a 32-bit machine, the result is sign-extended to form a signed, 32-bit normal number on the stack.

`w-store` Stores the 16-bit word atadr. On a 32-bit machine, the number “16b” is represented on the stack as the lower half of a 32-bit normal number, and only the lower 16 bits are stored at adr.

`L-fetch` Fetches the 32-bit longword atadr. On a 16-bit machine, the result is left on the stack as two 16-bit numbers, with the most-significant half on the top of the stack.

`L-store` Stores the 32-bit longword “L” at adr. On a 32-bit machine, the longword “L” is represented on the stack as two 16-bit numbers, with the most-significant half on the top of the stack.

Type Conversion

Some words for converting to and from 32-bit longwords:

`N->L` “n-to-l” Converts a normal number to an unsigned 32-bit longword. On a 16-bit machine, `N->L` is equivalent to `S->D`. Does nothing on a 32-bit machine.

`L->N` “l-to-n” Converts a 32-bit longword to a normal number. On a 16-bit machine, `L->N` is equivalent to `DROP`. Does nothing on a 32-bit machine.

Important Note

It is neither necessary nor desirable to use the “L” operators all the time on a 32-bit system. Most of the time you don’t really care whether a number is 16 bits or 32 bits. So you just use the normal operators like +, -, DUP, etc. The right time to use the “L” operators is when:

1. You require more than 16 bits, and
2. You want your code to run on both 16-bit and 32-bit machines.

In particular, I use the “L” operators when converting “double numbers” from 16-bit machines to 32-bit machines. Also remember that “double numbers” on 16-bit machines are not always the same as 32-bit longwords. Sometimes the “double number” operators are used to manipulate pairs of single numbers. Such uses must remain unchanged. A pair of numbers is still a pair of numbers, even on a 32-bit machine.

The Name’s the Thing

The underlying problem is that Forth often uses the same name for different purposes. Examples: 2+ is used to add the number two, and to increment an address to the next word; `2DROP` is used to remove two single numbers from the stack, and to remove a 32-bit number from the stack. This happens to have worked in the past, because Forth just assumed that every machine in the world is a 16-bit, byte-addressed machine. (In fact, it didn’t work on the Data General Nova, which is word addressed!) Different conceptual functions should have different names, even if the functions happen to have identical implementations on a particular machine.

That is why I am proposing new names without any new functions!

FIG-FORTH for the Compaq, IBM-PC, and compatibles. $35
Operates under DOS 2.0 or later, uses standard DOS files.
Full-screen editor uses 16 x 64 format.
Editor Help screen can be called up using a single keystroke.
Sound capability allows storing Forth with all currently defined words onto disk as a .COM file.
Definitions are provided to allow beginners to use Starting Forth as an introductory text.
Source code is available as an option, add $20.

Async Line Monitor
Use Compaq to capture, display, search, print, and save async data at 75-19.2k baud. Menu driven with extensive Help. Requires two async ports. $300

A Metacompiler on a host PC, produces a PROM for a target 6303/6803
Includes source for 6303 FIG-Forth. Application code can be Metacompiled with Forth to produce a target application PROM $280

FIG-Forth in a 2764 PROM for the 6303 as produced by the above Metacompiler. Includes a 8 screen RAM-Disk for stand-alone operation. $45

An all CMOS processor board utilizing the 6303.
Size: 3.93 x 6.75 inches.
Uses 11-25 volts at 12ma, plus current required for options. $210 - $280
Up to 24kb memory: 2 kb to 16kb RAM, 8k PROM contains Forth. Battery backup of RAM with off board battery.
Serial port and up to 40 pins of parallel I/O.
Processor bus available at optional header to allow expanded capability via user provided interface board.

Micro Computer Applications Ltd
8 Newfield Lane
Newtown, CT 06470
203-426-6164

Foreign orders add $5 shipping and handling. Connecticut residents add sales tax.
Yes, But ...

If we make all these new names for functions we already have, like

: LSWAP (L1 L2 -- L2 L1)
2SWAP ;

doesn’t the code run slower? There are two answers:

1. It's not much slower.
2. There's a way to get the speed back.

To speed it up, see the article on synonyms (Yngve, Victor H. “Synonyms,” Forth Dimensions VII/3). A synonym provides a new name for an existing word, with no run-time penalty. Basically, a synonym is an extra name for an existing word. When the compiler sees the new name, it compiles the compilation address of the existing word.

Tips for Converting Programs

When I convert a 16-bit program to a 32-bit machine, the first thing I do is search for all occurrences of the character 2. I look for the number two and for words that start with 2, such as 2DROP. Many of these have to change. Usually, words like 2+ change to NA1+, and 2* to /N*, etc. For words like 2DROP, you have to decide whether it is being used to drop a pair of normal numbers (in which case you leave it alone), or a 32-bit number (in which case it changes to LDROP).

The next thing I look for is words like D+, D-, etc., changing them to L1+,L-, etc.

Sometimes the word 0 is used to convert an unsigned normal number to a longword. This has to change to N->L.

Finally, I look for cases where stack manipulations like ROT are used to swap mixed 32-bit and 16-bit numbers. These change to words like NLSWAP. These are not as hard to find as you might guess, because they usually occur just before a 32-bit operator, so you should keep an eye out for them while looking for 2, D+, 0, etc.

Since each of the words proposed here can be implemented so easily, it is easy to add them to the program as you need them. I usually group their definitions together at the beginning of the source code, to make them easier to find.

Sometimes you may find you need a mixed 16-bit/32-bit operator not mentioned here. I recommend that you pick a name starting with either “NL” (if the longword is on the top of the stack) or “LN” (if the normal number is on top).

Further Reading

This wordset was originally proposed in two earlier papers.


These papers describe some other words not mentioned here, including names for words to perform extended addressing on 16-bit machines. Vol. 2 No. 4 of The Journal of Forth Application and Research contains several papers describing other aspects of 32-bit machines.

Mitch Bradley is the owner of Bradley Forthware, vendor of the 32-bit “Fortmacs,” a very complete Forth-83 environment for the Atari ST.
Elizabeth Rather is the president of FORTH, Inc. and was as close to Forth’s beginnings as anyone except its creator. At a recent convention, Michael Ham interviewed Ms. Rather at length about the early years of Forth and the history of the first company to deal in Forth systems and applications. She spoke frankly of product successes, lean times in the early days, and public perception.

Michael Ham: I’d like to get a sense of the history of FORTH, Inc., and then a sense of where it is today and what its plans are for the next thrust in Forth. I feel there have been many changes in terms of product packages and building up in another, new direction. So let’s start with the history.

Elizabeth Rather: I want to go back to the actual dawn of time, which was a day in 1971. I was working at the University of Arizona, in the registrar’s office, processing student records. My then husband was an astronomer at the National Radio Astronomy Observatory, also in Tucson. I walked into the lab one day and there was a rack of electronic equipment in the middle of the lab—which was not unusual—and there was a guy sitting in front of it. I asked, “What’s that?” and he said, “That’s a computer.” I was absolutely amazed, because I knew what a computer was: a computer occupied an entire room with a raised floor and had thousands of acolytes around it. This little box in the middle of the room with a guy sitting in front of it was something I’d never dealt with before. I came to understand that this little computer was going to be controlling the telescope and doing data acquisition and analysis, and all kinds of neat stuff. I asked, “How are you programming it?” He said, “Well, it’s a language called Forth.” I asked, “What is that?” And the rest is history.

MH: You had programmed before?

ER: At that time, I had been programming for about ten years in Fortran, assembly language, and a little bit of Cobol (I’m almost embarrassed to admit). I’d gotten very interested in some of the avant garde things that were going on. I’d been playing a little bit, just the previous week, as a matter of fact, with APL. I’d really fallen in love with APL, but it was doomed to be a very short love affair because I fell in love with Forth shortly afterwards.

MH: It shows you had a weakness for weird languages, at least.

ER: Right, and I realized it was really the interactivity of APL that appealed to me, and not anything particular about it as a language. Going at a program interactively is something I still think the world doesn’t value nearly enough. Even the languages people are using today, such as C, are not interactive, and it is hard to realize how incredibly powerful interactivity is.

MH: Right. I’ll put in one story of mine: people don’t understand that when you say it is interactive, it means you can do this back-and-forth with the program. They say, “Yeah, but so what?” I remember the first time I heard about a text editor. It was Wylbur, and I asked, “So, what does it do?” My friend explained to me, “You can correct words, and search and find them, and move lines around...” And I said, “That’s it? So what?” I didn’t understand at the time, but now I can’t write without a word processor.

When you found Forth, did you start programming in it right then?

ER: No, I didn’t. I’d always been interested in the concept of computers making things wiggle, which was actually a fairly new concept at that time. I was also pretty bored with the registrar’s office. Chuck was living at that time in Charlottesville, Virginia, and he would come out for a couple of months at a time, reprogram everything in sight, and then go back to Charlottesville, leaving chaos in his wake. Ned Conklin, who ran the Tucson division of the observatory, was a very good friend of ours, and he would come over for dinner, and wring his hands and cry a lot about this guy who wrote these beautiful programs that were always, chronically, ninety percent done.

I took advantage of the situation and volunteered to take on, as a moonlighting operation, the task of understanding the programs and providing some local support, talking on the phone with Chuck as need be, and solving problems as they arose. We originally thought it was going to be ten hours a week, and I found myself working for them over forty hours a week in addition to my regular job. We figured it was time to make some adjustments.
MH: That must have been like trying to learn Chinese by trying to read a newspaper with the aid of a dictionary. That’s a hard way to get into Forth.

ER: Well, contrary to popular opinion, Chuck does write. He is capable of it. In fact, he writes extremely well, and at that time he had written three volumes of documentation of Forth. In fact, he wrote a book — which is a wonderful book — called Programming a Problem-Oriented Language. I still have a copy in my office, and I keep cherishing the hope that someday I can edit and publish it, because it was about Forth at a time slightly before I met Chuck. It was a previous incarnation of Forth, but very recognizable. The book tells why it was like it was, how he was making his decisions. It was written in a style that is absolutely just like hearing Chuck talk. It’s wonderful to read from that point of view, and it’s very interesting as a historical document of how an extraordinarily bright person goes about thinking of a new language. I don’t think that’s ever been documented before. It really is almost a stream of consciousness account of how that took place, and it was wonderful. But it didn’t really explain a whole lot about how the current system worked, and it certainly looked funny. I really still sympathize with people who have never seen Forth before, who look at it and say, “Boy, is that ever a funny-looking language.”

I can remember how that felt. The other documentation he wrote tended to describe individual words. You had to know what the words were in order to look them up. It really was not as helpful as it might have been, so one of the very early things I did at the observatory was write a user’s manual on Forth per se. I think that was the first user’s manual on Forth per se. I remember thinking that it was so far in advance of the state of the art that it stayed an advanced system for a very long time. When we were near to finishing that project, I recall very clearly, we were sitting around our patio one summer evening in Tucson talking about what the future was going to hold for computers and mini-computers and programming, and what was going to happen to Forth, and so on. We had given away a number of systems to astronomers who had come there — we were a service organization, and astronomers came from all over the world to use that facility. A number of them were so impressed with the software that they took copies home and tried to adapt it for use. It had become apparent that Forth was as powerful for other people as it was for us. So this particular evening, we said, “You know, why don’t we form a company and do something? There’s got to be some money in it somewhere.”
So FORTH, Inc. was born that evening. It was in the summer of 1973. Chuck and his wife Min, myself and my husband John, and Ned Conklin, who was our mutual boss, went to see a lawyer the following week and incorporated FORTH, Inc.

MH: Was Forth primarily on one machine at that time?

ER: No, it was on a number of machines. It was on a PDP-11, which was the machine we upgraded. The very first was a Honeywell minicomputer; and it had migrated into the observatory on Varian 620s and Hewlett-Packard 2116s, and the Nova was across the street at Steward Observatory. So there were a number of systems.

MH: So Forth from its very beginning started this heritage of polyFORTH covering a broad range of machines and processors...

ER: Yes, that was true, and it's also true that the very first Forth systems were all multi-tasking, multi-user, standalone operating systems. Multi-tasking is absolutely not anything new or recent. It's been a mainstream of Forth development on every system that I have ever had personal experience with.

MH: Were Forth applications at the observatory strictly for telescope control and scientific computation? What range of applications was it being used on, and where did you see its use?

ER: Forth controlled the telescope, and it did high-speed data acquisition. We were recording 500 samples every 10 milliseconds, I believe, which is ticking along pretty well. We also had a very early Tektronix graphics terminal that we had a nice graphics package on, including some 3-D graphics capability and a lot of mathematical analysis. The object of the game was to send an astronomer home with his data mostly reduced on the spot; it was very important to allow him to reduce his data on the spot because he could adjust his observing program depending on what he was seeing or not seeing, as the case may be.

MH: Let's turn to your background. Your studies were not in computer science, as I recall.

ER: No, I have both an undergraduate and a graduate degree in medieval history. It's a lot of fun, but there's no money in it.

MH: Well, it does lead to a career in Forth. You began in computers just...

ER: I actually had studied physics as an undergraduate. I didn't complete a degree in physics, but I had studied it. I was married to an astronomer, and all our friends were scientists. I felt comfortable in the scientific community. I went to work in the days when any woman applying for a job was required to take a typing test. Regardless of what you said you were interested in, if you were a female and went into the personnel department, you took a typing test. It turns out, of course, that typing is one of the primary skills for a Forth programmer, so that was not inappropriate.

My first job was handling data processing for a physics research group at the Oak Ridge National Laboratory. They had an extremely primitive computer and some very primitive mainframes — I'm almost embarrassed to tell you when this was. I was working with the computers, and I convinced my boss, Jack Harvey, that I should be allowed to take a two-week Fortran course so I could handle the programs a little bit better. I did that and wrote a couple of very simple Fortran programs. Not long after that we moved to California. I went into the University of California's personnel office, and said, "I want a job." They asked, "What do you do?" and I said, "I'm a programmer."

They sent me for an interview with the astronomy department. I really wanted that job. The professor there had a group working on a NASA contract doing analysis of comet and satellite orbits, and he said, "We do all of our programming in assembly language on a 7094." I said, "Well, I have never actually written assembly language for the 7094." And he said, "Don't worry, we'll teach you." And they did.

MH: When you put your company together, did you start in Tucson?

ER: We never did any business as FORTH, Inc. in Arizona. We moved to California in late 1973, reincorporated the company there, and opened for business about then. I sent a press release to Computerworld. I had never written a press release before and didn't even know what one looked like. But I wrote one and sent it to several of the computer magazines. Computerworld ran it on the front page in about November of 1973. By December and January, the letters were flooding into our new home in Manhattan Beach, California. We got about 350 letters, and Chuck and Min moved to California about then. I had an old typewriter, and Chuck and I sat in the back room of my house, taking turns writing personal answers to all 350 letters. Out of that we started getting a few jobs, doing custom programming for people.

MH: Your first sale was to a man in New Jersey?

ER: Yes, Art Gravina of Cybek. He had been working with a very large BASIC program for business data processing. He had been the architect of the program, written in something called Extended BASIC. Art's dream was to do larger applications than this Extended BASIC could handle. He called us on the phone and we had several conversations. He said, "If what you say is real, we've got a tiger by the tail here." He came out for one weekend, Chuck and I flew up to northern California, and we all spent the weekend together at the offices of Systems Industries in Sunnyvale. They made a computer available to us starting at five o'clock Friday afternoon.

By Monday morning, we had ported Forth onto that computer from a paper tape we had taken with us from the observatory. We had to develop a new disk controller for it on the spot, hand coding in with the paper tape — it was unreal. Having gotten that done, we implemented all the rudiments of a database system, and wrote an application that would accept data, display data, write reports, and so on. We had all that done.
by Monday morning. We hadn’t had a whole lot of sleep, I must admit, but we had it all working, with several terminals up and the whole scene.

MH: It was a multi-user system?

ER: Yes, everything we’ve ever done has been multi-user. Art went back to New Jersey, quit his job the next day and founded a company. He has done extremely well since then, and has continued with large database applications, mostly in the area of hospital management. His company was recently bought by a division of McDonnell Douglas.

There was another hospital system written in polyFORTH that Honeywell is marketing. Bob Barnett, our second or third customer, wrote it. He’s in Iowa, and he would go away for a couple of years, developing his applications, then call us back in, then go away again on his own.

MH: How big is FORTH, Inc. today, and what are your major projects at this time?

ER: We’re back in Manhattan Beach after six years in Hermosa Beach. We have about 25 employees, most of them full-time, a few of them consultants. Our business is about two-thirds custom programming and one-third sales and support of polyFORTH. The custom programming that we’re doing is primarily in areas such as automated manufacturing, robotics, process control, that sort of thing.

MH: Private industry more than government?

ER: We have done some government work, but the vast majority of our effort has been private industry. We have felt more comfortable there. The greatest part of our business is products.

MH: Are your clients in California or all over the country?

ER: They are all over the country. We have worked for aerospace companies, major manufacturing companies, major engineering firms. From the beginning, we have focused mostly on large companies, although a number of our very successful customers have been start-up companies using polyFORTH to program a clever widget they have designed.

One of the most distributed polyFORTH applications is an automobile engine analyzer that was developed by a company called Allen Test Products in Kalamazoo, Michigan. It is sort of an expert system. It was originally written before expert systems were popular and it lacks some of the formalization that people expect of expert systems, but it really was a rule-based system, with definite rules in it. It takes an automobile mechanic through a series of tests and modifies the tests that will be performed based on the results and on what it is learning. It will pursue various avenues and, at the end, it will print out a report of what is wrong with the car and what it recommends should be done. This was picked up by Firestone and is in all the Firestone Master Care Centers across the United States. It’s called the Master Mind of Firestone, and it’s also sold privately as something called a SmartScope. It’s gone all over the world. The operator-interaction screens have been translated into at least five languages.

MH: Do you ever wish it said, “Written in polyFORTH?”

ER: You bet I do.

MH: Were there any black moments in Forth’s early history?

ER: It was a real hand-to-mouth existence for a while. We knew very little about marketing and very little about things like advertising. It is still true that people are reluctant to base major products on a programming language they’ve never heard of, and not nearly enough of them have heard about Forth. It was very hard. I was the marketing department and Chuck was the programmer. I spent days on the phone calling up people saying, “Can we do some work for you, can we talk?” And we had days when we didn’t know where the next nickel was coming from. In more recent times, there have been a couple of major downturns. But we’ve managed to weather all that. I went back to school and got an MBA, and that has helped.

MH: What were some of your big hits?

ER: There have been a large number of those. Wonderful moments early on with Art, when we gave demonstrations where we had done something really spectacular in no time at all. We replaced a program that was running on a 370, and the Forth version ran about four times as fast and did some things that the 370 hadn’t been able to do at all. That was a lot of fun.

We’ve been the first serious software on a number of processors. Honeywell, in the late 70s, came out with a new minicomputer called the Level 6, and polyFORTH was the first operating system of any kind on it. It was the first high-level programming language on the Level 6. In order to achieve that, we worked for several weeks at Honeywell’s facility in Massachusetts. We had access to the computer from five at night until five in the morning. We did that for several weeks. At the end of that time, we had polyFORTH up on it, and Honeywell couldn’t care less. They were still busy designing their operating system.

We were the first high-level language to run on the 8086, by nearly two years.

MH: I heard once that Radio Shack was thinking of putting Forth into their first personal computer instead of BASIC. Is that true?

ER: We had some very serious conversations with Radio Shack. I don’t think they were looking at it as an alternative to BASIC, but they were looking at carrying it. They decided, after an intensive series of conversations, that there was not a market for it. Dick Miller certainly proved that wrong.
MH: Based on what people order from FORTH, Inc., what would you say is the most popular microprocessor today? Where is the excitement?

ER: It is completely dominated by the IBM. In our business, we also see a great deal of work on the 68000. It is used very heavily in the kinds of factory automation applications we work on.

MH: What other things do you think are significant in the development of FORTH, Inc., or Forth; other moves in our history that we haven’t really touched on as you look back at watershed events and critical decisions.

ER: Goodness. I think probably one of the big watersheds was when we decided to go after microprocessors in 1977. That was when microprocessors were newborn. In early 1977, we implemented a very primitive version of Forth on the RCA 1802, which was a brand-new chip at that time. I gave a paper on it, jointly with some people at RCA, at Electro ’77 in Boston. Seeing the interest in that and seeing what was clearly taking shape around 8080s and around Intel — which all had just been introduced at that time — we decided that was the wave of the future and we were going to go after it at FORTH, Inc.

On the basis of that, we brought out our first standard mail-order product, which was called microFORTH. Prior to that, all our systems were custom written and personally installed on minicomputers. With the advent of the microprocessor, we invested from our very slender capital — it was a very big risk for us at the time — and bought an Intel blue box development system in the MDS-800 then, and we invested a considerable amount of effort in bringing out our first standard mail-order product of microFORTH. We then implemented that on several microprocessors. microFORTH was a moderately successful product for us although, in retrospect, technically it had an awful lot of shortcomings. When we upgraded to polyFORTH less than two years later, it was a tremendous improvement. What I think was most significant about that was that we marketed it by selling, for as I recall about $15, a book called microFORTH Primer. We sold several thousand of those.

It was the people who picked up the microFORTH Primer but were unable to afford to buy microFORTH who formed the Forth Interest Group and all that activity. So that certainly has to be regarded as a watershed. It was a watershed for FORTH, Inc. because it first got us into the standard product business and standard product support. Without that, I don’t think we would have survived or thrived as we have as an organization. But it also had the side effect of stimulating the formation of the Forth Interest Group and that whole community.

MH: Is your product line now, I imagine, primarily microcomputers? Do you have much mainframe stuff at all?

ER: We have never had any real mainframe software in the sense of IBMs and the like. We do work on some moderately large computers. We have a version of polyFORTH that runs under VMS on VAXes, and we have a thirty-two-bit system that runs on fairly good-sized 68000-based systems.

MH: If you had it to do over, what would you do differently?

ER: There are some interesting things I can say on that score. I think the major thing I would do differently is to take a much more serious interest in public relations in general. We’ve sponsored a number of magazine articles over the years, but we have never really had anything like the kind of intensive, continued, sustained public relations effort that I think we should have done and that, I think, is required to make Forth well known. Some of the spectacular things we’ve done, such as being the first high-level language, the first real operating system on some of these processors, the world doesn’t know about.

The world, even today, does not know about some of the very, very successful Forth applications — not just polyFORTH, but Forth applications in general. People, to a great extent, still think that Forth is sort of a freak, a hobbyist/hacker toy, and that serious people don’t program in it. That is quite patently not the case. We at FORTH, Inc., have not done as good a job as we might have done, and the Forth community in general has not done as good a job as it should have done, at telling the story of just how successful companies have been using Forth.

MH: Have you seen polyFORTH adopted in universities?

ER: Oh, yes. We have a fairly aggressive university discount policy and have for a number of years, and some of them have been very successful with it. There is an excellent group at Stanford, a longstanding and active group at MIT, and literally hundreds of other universities from around the country that have picked it up.

MH: And that contributes to its use...

ER: It does, but not nearly enough. What we see is that in the universities this is happening in the engineering departments, where they really are not interested in Forth per se, or any computer language per se. They are interested in getting results out of experiments. Of course, Forth has always appealed to people who are focused on getting results. We don’t see it penetrating the computer science departments because they believe their major raison d’etre is to train conventional programmers to use conventional programming tools. And polyFORTH, or Forth in general, is not perceived in that way.

Michael Ham currently works in large-scale data processing, and expects to have a Forth program published shortly for the IBM microcomputer. His work has appeared frequently in these pages and in other respected magazines.
One of the biggest problems when trying to learn Forth, especially for naive beginners, is stack errors that crash the whole system. In a classroom environment, where students have only an hour or so to use a computer, the time spent rebooting can be significant. Add to that the frustration of not knowing why the system crashes, and you can get students who aren't very excited about Forth.

Laxen and Perry address part of the problem in their F83 system with the word \texttt{?ENOUGH}. This word checks to see that enough items are on the stack before execution, preventing accidents like listing screen zero, which always messes up the eighty-column card in my Apple IIe. Otherwise, the main problem seems to be stack overflow errors, which cause the Apple to hang irretrievably. To solve this problem for beginners, we need a way of checking the stack for correct output at the end of a word as well. It is considered good style to include a stack diagram with each word definition, so why not use these to automate stack error checking?

The word that does the job is \texttt{?S}. The beginning programmer can include a stack diagram after the name of a new word, and have the computer use it to figure out the number of inputs and outputs the word requires. At run time, if the expected number of values differs from the actual number, an error message is displayed. Stack errors are caught before they do too much damage, and the programmer has some idea of where and why the error occurred. The immediate feedback is helpful to beginning Forth programmers. The teacher just has to be sure the student

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**FORTH-83**

**RUN-TIME STACK ERROR CHECKING**

CHARLES SHATTUCK-ROSEVILLE, CALIFORNIA

---

```
Scr#  32.                          24Jun86 CWS
0 \ Run-time stack error-checking
1 2 VARIABLE CHECKING  CHECKING OFF
2 VARIABLE INPUTS  VARIABLE OUTPUTS  VARIABLE PARAMETERS
4 5: "INOUTS ( n1 n2 -- n3 ) OVER DEPTH 3 -- )
6: "ABORT" Not enough input parameters" -- DEPTH 1= SWAP -- 1
7: "OUTOUTS ( n ) DEPTH != = NOT
8: "ABORT" Wrong number of output parameters"

10 \ IN/OUT ( n n2 )
11: \ Compile ?INOUTS \ Compile )R  Checking ON : IMMEDIATE
12: \ Checking IF Compile IF Compile "OUTPUTS THEN
13: \ [Compile] : Checking OFF : IMMEDIATE
14
15
```

---

```
Scr#  33.                          24Jun86 CWS
0 \ Run-time stack error-checking
1 2 \PARAMETERS? ( n -- )
3 \ DOP ASCII ? = IF 1 PARAMETERS @ + \ TRUE ELSE
4 DOP ASCII n = IF 1 PARAMETERS @ + \ TRUE ELSE
5 DOP ASCII d = IF 2 PARAMETERS @ + \ TRUE ELSE
6 DOP ASCII = IF OUTPUTS PARAMETERS " TRUE ELSE
7 DOP ASCII ) = IF ( leave ) FALSE ELSE
8 TRUE ABORT" Stack piture error" THEN THEN THEN THEN
9 SWAP DROP ;
10 \ IS \ INOUTS OFF \ OUTOUTS OFF \ INPUTS PARAMETERS !
12 \ Begin IF word IF \ IF \ PARAMETERS? NOT UNTIL
13 \ INOUTS @ (COMPIL) \ LITERAL OUTPUTS @ (COMPIL) \ LITERAL
14 \ (COMPIL) \ IN/OUT ; IMMEDIATE
15
```

---

```
Scr#  34.                          25Jun86 CWS
0 \ Run-time stack error-checking, example
1 2 \ ADD \ (S n1 n2 -- n3 ) + 1
3 \ Works fine if at least two numbers are on the stack at entry.
4 5 \ BAD \ (S n1 n2 -- ) + 1
6 \ Gives run-time error because expected number of outputs are
7 \ not equal to actual number of outputs.
8 9 \ DADD \ (S d1 d2 -- d3 ) \ D+ 1
10 \ Expected inputs and outputs are double numbers.
11 \ EQUAL \ (S n1 n2 -- ) = 1
12 \ Expected output is a flag, but \OUTPUTS only checks the
13 \ number of items on the stack, not their values.
14
```

---

Forth Dimensions 32 Volume IX, Number 1
uses a stack diagram with each definition. The two words \texttt{INPUTS} and \texttt{OUTPUTS} are similar to \texttt{ENOUGH} and could be used explicitly. But the point of this is to save beginners from making too many errors. By the time many beginners would become comfortable using these additional words, the need for them would have passed. That's why we need \texttt{IN/OUT} to make compilation of the error checker more automatic. They could also be used alone, much the same as \texttt{ENOUGH} is used now, but that is still asking a lot from the beginner. In the accompanying code, you will notice that ; is redefined to compile \texttt{OUTPUTS} automatically. The input to \texttt{OUTPUTS} is held on the return stack at run time, to avoid interfering with data on the parameter stack. Note also that the right parenthesis ending a stack comment must be preceded by a blank, or there will be a "stack picture error."

The final step is to let the stack picture words themselves execute \texttt{IN/OUT}, compiling \texttt{INPUTS} and \texttt{OUTPUTS}. (\$ initializes the variables \texttt{IN-PUTS}, \texttt{OUTPUTS}, and \texttt{PARA-METERS} to zero, zero, and \texttt{INPUTS}, respectively. These variables keep tallies of the number of input and output parameters specified by the stack diagram. Then a loop is entered that reads the stack comment, adding the correct number of bytes to \texttt{INPUTS}, switching to \texttt{OUTPUTS} when a -- (double hyphen) is encountered, and tallying those until the ) is seen. As here, only n, d, ?, -, and ) are recognized by the stack checker. Any other initial character causes a compile-time error.

Please don't get me wrong: I'm not suggesting that standard Forth programs should include run-time stack checking. I do think it is a good, temporary crutch for people who are frustrated by frequent and mysterious Forth system crashes. It also promotes good style, by encouraging the programmer to use stack diagrams to declare the inputs and outputs for each word they define. The cost in memory and speed are too great for anything but educational purposes.

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A perpetual computing problem has been to come up with a perpetual calendar. The solution of W.C. Elmore from the American Journal of Physics is easy to program in fixed-point, double-precision Forth, the language automatically taking the required integer parts. But first, a little background to the calculation.

The kind of year you and I are most interested in is defined in terms of the seasons. Unfortunately, the lengths of the four seasons and the days are not evenly divisible. Julius Caesar defined the Julian calendar, with the astronomy of his time, to include a leap year every four years. By 1500 A.D., the calendar was ten days off between dates by taking the difference of Gregorian days. UM/MOD I have renamed UM/MMOD to show more consistently the beginning and ending results (UD1 U2 -- U3 UD4), where U3 is the remainder and UD4 is the quotient. Here is an example of program operation for New Year’s Day 1985:

85 1 1 GREGDAY D.
will print 725010
1985 1 1 GREGDAY D.
will print 725010
725010, WEEKDAY will print 2, for Tuesday.

I encourage the reader to take this primitive further, and to write an actual calendar.

Allen Anway is a member of the American Association of Physics Teachers, and a computer coordinator for the University of Wisconsin at Superior. His eldest daughter is a third-generation physicist.

---

SCR # 77
0 ( # 077 ) (perpet calendar primitive)
1 FORTH-B3
2 ( year/month/day --- UD )
3 : GREGDAY
4 ROT (mdy) (short year correct)
5 DIP 100 IF 1900 THEN
6 ROT (d/ym) (special, prev year)
7 DUP 3 IF 12 + SWAP
8 1- SWAP THEN
9 2- 3059 100 UD/ ( month calculation)
10 ROT (y/ym) (day calculation)
11 32 + (yU) 10 (yU) ROT (Ud/y)
12 DIP 100 / NERATE SWAP (Ud/y)
13 DIP 400 / ROT + S:+D ROT (Ud/d)
14 36525 UD + 100 UD/MMOD (UD/Um/Mod/UD)
15 ROT DROP D= D+ 1;
16 ( UD -- mod ) (gregorian_day -- 0,6)
17 : WEEKDAY 3 0 D+ 7 UD/MMOD 2DROP 1
18 (UxWeekday, 1=Monday, ..., 6=Saturday)
19 15
20 21
23 (Roy issue) adapted by Allen Anway

---

For a perpetual calendar, we define a unique day number for each day, consecutive calendar days having consecutive day numbers. Elmore has calculated the following:

Julian Day N = Int[365.25y] + Int[30.59(m-2)] + d + 30

Gregorian Day N = Int[365.25y] + Int[y/400] - Int[y/100] + Int[30.59(m-2)] + d + 32

assuming that January is month 13 of the previous year, and that February is month 14 of the previous year.

These two formulas give consecutive Julian days across Pope Gregory’s calendar gap. The 0.59 (month) fraction is any value evenly: leap year evenly: not leap year...
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Like many Forth metacompilers, this system allows the generation of headless Forth code. Headers are used by the Forth outer interpreter when searching the dictionary. Once a word is compiled into a new word in the Forth dictionary, only its CFA (code field) and its PFA (parameter field) are required for its execution. Its “head” — the LFA (link field address) and NFA (name field address) — are not used except for future compilations or for direct execution when encountered by INTERPRET in the terminal input buffer or when loading a Forth screen.

For words that may only be needed a few times in the definition of higher-level words, there is no reason to have their heads (link and name fields) clutter up the dictionary and take up valuable space. On the other hand, it may require more space to define a new set of words to “behead” them than would be gained by stripping them of name and link fields.

The ideal headless compiler could be loaded into any Forth system, could compile code with or without headers, then could be forgotten when it is no longer needed.

This metacompiler does all these functions. It uses standard Forth-79 syntax, and has been implemented for two different types of Forth dictionary structures: the Forth PADS system and the MVP-FORTH system for CP/M. With little or no modifications, it can be transported to any Forth-79 system.

The above two Forth systems differ in two ways that must be accommodated by the headless compiler. First, the LFAs of the two are in different places. In the PADS version, the LFA precedes the NFA to speed up dictionary search times. Also in the PADS version, words below a certain point in the dictionary are searched before the rest of the dictionary. In the PADS version, virtual files are used in high memory and can cause serious system crashes if high memory is used in a scheme like this without paying extra-careful attention to the way virtual utilities and development aids use the same memory areas.

The headless compiler resides in high memory. It consists of three main sections:

1. The code for the new compiling words.
2. Space for storing the link and name field address that must be “pruned.”
3. The headers for the headless words.

The use of the system is straightforward, and shouldn’t cause any unusual side effects as long as a few things are remembered:

1. There are two areas of memory being used, both growing upward. Caution must be maintained with large systems so that the main dictionary does not creep up into the headless compiler.

2. The system must be pruned and unpatched before it can be saved. There cannot be any links to words in high memory beyond HERE.

3. The system cannot be simply stripped back by FORGET before pruning without careful consideration.

4. The defining words CREATE and DOES> do not function with the headless compiler and should be avoided or redefined to accommodate their special compiling functions.

5. A definition cannot be made that uses [COMPILE] to compile any of the new defining words (e.g., colon, tick, constant, variable, etc.). If these are required, they must be re-defined as a different word or patched into the words they are compiled in (like forward references in a metacompiler) at the end of compilation.

Most Forth metacompilers require that the entire dictionary be recompiled to get the advantage of a system with both heads and headerless words. If a system is to retain the basic Forth dictionary and its compiling words for the user, then very few, if any, of the low-level words will be removed from the dictionary. Only words that are to be taken out of the application for more efficient use of space and for security of the application code need to be stripped of their headers. This doesn’t have to involve a new metacompilation of the source code. In fact, the headless compiler can be invoked a number of times in the generation of the application. At each stage, a usable, extensible Forth system exists. The headless compiler might be used in a modular fashion, as subsystems are developed. These subsystems may be referenced later by a single word. The rest of the words in the subsystem probably won’t be used by any other subsystem and can be stripped of their heads once they are compiled. A simple tool like a
fancy VL1ST can list the CFAs of all headless words before they are pruned; if they are required in the future for some purpose, they can be executed and compiled even without their heads.

Compilation Steps
1. Set initial himem so there is enough room for it to grow without crashing into the disk buffers or other Forth stuff kept at the very top of memory, but far enough above the main system so words defined in the main dictionary won't creep up into it.
2. Load the headerless compiler screens.
3. Load your application code, editing the words HEADS-ON and HEADS-OFF in the screen files to enter the compilation mode you desire. Normally, heads-on words are words that will be needed after this compilation process, either directly by the user in the application, or indirectly by words that will use them after the headless compiler has been pruned. The first and last word in this application should have their heads on unless you are very careful in pruning and unpatching. Also, don't recklessly FORGET the last word with heads above the headless words — this can mess up Forth. Usually, the word MEND is included as the last word of the metacompiler, just to avoid this situation.
4. When the application is loaded, type HL1ST to get a printout of all the headless words and their CFAs, in case you need them later.
5. Type PRUNE to unhook all the heads of the headless words.
6. Type UNPATCH to unhook the new INTERPRET and re-establish the normal dictionary search order.
7. Enter ' META LFA 'MEND LFA ! to cut away the headless compiler. META is the last word defined before the headless compiler was loaded. MEND is the last word in the metacompiler and has a regular head.
8. Type FREEZE COLD and then save the system.

Limitations
DOES> will not work. A new version of DOES> must be generated in
the metacompiler, if it is required.

FORGET will not forget gracefully through the headless words. Use with caution.

' (tick) will work in the compiling and execution modes, but it cannot be compiled in definitions like:

:TEST ... [COMPILE]' ...

This is generally true of all new defining words, since the CFA of the new word would be compiled into the definition. This would crash the system after the heads are wiped out.

CODE is not implemented for headless words.

CP/M on IBM with Baby Blue

Since the memory space available for CP/M (Baby Blue CP/M board on the IBM PC) is greater than for normal CP/M systems, this extra memory can be used by the metacompiler. The metacompiler can be compiled in a lower part of the Forth system, then saved as a virtual file, reloaded when needed, and stripped away after headless compilation.

The following steps are required:

1. Set the new DPHEAD to LIMIT, and disable the stack check word ?STACK with

   ' EXIT CFA ' ?STACK !

   Then compile the metacompiler as normal.

2. Save the metacompiler as a virtual file by figuring out how many 1K blocks of memory it uses (usually between one and two) and save it to some unused Forth screens with:

   HEX LIMIT N BLOCK 400 CMOVE
   LIMIT 400 + N 1+
   BLOCK400 CMOVE
   (where N is the first screen)

3. Then do the normal PRUNE, UNPATCH, and ' META NFA ' MEND LPA ! to unpatch it.

4. Restore ?STACK with ' SP@ CFA ' ?STACK !

5. Now the virtual file can be moved into memory, used, and then unpatched.

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**SCR #3**
```
0 ( HEADLESS COMPILER -3 IBM )
1 VARIABLE TWIG-PTR ; ( ptr at current twig)
2 CREATE TWIGS 200 ALLOT ; ( table of twigs to be traced)
3: +TWIG TWIG-PTR @ DUP LFA ) : REDR3 ; ( get current twig addr)
4 IF "1" "twig ref blk ev" ABORT THEN TWIGS + UPDATE ;
5: CLR-TWIG TWIGS 100 OR FILL ; CLR-TWIG ( clear table)
6: TWIG-TBL (ADD ) TWIG-PTR + ! ; ( store current twig)
7: SETHEAD HERE DBODY ; DBHEAD @ DP ;
8: SETHEAD HERE DBODY @ DP @
9: HEADS unwind "HEADD?" H/B @ IF H/B
t10: HEADS-UPF "HEADB?" H/B @ IF 0 H/B!
L.
```

**SCR #4**
```
0 ( HEADLESS COMPILER -4 IBM )
1 UNPATCH ":INTERPRET-2" ( cfa ) ":INTERPRET-2" ;
2 "FORTH" "FIND 10" ; CLR-TWIG ( FKEY ) ":TWIG-PTR " ;
3 ( restore to normal INTERPRET, set PADS dict search order)
4 PRUNE "TWIG-PTR " ; ( Cut away heads)
5 BEGIN "TWIG-2-" ( get nfa of last regular head)
6 +TWIG-2- "LFA" ( get LFA of next regular head)
7 DUP @ TOP @ UK ABORT "twig res error" ; ( err chk)
8 "alva of WARNING" IF DP DUM CONT @ @ < FIND
t9 THEN DUP CP ; "handle,not unique " THEN
10 THEN DUM DR; "MIN 1" ; ALLOT DUP 0 TOP TEGGLE HERE 1-
11 TOP DUM DBODY , CURRENT @ SETHEAD
12 L.1 SNAP -- > IN 1 ; ( move input stream around)
```

**SCR #5**
```
0 ( HEADLESS COMPILER -5 IBM )
1 CREATE-HEAD "IN" @ SETHEAD FIRST 190 = HERE ;
2 IF CR ? EM "" close to buffer" CR
3 THEN LATEST :LW DUP 1+ @ 0 =
4 IF 0 DP @ + IF ELSE
5 THEN ABORT "null?" DUP CONTEXT @ @ 1 << FIND
6 THEN DUM DR 12 X pop INTO LFA @ NEXT HEAD
7 IN @ SNAP -- > IN 1 ; ( move input stream around head)
```
without recompiling every time.

6. One additional feature is added in this version. The NFA of the last word in the metacompiler must be saved so that the LFA of MEND can be patched in when it is reloaded. The word META allows a couple of extra bytes to save this address.

Here is a list of newly defined words and their functions in the headless compiler:

**HEADS-ON** (--) 
Starts the normal compilation of words into the Forth dictionary.

**HEADS-OFF** (--) 
Starts the compilation of headerless words into the Forth dictionary.

**CREATE-HEAD** (--) 
Creates a head in high memory for a word which will be headless in the main dictionary.

**VARIABLE** (--) 
Creates a variable without a head if HEADS-OFF has been previously set. Otherwise, creates a normal variable.

**CONSTANT** (--) 
Creates a constant without a head if HEADS-OFF has been previously set. Otherwise, creates a normal constant.

: 
(--) 
Creates a colon-defined word without a head if HEADS-OFF has been previously set. Otherwise, creates a normal colon-defined word.

**CREATE** (--) 
Creates a word without a head if HEADS-OFF has been previously set. Otherwise, creates a normal word.

; 
(--) pfa 
Gets the PFA of the following word in the input stream. Does CFA @ 2+ if the word is in himem.

**DPHEAD** (--) 
Variable containing current position of DP in himem.

**DPBODY** (--) 
Variable containing current position of DP.
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Variable containing current position of DP in main dictionary.

TIC (-- pfa) Gets PFA of a word with a normal head. This is the old ' and is used mainly for patching <INTERPRET.

<INTERPRET Interprets or compiles words, depending on STATE. If the word is found to have a CFA at an address above a certain point, it is a headerless word and its CFA is gotten with CFA @.

TWIGS (-- adr) An array in memory that records each time HEADS-ON or HEADS-OFF is switched. DP and LATEST at that point in time are stored in the array, and later used to behead the headless words.

TWIG-PTR (-- adr) A variable containing the next position in TWIGS to be used.

HLIST A routine to list all headless words in the dictionary (before pruning, of course) along with their CFAs. The CFAs can be used later for compilation or execution.

PRUNE A routine that strips away all the heads in high memory and resets the links in the main dictionary, so headerless words are not seen by FIND.

UNPATCH A routine to reset the normal INTERPRET and, in the PADS system, to reestablish the previous dictionary search order.

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MVP-FORTH Professional Applications Development System (PADS), Tom Wempe. FORTHKIT, 240 Prince Street, Los Gatos, CA 95030.

META FORTH™ A Metacompiler for FIG-FORTH, John J. Cassady. 11 Mira Monte Road, Orinda, CA 94563.


All About Forth, 2nd ed., Glen B. Haydon. Mountain View Press, Inc., P.O. Box 4656, Mountain View, CA 94040.


MetaFORTH for the 6502, Darrel Johansen. 1983, San Francisco, CA.

Thanks to Serge Modular Music Systems and to Orion Instruments for time, encouragement, and an application to use this project on.

Darrel Johansen has been programming in Forth for ten years, including projects in electronic music synthesizer control. He is currently senior engineer at Orion Instruments, Inc.
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  Contact Robert Washam
  91 Weis Dr.
  Ellisville, MO 63011

• NEW JERSEY
  New Jersey Chapter
  Rutgers Univ., Piscataway
  Nicholas Lordi (201) 338-9363

• NEW MEXICO
  Albuquerque Chapter
  1st Thurs., 7:30 p.m.
  Physics & Astronomy Bldg.
  Univ. of New Mexico
  Jon Bryan (505) 298-3292

• NEW YORK
  FIG, New York
  2nd Wed., 7:45 p.m.
  Manhattan
  Ron Martinez (212) 866-1157
  Rochester Chapter
  4th Sat., 1 p.m.
  Monroe Comm. College
  Bldg. 7, Rm. 102
  Frank Lanzafame (716) 235-0168
  Syracuse Chapter
  3rd Wed., 7 p.m.
  Henry J. Fay (315) 446-4600

• NORTH CAROLINA
  Raleigh Chapter
  Frank Bridges (919) 552-1357

• OHIO
  Akron Chapter
  3rd Mon., 7 p.m.
  McDowell Library
  Thomas Franks (216) 336-3167
  Athens Chapter
  Israel Urieli (614) 594-3731
  Cleveland Chapter
  4th Tues., 7 p.m.
  Chagrin Falls Library
  Gary Bergstrom (216) 247-2492
  Dayton Chapter
  2nd Tues. & 4th Wed., 6:30 p.m.
  CFC, 11 W. Monument Ave., #612
  Gary Granger (513) 257-6984

• OKLAHOMA
  Central Oklahoma Chapter
  3rd Wed., 7:30 p.m.
  Health Tech. Bldg., OSU Tech.
  Contact Larry Somers
  2410 N.W. 49th
  Oklahoma City, OK 73112

• OREGON
  Greater Oregon Chapter
  Beaverton
PENNSYLVANIA
Philadelphia Chapter
4th Tues., 7 p.m.
Drexel University, Stratton Hall
Melanie Hoag (215) 895-2628

TENNESSEE
East Tennessee Chapter
Oak Ridge
2nd Tues., 7:30 p.m.
800 Oak Ridge Turnpike,
Richard Secrist (615) 483-7242

TEXAS
Austin Chapter
Contact Matt Lawrence
P.O. Box 180409
Austin, TX 78718
Dallas/Ft. Worth
Metroplex Chapter
4th Thurs., 7 p.m.
Chuck Darrett (214) 245-1064
Houston Chapter
1st Mon., 7 p.m.
Univ. of St. Thomas
Russel Harris (713) 461-1618
Periman Basin Chapter
Odessa
Carl Bryson (915) 337-8994

UTAH
North Orem FIG Chapter
Contact Ron Tanner
748 N. 1340 W.
Orem, UT 84057

VERMONT
Vermont Chapter
Vergennes
3rd Mon., 7:30 p.m.
Vergennes Union High School
Rm. 210, Mokton Rd.
Don VanSypeel (802) 588-6698

VIRGINIA
First Forth of Hampton Roads
William Edmonds (804) 898-4099
Potomac Chapter
Arlington
2nd Tues., 7 p.m.
Lee Center
Lee Highway at Lexington St.
Joel Shippens (703) 860-9260
Richmond Forth Group
2nd Wed., 7 p.m.
154 Business School
Univ. of Richmond
Donald A. Full (804) 739-3623

WISCONSIN
Lake Superior FIG Chapter
2nd Fri., 7:30 p.m.
Main 195, UW-Superior
Allen Anway (715) 394-8360
MAD Apple Chapter
Contact Bill Horton
502 Atlas Ave.
Madison, WI 53714
Milwaukee Area Chapter
Donald Kimes (414) 377-5070

INTERNATIONAL

AUSTRALIA
Melbourne Chapter
1st Fri., 8 p.m.
Contact Lance Collins
65 Martin Road
Glen Iris, Victoria 3146
03/29-2600
Sydney Chapter
2nd Fri., 7 p.m.
John Goodsell Bldg., Rm. LG19
Univ. of New South Wales
Contact Peter Tregear
10 Binda Rd., Yowie Bay
02/524-7490

BELGIUM
Belgium Chapter
4th Wed., 20h00
Contact Luk Van Loock
Larikdreff 20
2120 Schoten
03/658-6343
Southern Belgium Chapter
Contact Jean-Marc Bertinchamps
Rue N. Monnom, 2
B-6290 Nalinnes
071/213858

CANADA
Northern Alberta Chapter
4th Sat., 1 p.m.
N. Alta. Inst. of Tech.
Tony Van Muyden (403) 962-2203
Nova Scotia Chapter
Halifax
Howard Harawitz (902) 477-3665
Southern Ontario Chapter
Quarterly, 1st Sat., 2 p.m.
Genl. Sci. Bldg., Rm. 212
McMaster University
Dr. N. Sointseff (416) 525-9140
ext. 3
Toronto Chapter
Contact John Clark Smith
P.O. Box 230, Station H
Toronto, ON M4C 3J2
Vancouver Chapter
Don Vanderweeke (604) 941-4073

COLOMBIA
Colombia Chapter
Contact Luis Javier Parra B.
Aptdo. Aereo 100394
Bogota 214-0345

DENMARK
Forth Interesse Grupe
Denmark
Copenhagen
Erik Oestergaard, 1-520494

ENGLAND
Forth Interest Group- U.K.
London
1st Thurs., 7 p.m.
Polytechnic of South Bank
Rm. 406
Borough Rd.
Contact D.J. Neale
58 Woodland Way
Morden, Surry SM4 4DS

FRANCE
French Language Chapter
Contact Jean-Daniel Dodin
77 Rue du Cagire
31100 Toulouse
(16-61)44.03.06
FIG des Alpes Chapter
Annely
Georges Seibol, 50 57 0280

GERMANY
Hamburg FIG Chapter
4th Sat., 1500h
Contact Horst-Gunter Lymes Schanzstraasse 27
2000 Hamburg 6

HOLLAND
Holland Chapter
Contact Adriaan van Roosmalen
Heusden Houtestraat 134
4817 We Brda
31 76 713104

IRELAND
Irish Chapter
Contact Hugh Dobbs
Newton School
Waterford
051/75757 or 051/74124

ITALY
FIG Italia
Contact Marco Tausel
Via Gerolamo Forini 48
20161 Milano
02/433249

JAPAN
Japan Chapter
Contact Toshi Inoue
Dept. of Mineral Dev. Eng.
University of Tokyo
7-3-1 Hongo, Bunkyo-ku
812-2111 ext. 7073

NORWAY
Bergen Chapter
Kjell Birger Faaraas, 47-518-7784

REPUBLIC OF CHINA
(R.O.C.)
Contact Ching-Tang Tzeng
P.O. Box 28
Lung-Tan, Taiwan 325

SPECIAL GROUPS

Apple Corps Forth Users
Contact Hugh Dodds
Newton School
Waterford
051/75757 or 051/74124

Figgraph
Howard Pearlmutter
(408) 425-8700

NC4000 Users Group
John Carpenter (415) 960-1256
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