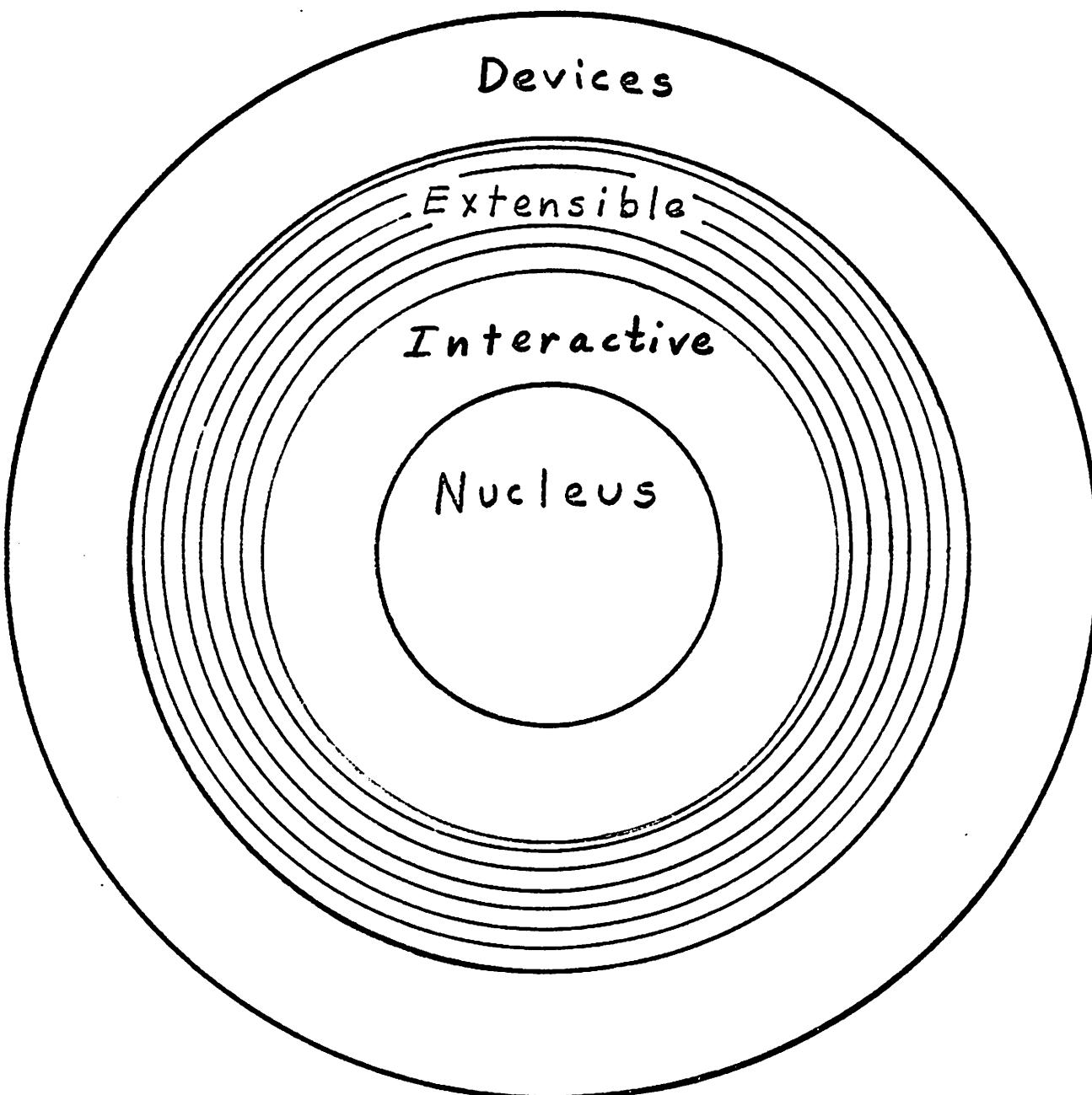


DEFINING WORDS

(or how to write a compiler in 25 words or less)

Application

Layers



USING DEFINING WORDS

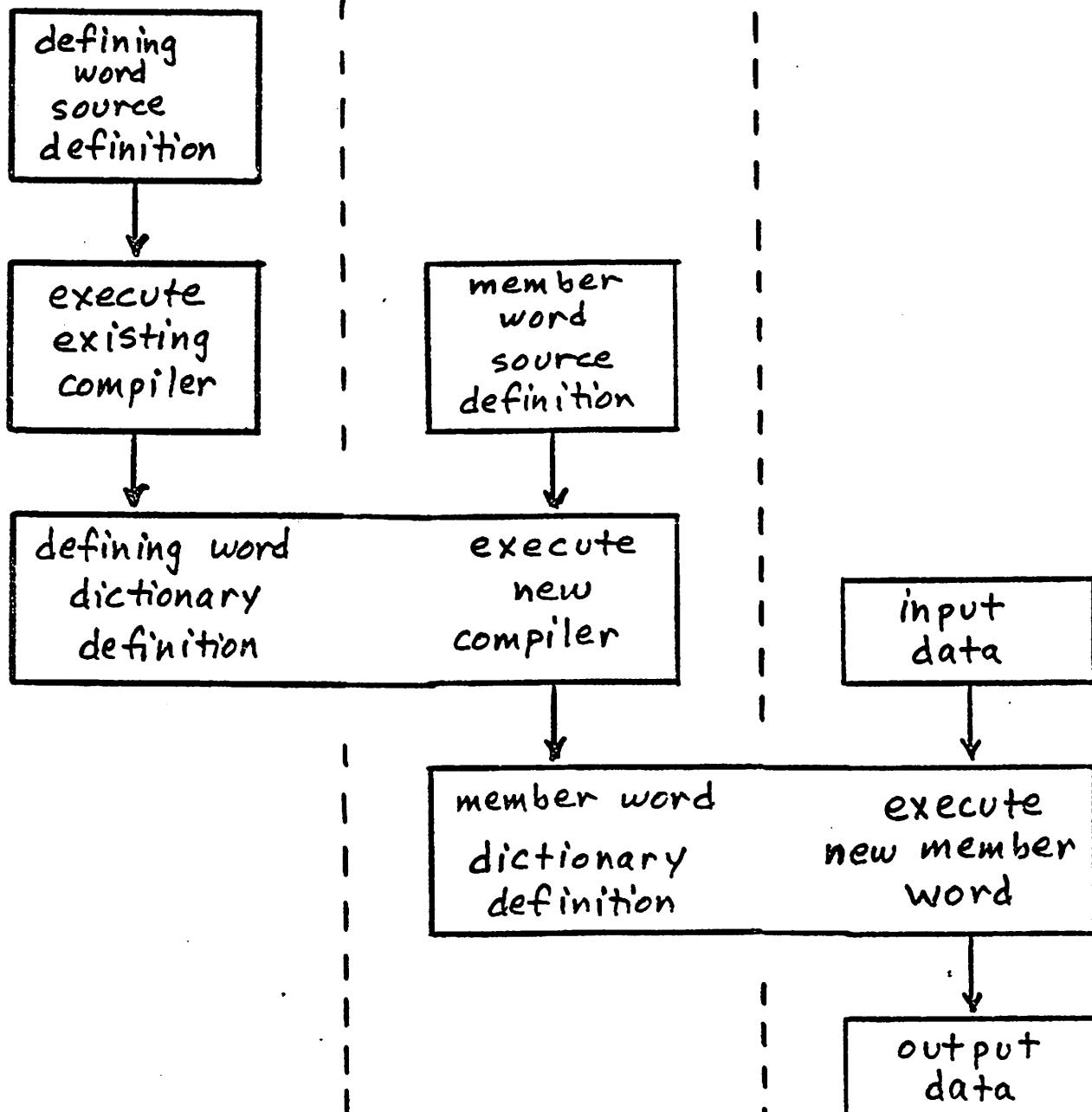
Time

Sequence:

1

2

3



Compile a new defining word.

Execute the new defining word;
Compile a new member word.

Execute the new member word.

DEFINING WORDS

are FORTH definitions which, when executed, create entire new definitions in the dictionary.

Predefined defining words:

:

CONSTANT
VARIABLE
USER
VOCABULARY
CODE

It is possible to create new defining words (ie, specialized compilers) which can subsequently be used to create a new family of member words.

Defining words are useful for creating data structures and procedures

which share a common execution-time behaviour.

Proper use can substantially reduce software development time, reduce program size, and improve readability with no execution-time penalty.

To define a new defining word,
an existing defining word (eg, :) is used.
This occurs at sequence ①.

The definition specifies the
compile-time activity ②
and the
execution-time activity ③
of each member of the family.

The form of a new defining word's definition is

• new-defining-word

② <BUILDS compile-time words

③ DOES> execution-time words
or
;CODE assembly language

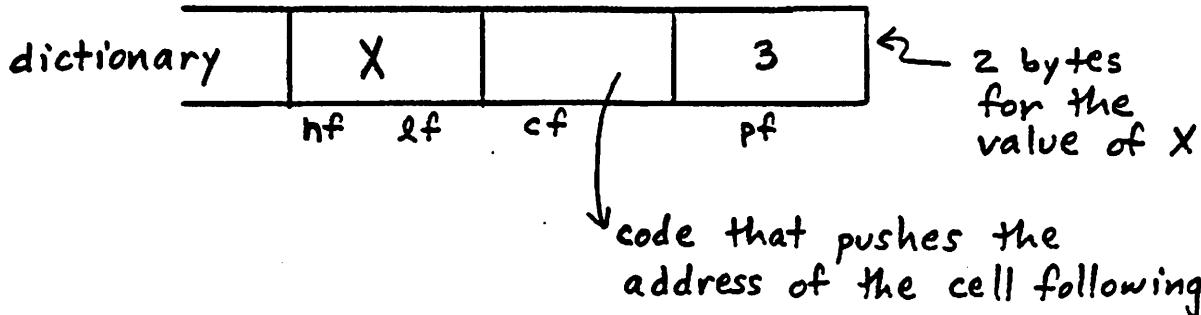
;

Example of a high-level definition of a defining word: VARIABLE

(2)

3 VARIABLE X

member creation-time



(3)

3 X !

X ? CR 3 ok

member execution-time

(1)

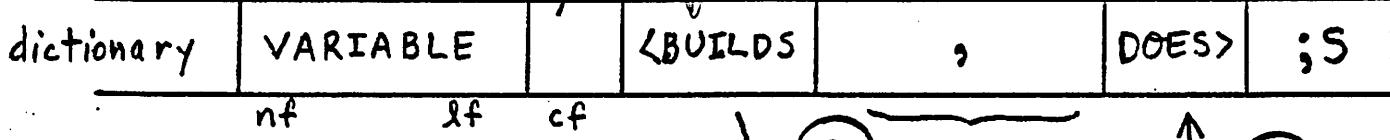
: VARIABLE

defining word creation-time

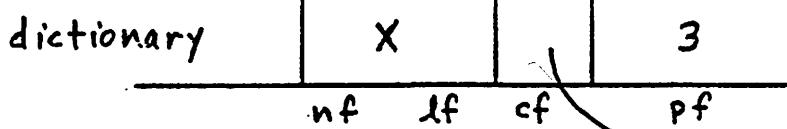
<BUILDS ,

DOES>

; ; CODE for speed



~ (2)
result



: CONSTANT <BUILDS , DOES> @;

"mysterious
GREEN ARROW!" see yellow
nodes (142)

"logical" drawing
- not physical
printers

Create a 1 Dimensional byte array

Determine the compile-time action of member words:

2
use defining word

5 CVECTOR LINE

#bytes

array name

new defining word

Determine the execution-time action of member words:

3
use member word

3 LINE C@

subscript

returns address of selected byte

Define the new defining word:

no. elts ---

: CVECTOR

1
create defining word

<BUILDS ALLOT DOES> + ;

run:

dictionary	CVECTOR		<BUILDS	ALLOT	DOES>	+	;
	nf	lf	cf				

2
result

dictionary	LINE		5 bytes
	nf	lf	cf

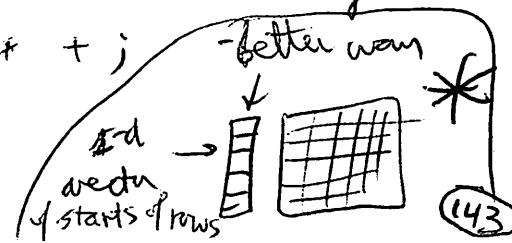
2

3

2-d vector array
-FORTRAN etc
use * & + to
index into 1-d
array

2 byte array:

: VECTOR <BUILDS 2* ALLOT DOES> SWAP 2+ + ;



Examples of using LINE:

: FILL-LINE 5 0 DO 65 I + I LINE C!
LOOP ;

: PRINT-LINE 5 0 DO I LINE C@ EMIT
SPACE LOOP ;

FILL-LINE (CR) ok

PRINT-LINE (CR) A B C D E ok

Variations on CVECTOR:

Subscripts starting from 1 (instead of 0)

: CVECTOR <BUILDS ALLOT
DOES> + 1- ;

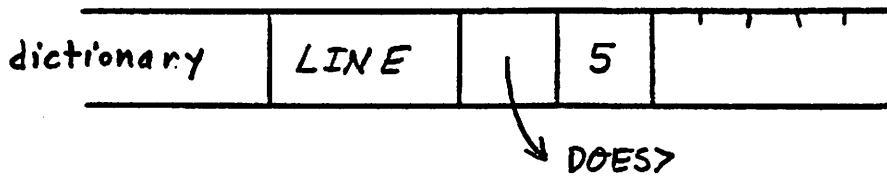
Initialize member arrays to blanks when
they are created

: BLANK&ALLOT (#bytes ---)
HERE OVER BLANKS
ALLOT ;

: CVECTOR <BUILDS BLANK&ALLOT
DOES> + ;

Check subscript range on each reference
to all member words

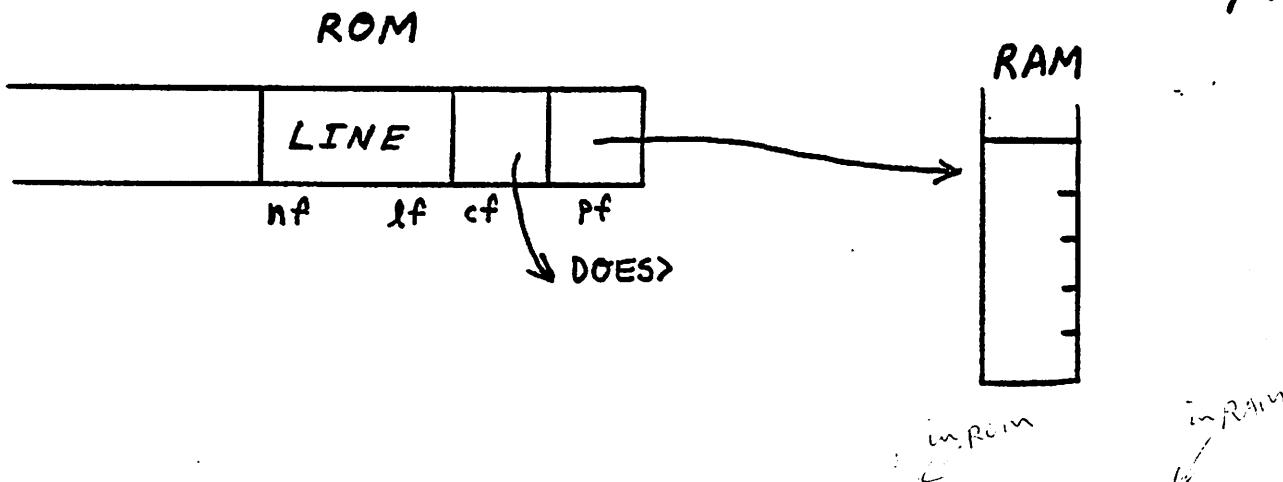
Must store array size in member's definition



```
: CVECTOR <BUILDS DUP , ALLOT
          OVER OVER
DOES> 2DUP @ UK
IF      + 2+   checks for negative subscripts
ELSE    @ . .
        ." Range error" ABORT
THEN   ;
```

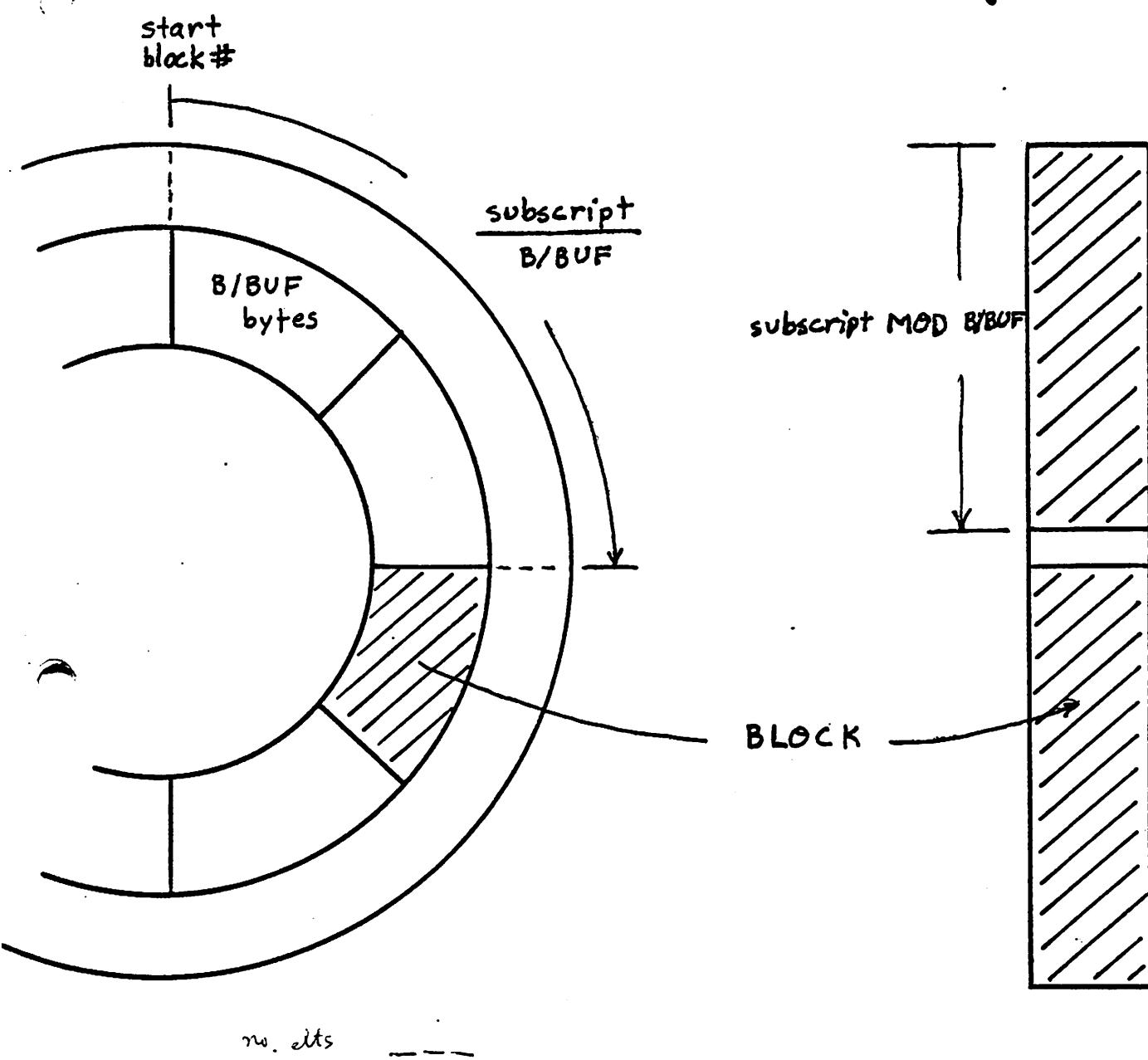
checks for signed values

Definition in ROM, data in RAM (writable memory)



```
: CVECTOR <BUILDS THERE , ALLOT
DOES> @ + ;
```

Virtual array: definition in the dictionary,
data on mass storage



: CVECTOR <BUILDS start-block# , DROP

DOES > @ SWAP

B/BUF /MOD ROT +

BLOCK + UPDATE ;

Defining word example: CASE: execution vector

Define some cases

: OPET ." DOG " ; : 1PET ." CAT " ;

: 2PET ." RAT " ; : 3PET ." SNAKE " ;

Using defining word

(2) CASE: ANIMAL OPET 1PET 2PET 3PET ;

source definition

Creating defining word

: CASE: <BUILDS] SMUDGE

(1) DOES> SWAP 2* + @

EXECUTE ;

dictionary	CASE:	run:	<BUILDS]	SMUDGE	DOES>	SWAP	...
	nf	lf	cf					

2	result	dictionary	ANIMAL		OPET	1PET	2PET	3PET	;5
			nf	lf	cf				

Using member word

- (3) 0 ANIMAL CR DOG ok
 1 ANIMAL CR CAT ok
 3 ANIMAL CR SNAKE ok

← P.g. Kim used this for a random phrase generator

SOFTAPE :

;" is <BUILDS , DOES>